# SPEC® CINT2006 Result

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## Lenovo Group Limited

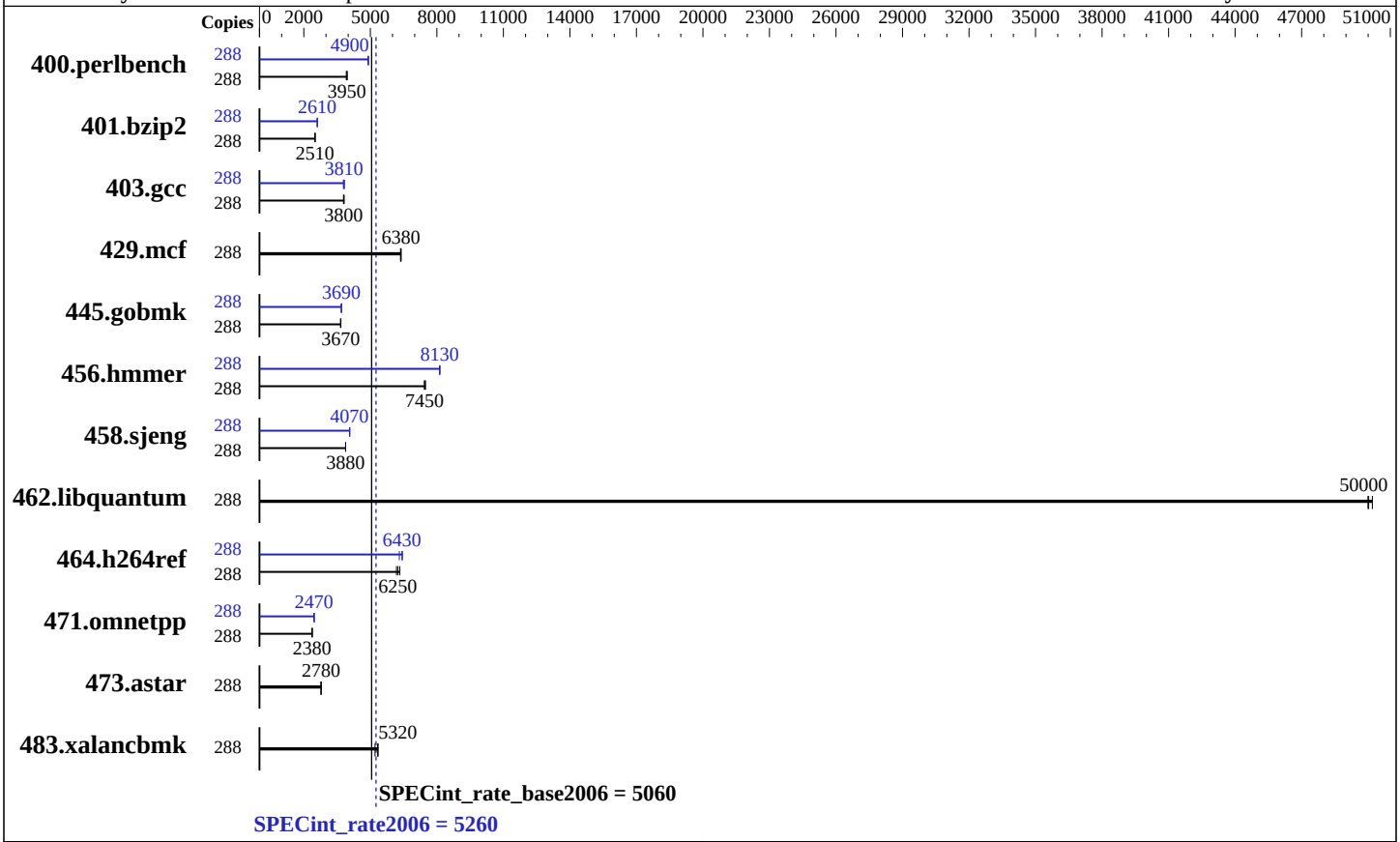
Lenovo System x3950 X6
(Intel Xeon E7-8870 v3, 2.10 GHz)

SPECint®_rate2006 = 5260

SPECint_rate_base2006 = 5060

**CPU2006 license:** 9017
**Test sponsor:** Lenovo Group Limited
**Tested by:** Lenovo Group Limited

**Test date:** Oct-2015
**Hardware Availability:** Jul-2015
**Software Availability:** Oct-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7-8870 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.90 GHz |
| CPU MHz: | 2100 |
| FPU: | Integrated |
| CPU(s) enabled: | 144 cores, 8 chips, 18 cores/chip, 2 threads/core |
| CPU(s) orderable: | 4, 6, 8 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 45 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz) |
| Disk Subsystem: | 1 x 400 GB SSD |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 (x86_64) 3.12.28-4-default |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 288 | 712 | 3950 | **712** | **3950** | 719 | 3910 | 288 | **574** | **4900** | 574 | 4900 | 572 | 4920 |
| 401.bzip2 | 288 | 1113 | 2500 | **1108** | **2510** | 1107 | 2510 | 288 | 1066 | 2610 | 1066 | 2610 | **1066** | **2610** |
| 403.gcc | 288 | 612 | 3790 | **609** | **3800** | 607 | 3820 | 288 | 612 | 3790 | **609** | **3810** | 605 | 3830 |
| 429.mcf | 288 | **412** | **6380** | 413 | 6360 | 411 | 6390 | 288 | **412** | **6380** | 413 | 6360 | 411 | 6390 |
| 445.gobmk | 288 | 823 | 3670 | **824** | **3670** | 827 | 3650 | 288 | 817 | 3700 | 819 | 3690 | **818** | **3690** |
| 456.hmmer | 288 | 361 | 7430 | **361** | **7450** | 359 | 7490 | 288 | **331** | **8130** | 330 | 8140 | 331 | 8120 |
| 458.sjeng | 288 | **897** | **3880** | 897 | 3880 | 897 | 3890 | 288 | **857** | **4070** | 857 | 4070 | 856 | 4070 |
| 462.libquantum | 288 | **119** | **50000** | 119 | 50000 | 119 | 50200 | 288 | **119** | **50000** | 119 | 50000 | 119 | 50200 |
| 464.h264ref | 288 | 1032 | 6180 | **1020** | **6250** | 1007 | 6330 | 288 | 989 | 6450 | 1011 | 6310 | **992** | **6430** |
| 471.omnetpp | 288 | **758** | **2380** | 758 | 2370 | 757 | 2380 | 288 | 734 | 2450 | **729** | **2470** | 728 | 2470 |
| 473.astar | 288 | 728 | 2780 | 727 | 2780 | **728** | **2780** | 288 | 728 | 2780 | 727 | 2780 | **728** | **2780** |
| 483.xalancbmk | 288 | 371 | 5350 | **373** | **5320** | 381 | 5210 | 288 | 371 | 5350 | **373** | **5320** | 381 | 5210 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
Operating Mode set to Custom in BIOS
Cstates disabled in BIOS
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-lea3 Fri Oct 23 02:08:29 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-8870 v3 @ 2.10GHz
      8 "physical id"s (chips)
      288 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
```

Continued on next page

## Platform Notes (Continued)

```
        cpu cores : 18
        siblings  : 36
        physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 4: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 5: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 6: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 7: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
     cache size : 46080 KB

  From /proc/meminfo
     MemTotal:       2117726352 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     SUSE Linux Enterprise Server 12

  From /etc/*release* /etc/*version*
     SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 0
        # This file is deprecated and will be removed in a future service pack or
        release.
        # Please check /etc/os-release for details about this release.
     os-release:
        NAME="SLES"
        VERSION="12"
        VERSION_ID="12"
        PRETTY_NAME="SUSE Linux Enterprise Server 12"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12"

  uname -a:
     Linux linux-lea3 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
     (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Oct 23 02:07 last=5

  SPEC is set to: /cpu2006.1.2
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda3      xfs   371G  8.1G  363G   3% /
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.
```

Continued on next page

## Platform Notes (Continued)

```
    BIOS IBM -[A9E125JUS-2.00]- 06/18/2015
    Memory:
     126x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1600 MHz
     2x Hynix HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1600 MHz
     64x NO DIMM Unknown

  (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:
```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-auto-ilp32`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

Continued on next page

# Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-CC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-CC.xml

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