



SPEC[®] CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10
(2.40 GHz, Intel Xeon Gold 6148)

SPECfp[®]_rate2006 = Not Run

SPECfp_rate_base2006 = 1390

CPU2006 license: 3

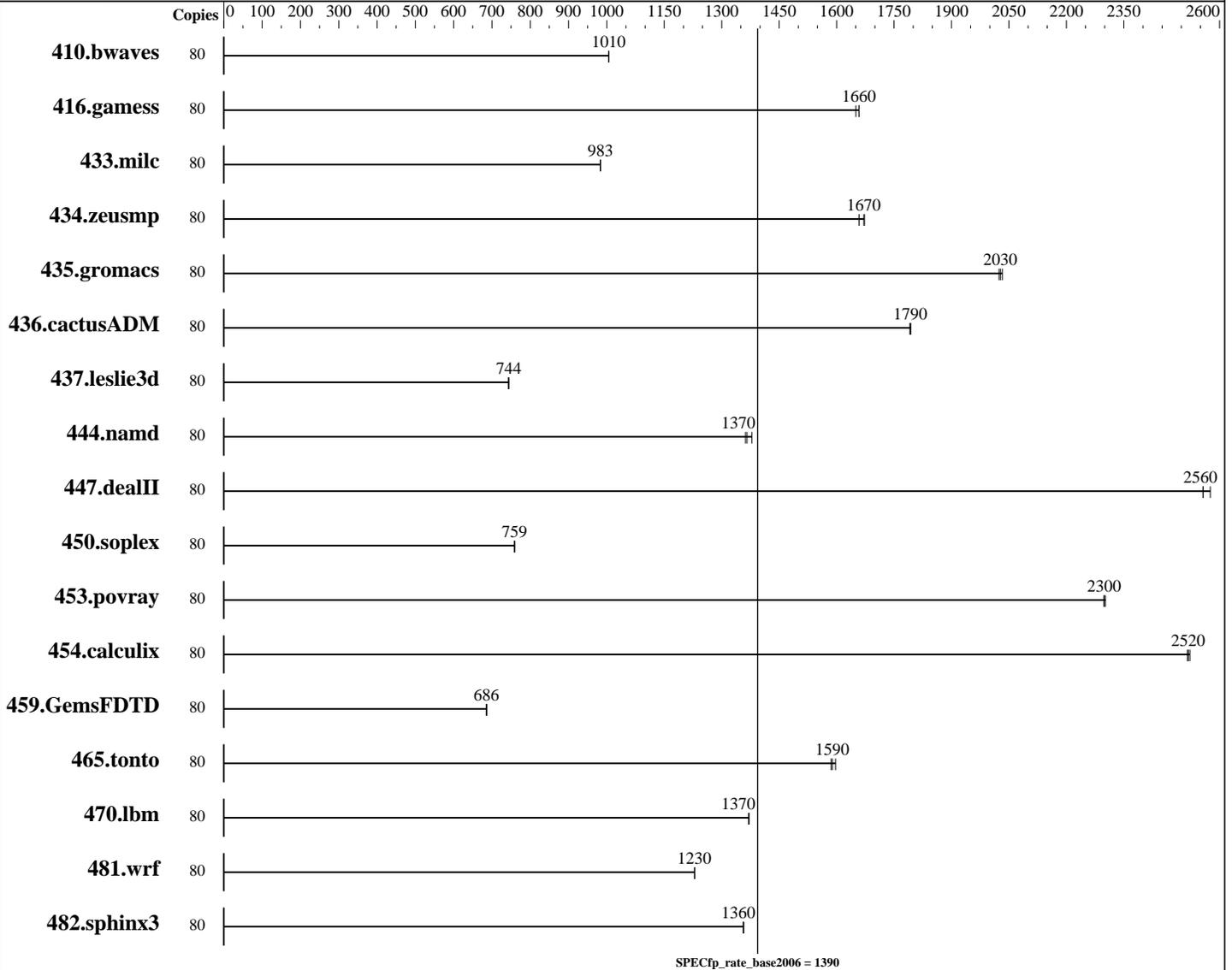
Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017



Hardware

CPU Name: Intel Xeon Gold 6148
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chip(s)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP3
 Kernel 4.4.73-5-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10

(2.40 GHz, Intel Xeon Gold 6148)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 1390

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

L3 Cache: 27.5 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 2 x 480 GB SATA SSD, RAID 0
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	1081	1010	<u>1082</u>	<u>1010</u>	1082	1000							
416.gamess	80	944	1660	949	1650	<u>944</u>	<u>1660</u>							
433.milc	80	747	983	747	984	<u>747</u>	<u>983</u>							
434.zeusmp	80	439	1660	<u>436</u>	<u>1670</u>	435	1670							
435.gromacs	80	282	2020	281	2030	<u>282</u>	<u>2030</u>							
436.cactusADM	80	534	1790	<u>533</u>	<u>1790</u>	533	1790							
437.leslie3d	80	1012	743	1011	744	<u>1011</u>	<u>744</u>							
444.namd	80	465	1380	<u>470</u>	<u>1370</u>	471	1360							
447.dealII	80	358	2560	355	2580	<u>358</u>	<u>2560</u>							
450.soplex	80	880	758	879	759	<u>879</u>	<u>759</u>							
453.povray	80	185	2300	<u>185</u>	<u>2300</u>	185	2300							
454.calculix	80	262	2520	<u>262</u>	<u>2520</u>	262	2520							
459.GemsFDTD	80	1238	686	1237	686	<u>1238</u>	<u>686</u>							
465.tonto	80	493	1600	<u>495</u>	<u>1590</u>	496	1590							
470.lbm	80	801	1370	<u>802</u>	<u>1370</u>	802	1370							
481.wrf	80	727	1230	727	1230	<u>727</u>	<u>1230</u>							
482.sphinx3	80	1149	1360	<u>1149</u>	<u>1360</u>	1150	1360							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set with "tuned-adm profile throughput-performance"
Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10

(2.40 GHz, Intel Xeon Gold 6148)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 1390

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on bl460c115 Wed Dec 6 01:57:24 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz
 2 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 20
  siblings     : 40
 physical 0:   cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
 physical 1:   cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
 cache size    : 28160 KB
```

From /proc/meminfo

```
MemTotal:      197741232 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10

(2.40 GHz, Intel Xeon Gold 6148)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 1390

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
Linux bl460c115 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 6 01:56
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   852G   58G  795G   7% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I41 11/14/2017

Memory:

4x UNKNOWN NOT AVAILABLE

12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10

(2.40 GHz, Intel Xeon Gold 6148)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 1390

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>



SPEC CFP2006 Result

Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen10
(2.40 GHz, Intel Xeon Gold 6148)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 1390

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 16 12:09:41 2018 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 January 2018.