SPEC® CINT2006 Result
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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL560 Gen10
(2.40 GHz, Intel Xeon Gold 6148)

$SPECint^{®}\_rate2006 =$ Not Run

SPECint_rate_base2006 = 3730

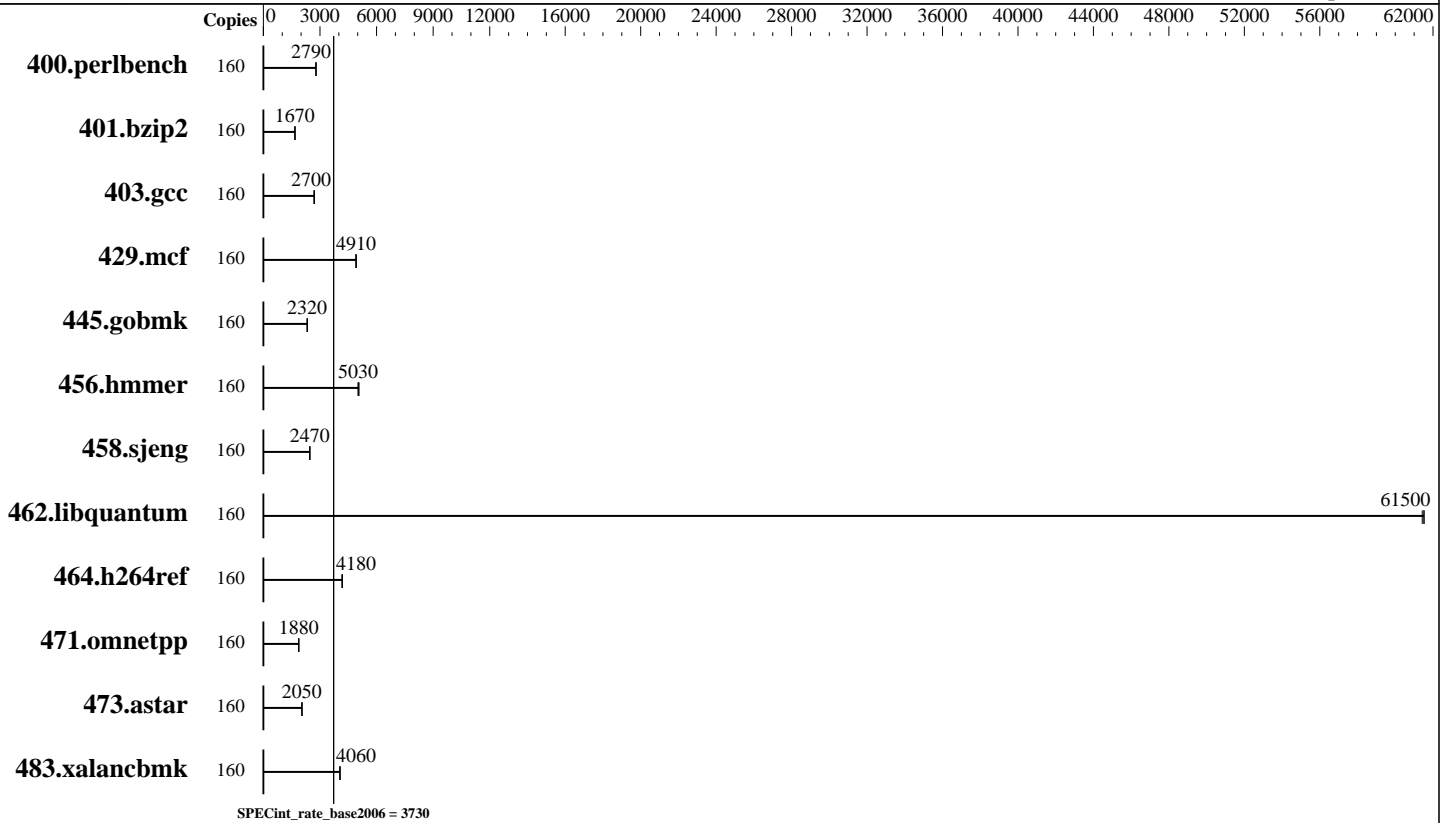|	
CPU2006 license: 3	**Test date:** Nov-2017
Test sponsor: HPE	**Hardware Availability:** Oct-2017
Tested by: HPE	**Software Availability:** Apr-2017

Copies

- 400.perlbench 160 — 2790
- 401.bzip2 160 — 1670
- 403.gcc 160 — 2700
- 429.mcf 160 — 4910
- 445.gobmk 160 — 2320
- 456.hmmer 160 — 5030
- 458.sjeng 160 — 2470
- 462.libquantum 160 — 61500
- 464.h264ref 160 — 4180
- 471.omnetpp 160 — 1880
- 473.astar 160 — 2050
- 483.xalancbmk 160 — 4060

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Hardware

CPU Name:	Intel Xeon Gold 6148
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	80 cores, 4 chips, 20 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2, 4 chip(s)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	27.5 MB I+D on chip per chip
Other Cache:	None
Memory:	768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem:	2 x 480 GB SATA SSD, RAID 0
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 (x86_64) SP2 Kernel 4.4.21-69-default
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	160	560	2790	561	2790	**560**	**2790**							
401.bzip2	160	919	1680	**923**	**1670**	924	1670							
403.gcc	160	480	2680	**477**	**2700**	477	2700							
429.mcf	160	296	4920	297	4910	**297**	**4910**							
445.gobmk	160	722	2320	**722**	**2320**	722	2330							
456.hmmer	160	297	5020	294	5070	**297**	**5030**							
458.sjeng	160	789	2460	**785**	**2470**	785	2470							
462.libquantum	160	54.0	61400	53.9	61500	**53.9**	**61500**							
464.h264ref	160	849	4170	846	4180	**847**	**4180**							
471.omnetpp	160	534	1870	533	1880	**533**	**1880**							
473.astar	160	549	2050	**549**	**2050**	550	2040							
483.xalancbmk	160	272	4070	272	4050	**272**	**4060**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled by default
    Filesystem page cache cleared with:
     shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
    runspec command invoked through numactl i.e.:
     numactl --interleave=all runspec <etc>
    irqbalance disabled with "service irqbalance stop"
    tuned profile set wtih "tuned-adm profile throughput-performance"
    VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
    Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

    BIOS Configuration:
     Thermal Configuration set to Maximum Cooling
     LLC Prefetch set to Enabled
     LLC Dead Line Allocation set to Disabled
     Stale A to S set to Enabled
     Memory Patrol Scrubbing set to Disabled
     Workload Profile set to General Throughput Compute
      Minimum Processor Idle Power Core C-State set to C1E State

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Platform Notes (Continued)

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-e6az Wed Nov 29 11:16:55 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz
      4 "physical id"s (chips)
      160 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 20
      siblings  : 40
      physical 0: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
      physical 1: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
      physical 2: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
      physical 3: cores 0 1 2 3 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
   cache size : 28160 KB

From /proc/meminfo
   MemTotal:       792269984 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-e6az 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
   (9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

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Platform Notes (Continued)

```
run-level 3 Nov 29 11:16

SPEC is set to: /home/cpu2006
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda4       xfs   852G   86G  767G  11% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HPE U34 09/29/2017
   Memory:
   48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
```

Base Compiler Invocation

C benchmarks:
    icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
    icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

```
   400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
       401.bzip2: -D_FILE_OFFSET_BITS=64
         403.gcc: -D_FILE_OFFSET_BITS=64
         429.mcf: -D_FILE_OFFSET_BITS=64
       445.gobmk: -D_FILE_OFFSET_BITS=64
       456.hmmer: -D_FILE_OFFSET_BITS=64
       458.sjeng: -D_FILE_OFFSET_BITS=64
   462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
      464.h264ref: -D_FILE_OFFSET_BITS=64
      471.omnetpp: -D_FILE_OFFSET_BITS=64
```

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Base Portability Flags (Continued)

        473.astar: `-D_FILE_OFFSET_BITS=64`
    483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

    `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
    `-qopt-mem-layout-trans=3`

C++ benchmarks:

    `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
    `-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap`

Base Other Flags

C benchmarks:

        403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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