



SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.00 GHz, Intel Xeon Gold 6154)

SPECfp®2006 =

Not Run

SPECfp_base2006 =

149

CPU2006 license: 3

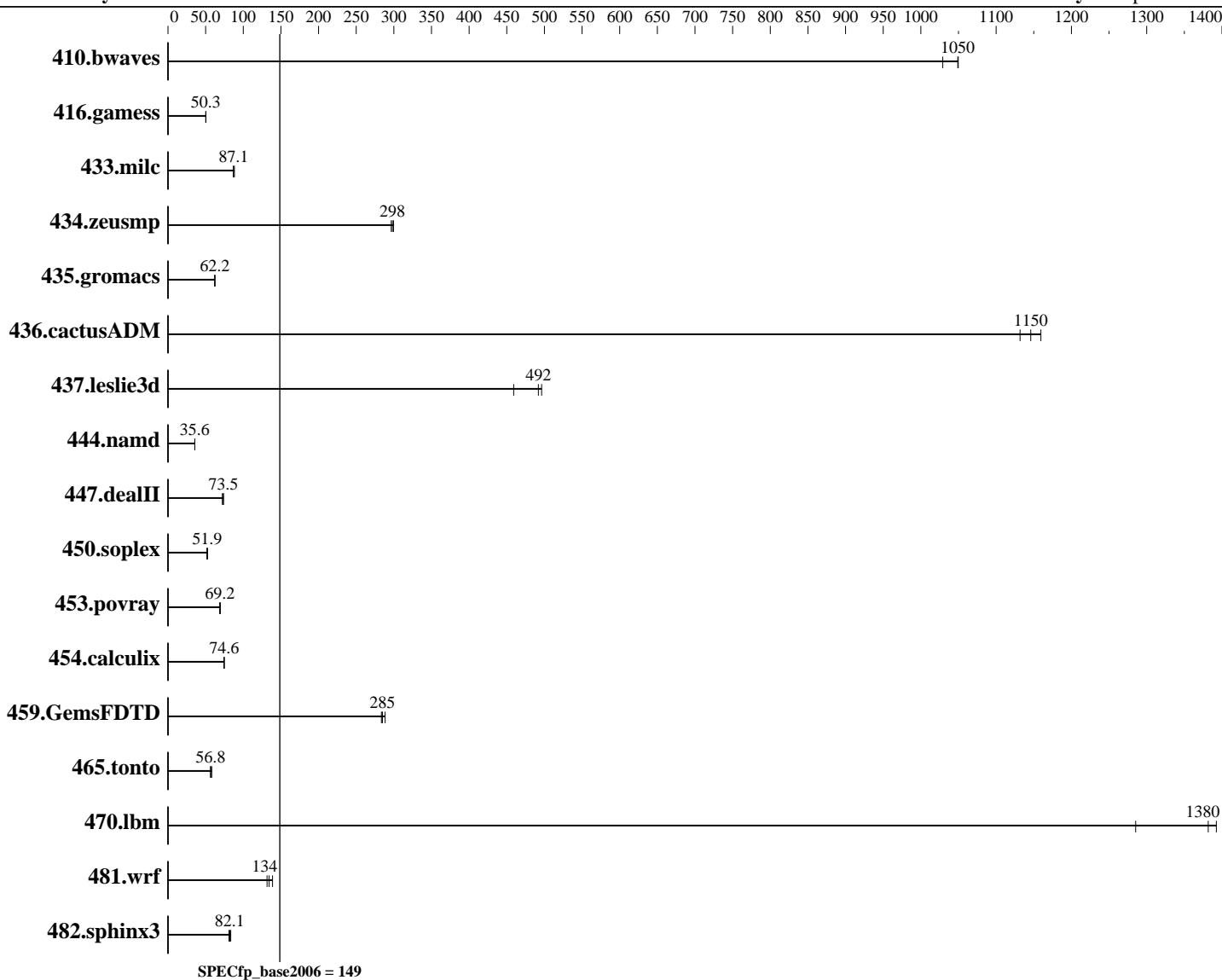
Test sponsor: HPE

Tested by: HPE

Test date: Oct-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6154
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
 CPU(s) orderable: 1, 2 chip(s)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 Compiler: Kernel 3.10.0-514.el7.x86_64
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: xfs

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant DL360 Gen10
(3.00 GHz, Intel Xeon Gold 6154)

SPECfp2006 = Not Run

SPECfp_base2006 = 149

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Oct-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12.9	1050	13.0	1050	13.2	1030						
416.gamess	389	50.3	390	50.3	389	50.3						
433.milc	106	86.8	104	88.2	105	87.1						
434.zeusmp	30.7	297	30.5	298	30.4	300						
435.gromacs	115	62.2	115	62.2	115	62.3						
436.cactusADM	10.4	1150	10.3	1160	10.6	1130						
437.leslie3d	19.1	492	20.5	459	18.9	496						
444.namd	225	35.6	225	35.7	225	35.6						
447.dealII	155	73.7	159	72.2	156	73.5						
450.soplex	159	52.5	161	51.9	161	51.8						
453.povray	76.9	69.2	77.4	68.7	76.6	69.4						
454.calculix	111	74.6	111	74.5	111	74.6						
459.GemsFDTD	36.8	288	37.4	283	37.2	285						
465.tonto	173	56.8	174	56.4	169	58.1						
470.lbm	9.94	1380	10.7	1290	9.87	1390						
481.wrf	84.9	132	83.1	134	80.4	139						
482.sphinx3	237	82.1	234	83.4	241	81.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
IRQ balance service was stop using "service irqbalance stop"

Tuned-adm profile was set to Throughput-Performance

Platform Notes

BIOS Configuration:

Intel Hyperthreading set to Disabled

Thermal Configuration set to Maximum Cooling

LLC Prefetch set to Enabled

LLC Dead Line Allocation set to Disabled

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.00 GHz, Intel Xeon Gold 6154)

SPECfp2006 =

Not Run

SPECfp_base2006 =

149

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date:

Oct-2017

Hardware Availability:

Oct-2017

Software Availability:

Apr-2017

Platform Notes (Continued)

Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Peak Frequency Compute
Energy/Performance Bias set to Maximum Performance
Workload Profile set to Custom
NUMA Group Size Optimization set to Flat
Sysinfo program /home/specuser/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on dl360gen10rhel73 Tue Oct 31 13:26:03 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
        2 "physical id"s (chips)
        36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 18
        siblings   : 18
        physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 25344 KB
```

```
From /proc/meminfo
MemTotal:       197571680 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.3 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.3"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

uname -a:
Linux dl360gen10rhel73 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT
2016 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 31 13:25

SPEC is set to: /home/specuser/cpu2006
Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.00 GHz, Intel Xeon Gold 6154)

SPECfp2006 =

Not Run

SPECfp_base2006 =

149

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Oct-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	392G	37G	356G	10%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 09/29/2017

Memory:

24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=core,compact"

LD_LIBRARY_PATH = "/home/specuser/cpu2006/lib/ia32:/home/specuser/cpu2006/lib/intel64:/home/specuser/cpu2006/sh10.2"

OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.00 GHz, Intel Xeon Gold 6154)

SPECfp2006 =

Not Run

SPECfp_base2006 =

149

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date:

Oct-2017

Hardware Availability:

Oct-2017

Software Availability:

Apr-2017

Base Portability Flags (Continued)

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revF.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Nov 29 12:30:07 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 November 2017.