



SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

Synergy 480 Gen10

(2.10 GHz, Intel Xeon Gold 6130)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 1230

CPU2006 license: 3

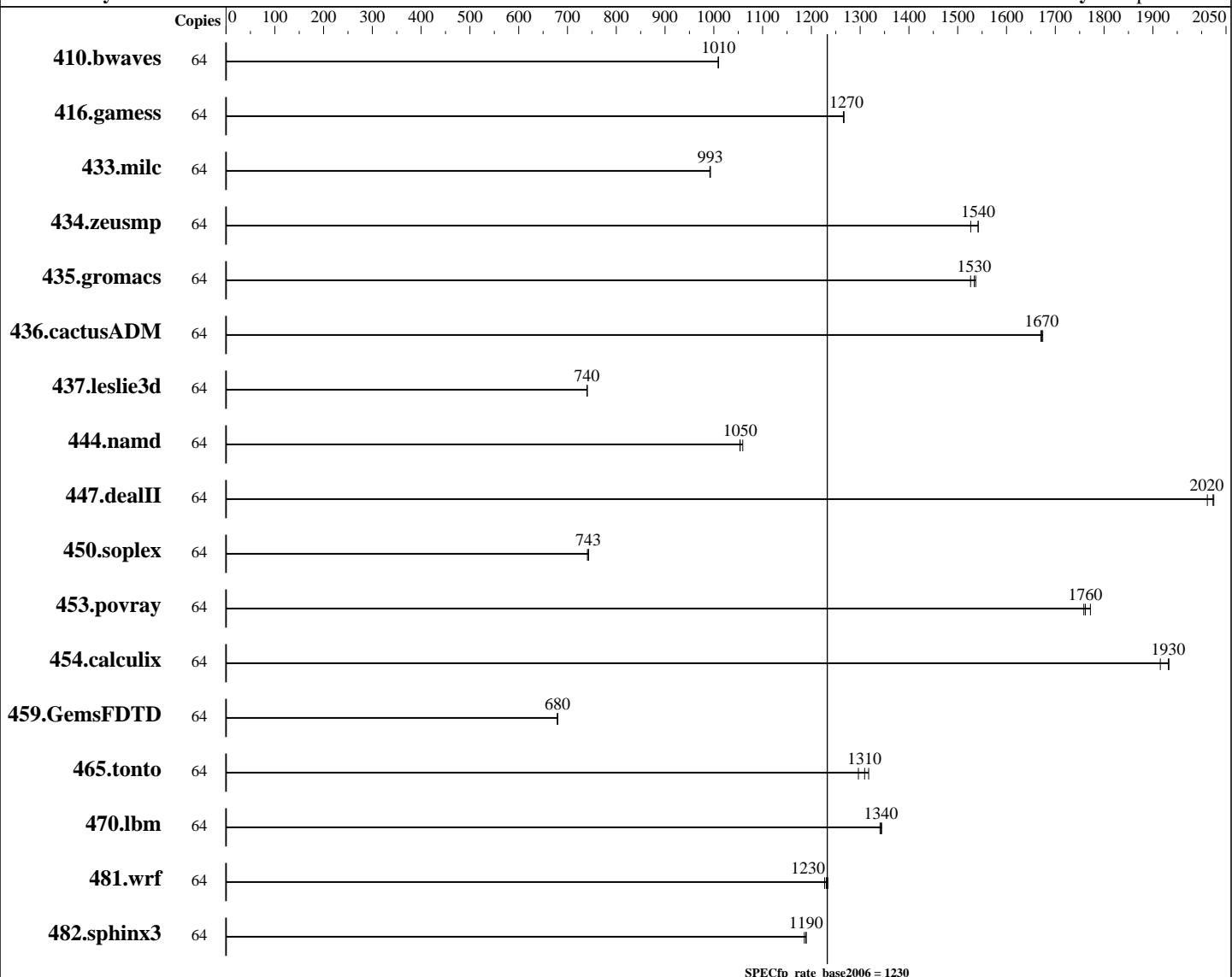
Test sponsor: HPE

Tested by: HPE

Test date: Oct-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2016



Hardware

CPU Name: Intel Xeon Gold 6130
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chip(s)
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
 Compiler: Kernel 4.4.21-69-default
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 22 MB I+D on chip per chip
 Other Cache: None
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
 Disk Subsystem: 1 x 960 GB SSD SATA, RAID 0
 Other Hardware: None

Base Pointers: 32/64-bit
 Peak Pointers: Not Applicable
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	64	861	1010	862	1010	862	1010							
416.gamess	64	989	1270	990	1270	990	1270							
433.milc	64	592	992	592	993	592	993							
434.zeusmp	64	378	1540	381	1530	378	1540							
435.gromacs	64	297	1540	298	1530	299	1530							
436.cactusADM	64	458	1670	457	1670	457	1670							
437.leslie3d	64	812	741	813	740	813	740							
444.namd	64	485	1060	487	1050	487	1050							
447.dealII	64	364	2010	362	2020	362	2020							
450.soplex	64	719	743	720	741	718	743							
453.povray	64	192	1770	194	1760	193	1760							
454.calculix	64	273	1930	273	1930	276	1920							
459.GemsFDTD	64	1000	679	998	680	999	680							
465.tonto	64	478	1320	481	1310	486	1300							
470.lbm	64	656	1340	655	1340	654	1340							
481.wrf	64	579	1230	583	1230	581	1230							
482.sphinx3	64	1049	1190	1049	1190	1052	1190							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>



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Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetcher set to Enabled
LLC Dead Line Allocation set to Disabled
Stale A to S set to Disabled
Workload Pofile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-vjvj Thu Oct 12 12:41:50 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
2 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

From /proc/meminfo
MemTotal: 395924520 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
This file is deprecated and will be removed in a future service pack or release.
Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"

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Platform Notes (Continued)

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

```
Linux linux-vjuj 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 12 12:05

SPEC is set to: /home/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	852G	6.3G	846G	1%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 09/27/2017

Memory:

24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>



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For other inquiries, please contact webmaster@spec.org.

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