



# SPEC® CFP2006 Result

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## Lenovo Global Technology

ThinkSystem SR950  
(2.10 GHz, Intel Xeon Platinum 8170M)

SPECfp®\_rate2006 = 6450

SPECfp\_rate\_base2006 = 6280

CPU2006 license: 9017

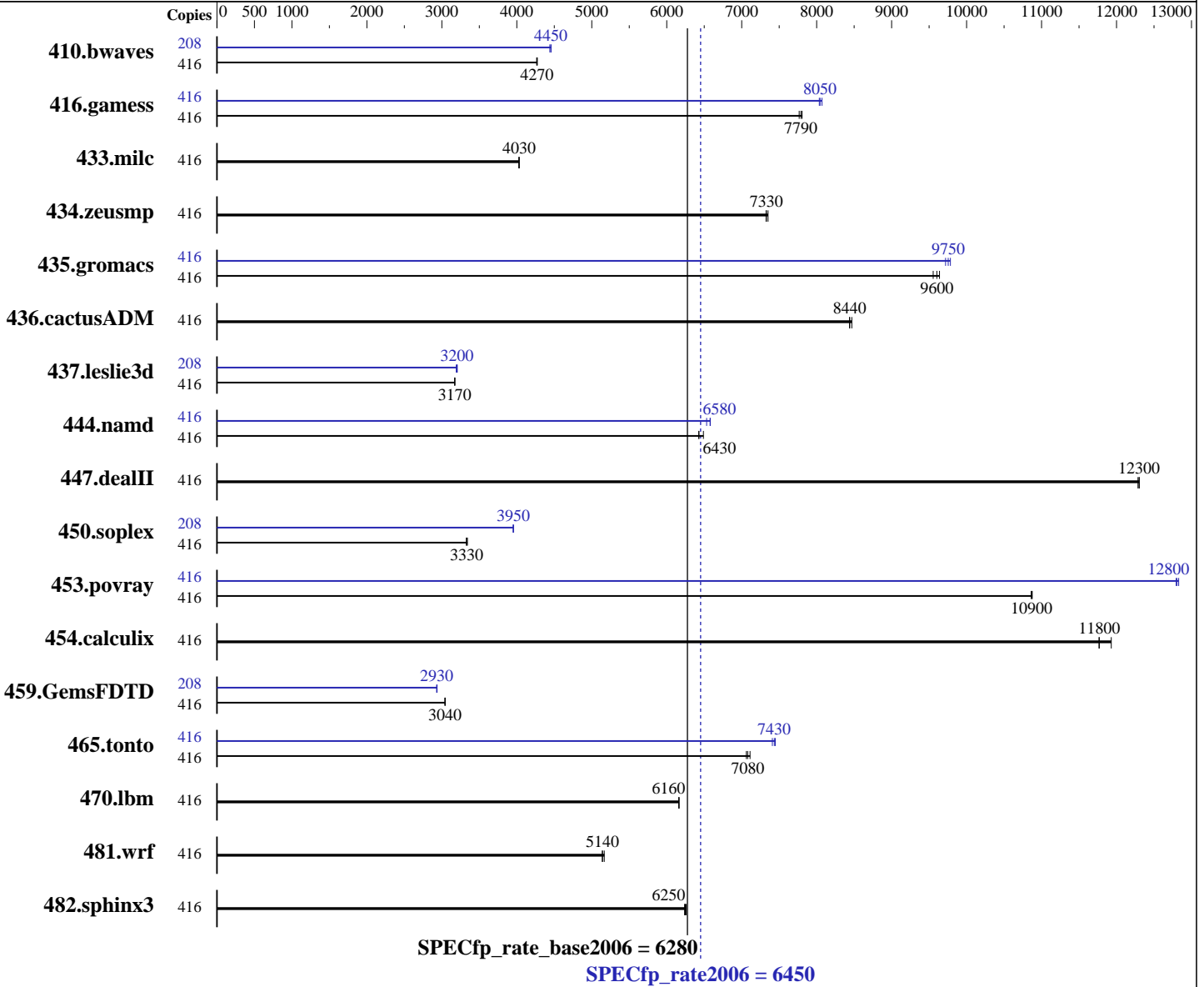
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Aug-2017

Hardware Availability: Sep-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Platinum 8170M  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 208 cores, 8 chips, 26 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4,8 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: tmpfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 35.75 MB I+D on chip per chip  
Other Cache: None  
Memory: 3 TB (96 x 32 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 800 GB tmpfs  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	416	1323	4270	1326	4260	<u>1324</u>	<u>4270</u>	208	<u>635</u>	<u>4450</u>	634	4460	637	4440
416.gamess	416	1043	7810	<u>1046</u>	<u>7790</u>	1049	7770	416	1014	8030	<u>1012</u>	<u>8050</u>	1009	8070
433.milc	416	948	4030	<u>947</u>	<u>4030</u>	947	4030	416	948	4030	<u>947</u>	<u>4030</u>	947	4030
434.zeusmp	416	<u>516</u>	<u>7330</u>	515	7350	517	7320	416	<u>516</u>	<u>7330</u>	515	7350	517	7320
435.gromacs	416	311	9550	308	9640	<u>309</u>	<u>9600</u>	416	<u>305</u>	<u>9750</u>	306	9720	304	9780
436.cactusADM	416	589	8440	<u>589</u>	<u>8440</u>	587	8470	416	589	8440	<u>589</u>	<u>8440</u>	587	8470
437.leslie3d	416	1232	3170	1234	3170	<u>1232</u>	<u>3170</u>	208	610	3210	<u>610</u>	<u>3200</u>	613	3190
444.namd	416	514	6490	519	6430	<u>519</u>	<u>6430</u>	416	511	6530	<u>507</u>	<u>6580</u>	507	6580
447.dealII	416	<u>387</u>	<u>12300</u>	387	12300	387	12300	416	<u>387</u>	<u>12300</u>	387	12300	387	12300
450.soplex	416	1043	3330	1038	3340	<u>1041</u>	<u>3330</u>	208	<u>439</u>	<u>3950</u>	439	3960	439	3950
453.povray	416	204	10900	<u>204</u>	<u>10900</u>	203	10900	416	173	12800	173	12800	<u>173</u>	<u>12800</u>
454.calculix	416	288	11900	<u>292</u>	<u>11800</u>	292	11800	416	288	11900	<u>292</u>	<u>11800</u>	292	11800
459.GemsFDTD	416	1451	3040	<u>1450</u>	<u>3040</u>	1450	3040	208	752	2940	753	2930	<u>753</u>	<u>2930</u>
465.tonto	416	<u>578</u>	<u>7080</u>	576	7110	580	7060	416	550	7450	<u>551</u>	<u>7430</u>	553	7410
470.lbm	416	<u>927</u>	<u>6160</u>	928	6160	927	6160	416	<u>927</u>	<u>6160</u>	928	6160	927	6160
481.wrf	416	904	5140	<u>903</u>	<u>5140</u>	899	5170	416	904	5140	<u>903</u>	<u>5140</u>	899	5170
482.sphinx3	416	1300	6240	<u>1298</u>	<u>6250</u>	1295	6260	416	1300	6240	<u>1298</u>	<u>6250</u>	1295	6260

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Tmpfs filesystem can be set with:

```
mount -t tmpfs -o size=800g tmpfs /home
```

Process tuning setting:

```
echo 50000 > /proc/sys/kernel/sched_cfs_bandwidth_slice_us
echo 240000000 > /proc/sys/kernel/sched_latency_ns
echo 5000000 > /proc/sys/kernel/sched_migration_cost_ns
echo 100000000 > /proc/sys/kernel/sched_min_granularity_ns
```

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## Operating System Notes (Continued)

```
echo 150000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
```

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

SNC set to Enable

DCU Streamer Prefetcher set to Disable

Stale AtoS set to Enable

LLC dead line alloc set to Disable

Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on Proton8S-SUSE12SP2 Thu Aug 10 23:57:27 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8170M CPU @ 2.10GHz

8 "physical id"s (chips)

416 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 26

siblings : 52

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 4: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 5: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 6: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

physical 7: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25  
26 27 28 29

cache size : 36608 KB

From /proc/meminfo

MemTotal: 3170207836 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

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## Platform Notes (Continued)

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION\_ID="12.2"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux Proton8S-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Aug 10 23:52

SPEC is set to: /home/cpu2006-1.2-ic17.0u3

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	800G	3.7G	797G	1%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[PSE105L-1.00]- 06/22/2017

Memory:

96x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealIII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml>

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