



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55

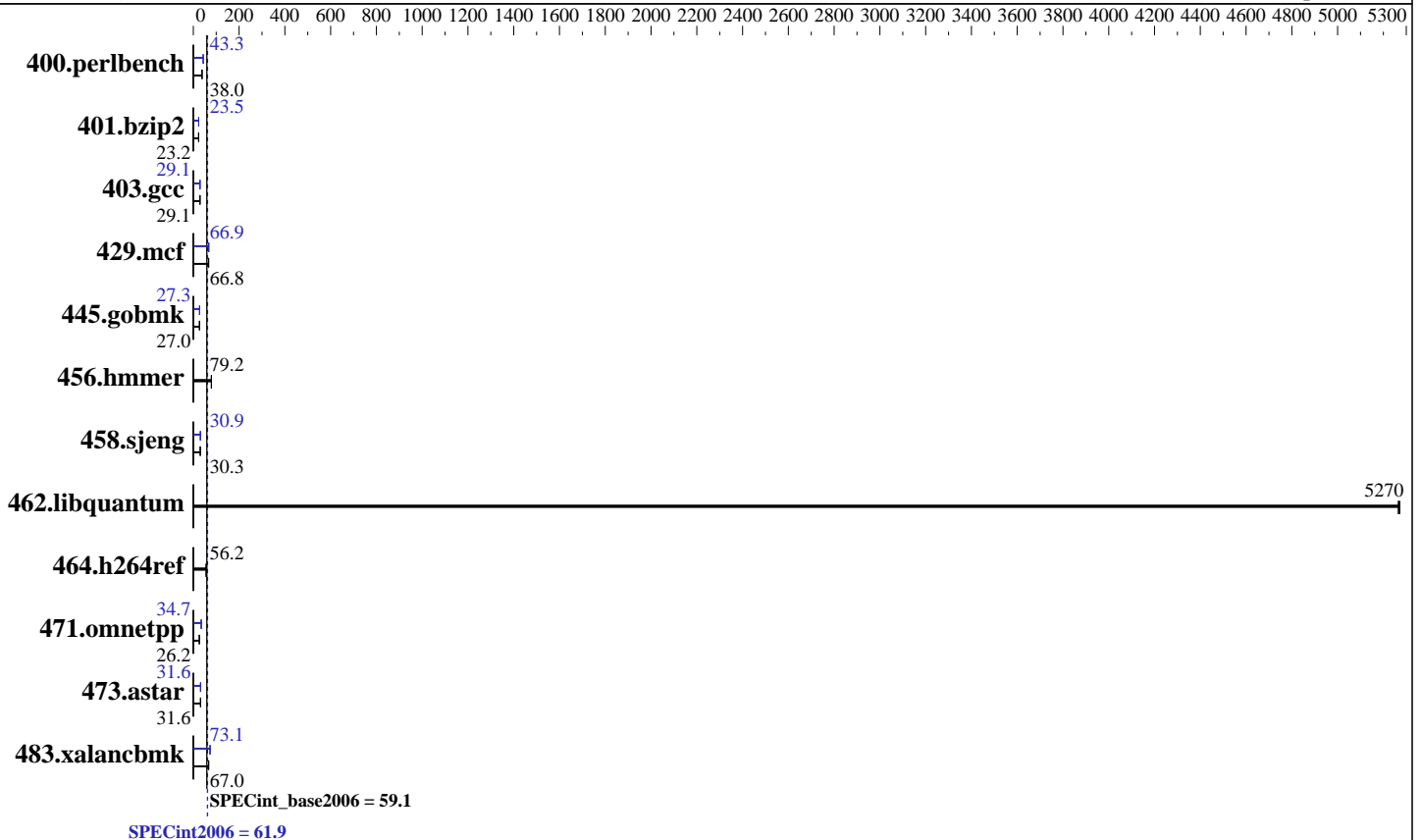
Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Silver 4116
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 16.5 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400 MT/s)
 Disk Subsystem: 1 x 1 TB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP2
 4.4.21-69-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++
 Compiler for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2017
Hardware Availability: Sep-2017
Software Availability: Apr-2017

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	256	38.1	<u>257</u>	<u>38.0</u>	257	38.0	226	43.3	<u>226</u>	<u>43.3</u>	226	43.3
401.bzip2	<u>416</u>	<u>23.2</u>	417	23.2	415	23.2	412	23.4	411	23.5	<u>411</u>	<u>23.5</u>
403.gcc	<u>277</u>	<u>29.1</u>	278	29.0	277	29.1	273	29.5	280	28.7	<u>277</u>	<u>29.1</u>
429.mcf	136	67.2	<u>136</u>	<u>66.8</u>	138	66.1	<u>136</u>	<u>66.9</u>	134	68.3	137	66.7
445.gobmk	389	27.0	<u>389</u>	<u>27.0</u>	389	26.9	<u>384</u>	<u>27.3</u>	383	27.4	384	27.3
456.hammer	<u>118</u>	<u>79.2</u>	119	78.5	118	79.2	<u>118</u>	<u>79.2</u>	119	78.5	118	79.2
458.sjeng	<u>399</u>	<u>30.3</u>	399	30.4	399	30.3	391	30.9	<u>391</u>	<u>30.9</u>	391	30.9
462.libquantum	<u>3.93</u>	<u>5270</u>	3.94	5260	3.93	5270	<u>3.93</u>	<u>5270</u>	3.94	5260	3.93	5270
464.h264ref	393	56.3	<u>393</u>	<u>56.2</u>	394	56.1	393	56.3	<u>393</u>	<u>56.2</u>	394	56.1
471.omnetpp	<u>238</u>	<u>26.2</u>	237	26.4	239	26.2	179	34.9	183	34.2	<u>180</u>	<u>34.7</u>
473.astar	223	31.4	<u>222</u>	<u>31.6</u>	222	31.6	<u>222</u>	<u>31.6</u>	223	31.5	222	31.6
483.xalancbmk	104	66.6	<u>103</u>	<u>67.0</u>	103	67.1	93.8	73.6	94.4	73.1	<u>94.4</u>	<u>73.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Sub NUMA Cluster disabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /root/cpu2006-1.2_ic17u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-vw3y Fri Jul 28 01:43:01 2017

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55

Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 24
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 16896 KB

```

```

From /proc/meminfo
MemTotal:      196684112 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux linux-vw3y 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 28 01:42

```

SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   923G  9.1G  914G   1% /

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55

Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.4.2 07/21/2017

Memory:

12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz, configured at 2400 MHz

4x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/root/cpu2006-1.2_icl7u3/lib/ia32:/root/cpu2006-1.2_icl7u3/lib/intel64:/root/cpu2006-1.2_icl7u3/sh10.2"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55

Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55

Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

Peak Portability Flags (Continued)

445.gobmk: -D_FILE_OFFSET_BITS=64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
 -qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
 -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -qopt-ra-region-strategy=block
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 61.9

PowerEdge R440 (Intel Xeon Silver 4116, 2.10 GHz)

SPECint_base2006 = 59.1

CPU2006 license: 55

Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

Peak Optimization Flags (Continued)

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Sep 20 11:03:04 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 September 2017.