



SPEC® CINT2006 Result

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Huawei

SPECint®_rate2006 = 2210

Huawei 2488 V5 (Intel Xeon Gold 6134M)

SPECint_rate_base2006 = 2080

CPU2006 license: 3175

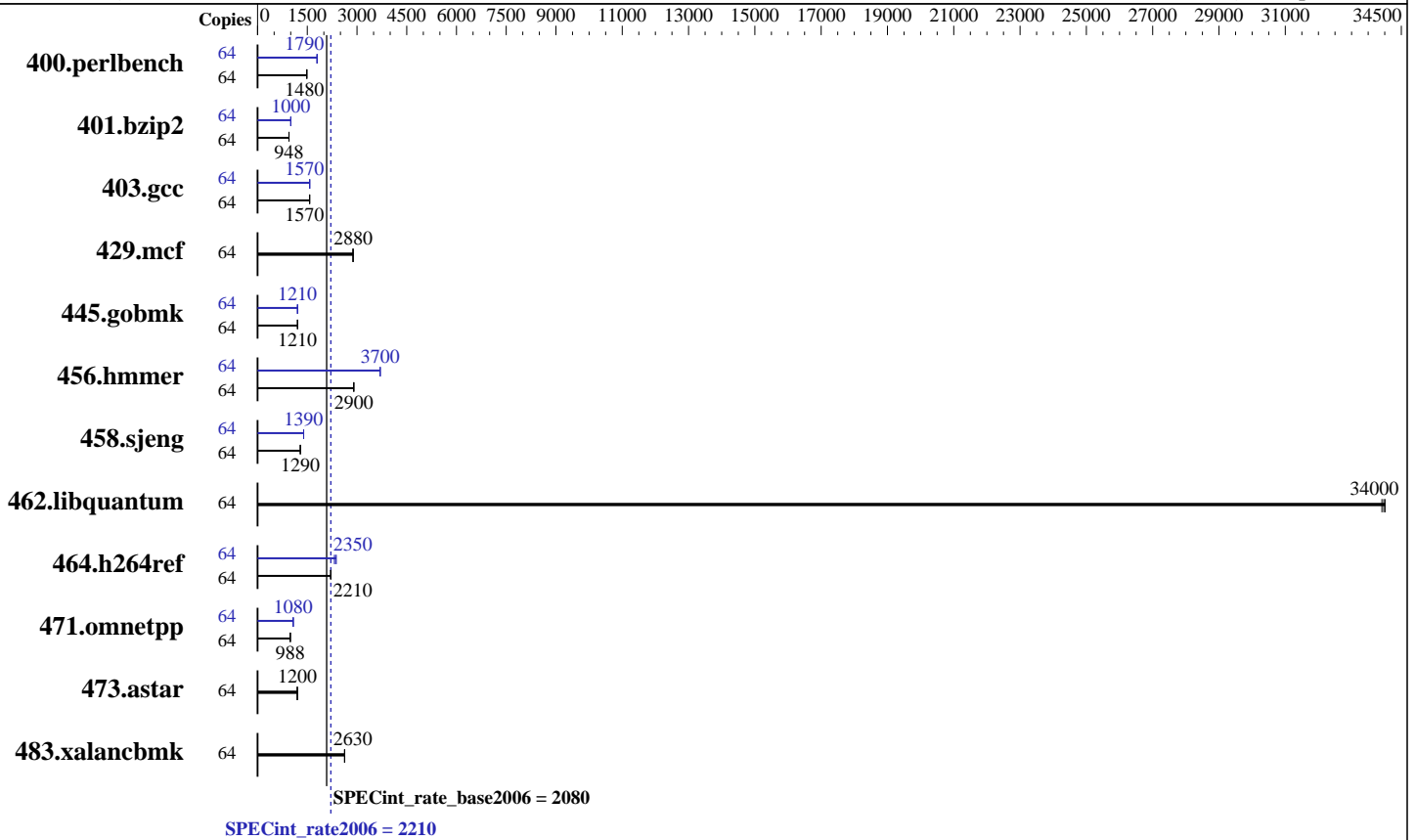
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6134M
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 24.75 MB I+D on chip per chip
 Other Cache: None
 Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)
 Disk Subsystem: 2 x 300 GB SAS, 10K RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 5 (multi user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------------|--------------------|--------------------|---------------------|-------------------|--------------------|--------|-------------------|--------------------|--------------------|---------------------|-------------------|--------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 64 | 420 | 1490 | 421 | 1480 | <u>421</u> | <u>1480</u> | 64 | 349 | 1790 | <u>349</u> | <u>1790</u> | 347 | 1800 |
| 401.bzip2 | 64 | 650 | 950 | <u>651</u> | <u>948</u> | 653 | 946 | 64 | 614 | 1010 | 617 | 1000 | <u>616</u> | <u>1000</u> |
| 403.gcc | 64 | 327 | 1570 | 329 | 1570 | <u>328</u> | <u>1570</u> | 64 | <u>328</u> | <u>1570</u> | 328 | 1570 | 327 | 1570 |
| 429.mcf | 64 | 202 | 2890 | 203 | 2880 | <u>203</u> | <u>2880</u> | 64 | 202 | 2890 | 203 | 2880 | <u>203</u> | <u>2880</u> |
| 445.gobmk | 64 | <u>557</u> | <u>1210</u> | 556 | 1210 | 558 | 1200 | 64 | 556 | 1210 | 558 | 1200 | <u>557</u> | <u>1210</u> |
| 456.hammer | 64 | 206 | 2900 | 206 | 2910 | <u>206</u> | <u>2900</u> | 64 | 161 | 3700 | 162 | 3700 | <u>161</u> | <u>3700</u> |
| 458.sjeng | 64 | <u>600</u> | <u>1290</u> | 600 | 1290 | 600 | 1290 | 64 | 557 | 1390 | 557 | 1390 | <u>557</u> | <u>1390</u> |
| 462.libquantum | 64 | 39.0 | 34000 | <u>39.0</u> | <u>34000</u> | 39.1 | 33900 | 64 | 39.0 | 34000 | <u>39.0</u> | <u>34000</u> | 39.1 | 33900 |
| 464.h264ref | 64 | 639 | 2220 | <u>641</u> | <u>2210</u> | 643 | 2200 | 64 | 611 | 2320 | <u>602</u> | <u>2350</u> | 596 | 2380 |
| 471.omnetpp | 64 | 405 | 988 | 405 | 988 | <u>405</u> | <u>988</u> | 64 | 372 | 1070 | 372 | 1080 | <u>372</u> | <u>1080</u> |
| 473.astar | 64 | 374 | 1200 | 376 | 1200 | <u>375</u> | <u>1200</u> | 64 | 374 | 1200 | 376 | 1200 | <u>375</u> | <u>1200</u> |
| 483.xalancbmk | 64 | 168 | 2630 | 168 | 2620 | <u>168</u> | <u>2630</u> | 64 | 168 | 2630 | 168 | 2620 | <u>168</u> | <u>2630</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with:
cpupower -c all frequency-set -g performance

Platform Notes

BIOS configuration:
Set SNC to enabled
Set IMC interleaving to 1 way interleave
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-va9t Thu Aug 24 11:38:59 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6134M CPU @ 3.20GHz
4 "physical id"s (chips)
64 "processors"

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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings  : 16
physical 0: cores 0 2 3 9 16 19 26 27
physical 1: cores 0 2 4 8 9 10 18 27
physical 2: cores 0 2 3 9 16 19 26 27
physical 3: cores 0 2 3 9 16 19 26 27
cache size : 25344 KB
```

From /proc/meminfo

```
MemTotal:      394396252 kB
```

```
HugePages_Total:      0
```

```
Hugepagesize:      2048 kB
```

/usr/bin/lsc_release -d

```
SUSE Linux Enterprise Server 12 SP2
```

From /etc/*release* /etc/*version*

SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
```

```
VERSION = 12
```

```
PATCHLEVEL = 2
```

```
# This file is deprecated and will be removed in a future service pack or release.
```

```
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
```

```
VERSION="12-SP2"
```

```
VERSION_ID="12.2"
```

```
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
```

```
ID="sles"
```

```
ANSI_COLOR="0;32"
```

```
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

uname -a:

```
Linux linux-va9t 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 5 Aug 24 11:36

SPEC is set to: /home/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   516G  17G  499G   4% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS INSYDE Corp. 0.38 07/28/2017
Memory:
8x NO DIMM NO DIMM
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalanbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -D_FILE_OFFSET_BITS=64

429.mcf: -D_FILE_OFFSET_BITS=64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

464.h264ref: -D_FILE_OFFSET_BITS=64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -D_FILE_OFFSET_BITS=64

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Peak Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmmer: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto-ilp32
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)
-qopt-ra-region-strategy=block
-qopt-mem-layout-trans=3 -Wl,-z,muldefs
-L/sh10.2 -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>

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For other inquiries, please contact webmaster@spec.org.

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