



# SPEC® CINT2006 Result

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## Lenovo Global Technology

### SPECint®\_rate2006 = 266

Lenovo ThinkServer TS460  
(3.50 GHz, Intel Xeon E3-1230 v6)

### SPECint\_rate\_base2006 = 257

CPU2006 license: 9017

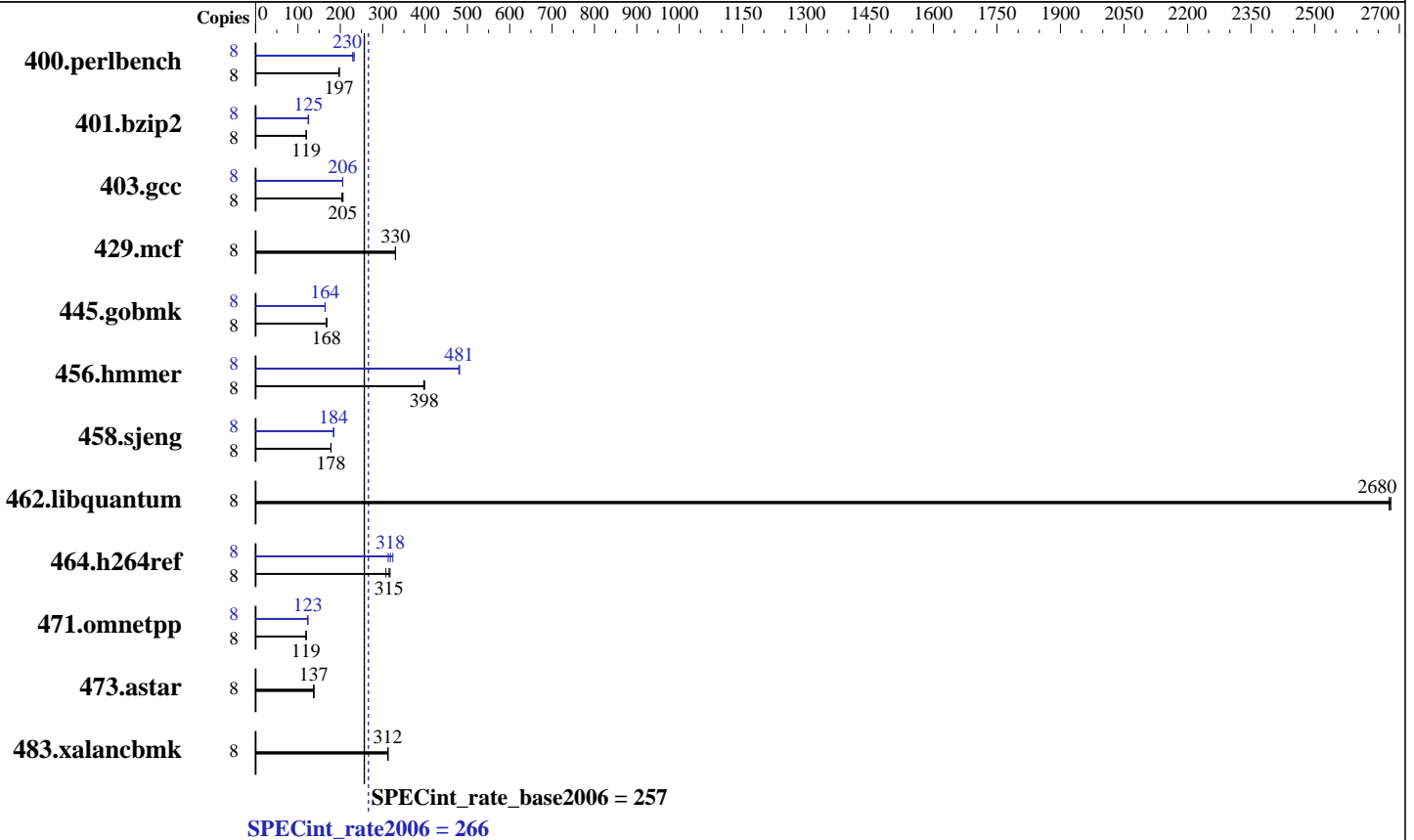
Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Jun-2017

Tested by: Lenovo Global Technology

Software Availability: Nov-2016



### Hardware

CPU Name: Intel Xeon E3-1230 v6  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2400T-E)  
 Disk Subsystem: 1 x 960 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
 Kernel 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux  
 Auto Parallel: No  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b><u>396</u></b>	<b><u>197</u></b>	397	197	396	197	8	<b><u>340</u></b>	<b><u>230</u></b>	340	230	336	233
401.bzip2	8	643	120	647	119	<b><u>646</u></b>	<b><u>119</u></b>	8	623	124	<b><u>617</u></b>	<b><u>125</u></b>	617	125
403.gcc	8	312	206	<b><u>314</u></b>	<b><u>205</u></b>	316	204	8	<b><u>313</u></b>	<b><u>206</u></b>	313	206	313	206
429.mcf	8	221	330	221	330	<b><u>221</u></b>	<b><u>330</u></b>	8	221	330	221	330	<b><u>221</u></b>	<b><u>330</u></b>
445.gobmk	8	500	168	<b><u>501</u></b>	<b><u>168</u></b>	502	167	8	511	164	<b><u>511</u></b>	<b><u>164</u></b>	512	164
456.hammer	8	188	397	<b><u>187</u></b>	<b><u>398</u></b>	187	399	8	155	481	<b><u>155</u></b>	<b><u>481</u></b>	156	480
458.sjeng	8	<b><u>544</u></b>	<b><u>178</u></b>	543	178	545	178	8	524	185	<b><u>525</u></b>	<b><u>184</u></b>	526	184
462.libquantum	8	<b><u>61.9</u></b>	<b><u>2680</u></b>	61.9	2680	61.9	2680	8	<b><u>61.9</u></b>	<b><u>2680</u></b>	61.9	2680	61.9	2680
464.h264ref	8	558	317	<b><u>562</u></b>	<b><u>315</u></b>	575	308	8	546	324	<b><u>557</u></b>	<b><u>318</u></b>	565	313
471.omnetpp	8	418	119	<b><u>420</u></b>	<b><u>119</u></b>	420	119	8	405	123	<b><u>405</u></b>	<b><u>123</u></b>	407	123
473.astar	8	408	138	409	137	<b><u>409</u></b>	<b><u>137</u></b>	8	408	138	409	137	<b><u>409</u></b>	<b><u>137</u></b>
483.xalancbmk	8	176	313	<b><u>177</u></b>	<b><u>312</u></b>	177	312	8	176	313	<b><u>177</u></b>	<b><u>312</u></b>	177	312

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on TS460 Thu Jul 20 17:39:40 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1230 v6 @ 3.50GHz
1 "physical id"s (chips)
8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings : 8
```

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### Platform Notes (Continued)

physical 0: cores 0 1 2 3  
cache size : 8192 KB

From /proc/meminfo  
MemTotal: 32888952 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)  
VERSION = 12  
PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"  
VERSION="12-SP2"  
VERSION\_ID="12.2"  
PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"  
ID="sles"  
ANSI\_COLOR="0;32"  
CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux TS460 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jul 20 17:39

SPEC is set to: /home/cpu2006-1.2-ic17.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	btrfs	895G	21G	873G	3%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. TB1TS223 03/16/2017

Memory:

2x Micron 18ASF2G72AZ-2G3B1 16 GB 2 rank 2400 MHz  
2x Not Specified Not Specified

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
echo 2 > /proc/sys/vm/drop\_caches

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)

-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)

-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto-ilp32  
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)  
-qopt-ra-region-strategy=block  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-KBL-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-KBL-revC.xml>

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