



# SPEC® CINT2006 Result

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Huawei

**SPECint\_rate2006 = 1390**

Huawei 2288H V5 (Intel Xeon Platinum 8153)

**SPECint\_rate\_base2006 = 1290**

CPU2006 license: 3175

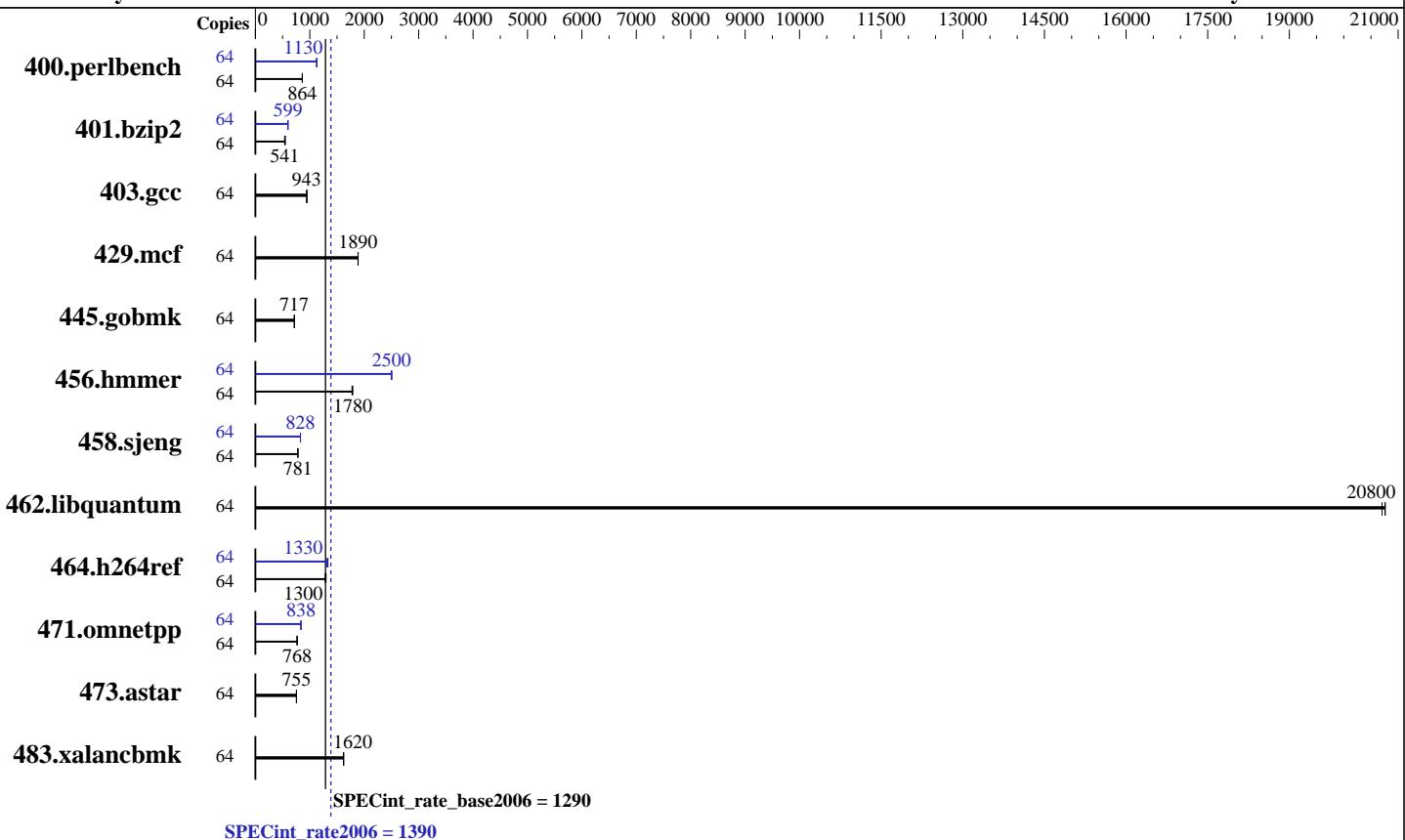
**Test date:** Jul-2017

**Test sponsor:** Huawei

**Hardware Availability:** Sep-2017

**Tested by:** Huawei

**Software Availability:** Nov-2016



## Hardware

CPU Name: Intel Xeon Platinum 8153  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 22 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
 Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Compiler: 3.10.0-514.el7.x86\_64 C/C++: Version 17.0.1.132 of Intel C/C++ Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	727	860	724	864	<b>724</b>	<b>864</b>	64	<b>555</b>	<b>1130</b>	555	1130	554	1130
401.bzip2	64	<b>1141</b>	<b>541</b>	1121	551	1147	538	64	<b>1032</b>	<b>599</b>	1030	599	1032	599
403.gcc	64	<b>546</b>	<b>943</b>	544	947	547	943	64	<b>546</b>	<b>943</b>	544	947	547	943
429.mcf	64	<b>309</b>	<b>1890</b>	310	1890	309	1890	64	<b>309</b>	<b>1890</b>	310	1890	309	1890
445.gobmk	64	936	717	<b>936</b>	<b>717</b>	938	716	64	936	717	<b>936</b>	<b>717</b>	938	716
456.hmmer	64	336	1780	334	1790	<b>335</b>	<b>1780</b>	64	238	2510	239	2500	<b>239</b>	<b>2500</b>
458.sjeng	64	<b>991</b>	<b>781</b>	994	779	990	782	64	<b>935</b>	<b>828</b>	933	830	936	827
462.libquantum	64	63.9	20800	<b>63.9</b>	<b>20800</b>	64.0	20700	64	63.9	20800	<b>63.9</b>	<b>20800</b>	64.0	20700
464.h264ref	64	1090	1300	<b>1093</b>	<b>1300</b>	1105	1280	64	1080	1310	1063	1330	<b>1063</b>	<b>1330</b>
471.omnetpp	64	<b>521</b>	<b>768</b>	521	767	521	768	64	479	835	<b>478</b>	<b>838</b>	477	838
473.astar	64	<b>595</b>	<b>755</b>	595	755	595	755	64	<b>595</b>	<b>755</b>	595	755	<b>595</b>	755
483.xalancbmk	64	272	1620	273	1620	<b>273</b>	<b>1620</b>	64	272	1620	273	1620	<b>273</b>	<b>1620</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set SNC to Enable

Set IMC Interleaving to 1 way

Set Patrol Scrub to Disable

Sysinfo program /spec17/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on localhost.localdomain Fri Jul 14 17:33:30 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz

2 "physical id"s (chips)

64 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 16
siblings   : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 kB
```

```
From /proc/meminfo
MemTotal:      790481628 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.3 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.3"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 14 17:31
```

```
SPEC is set to: /spec17
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   898G  17G  882G   2% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS INSYDE Corp. 0.20 07/14/2017
Memory:
 24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec17/lib/ia32:/spec17/lib/intel64:/spec17/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

## Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hammer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
                   -qopt-mem-layout-trans=3

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32  
                   -qopt-mem-layout-trans=3

458 sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -unroll14 -auto-ilp32  
                   -qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -unroll12 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2)  
                   -qopt-ra-region-strategy=block  
                   -qopt-mem-layout-trans=3 -Wl,-z,muldefs  
                   -L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>

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