



SPEC[®] CFP2006 Result

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Supermicro

(Test Sponsor: Advanced Micro Devices)

Supermicro AS-1123US-TR4,
AMD EPYC 7601

SPECfp[®]_rate2006 = 1840

SPECfp_rate_base2006 = 1660

CPU2006 license: 49

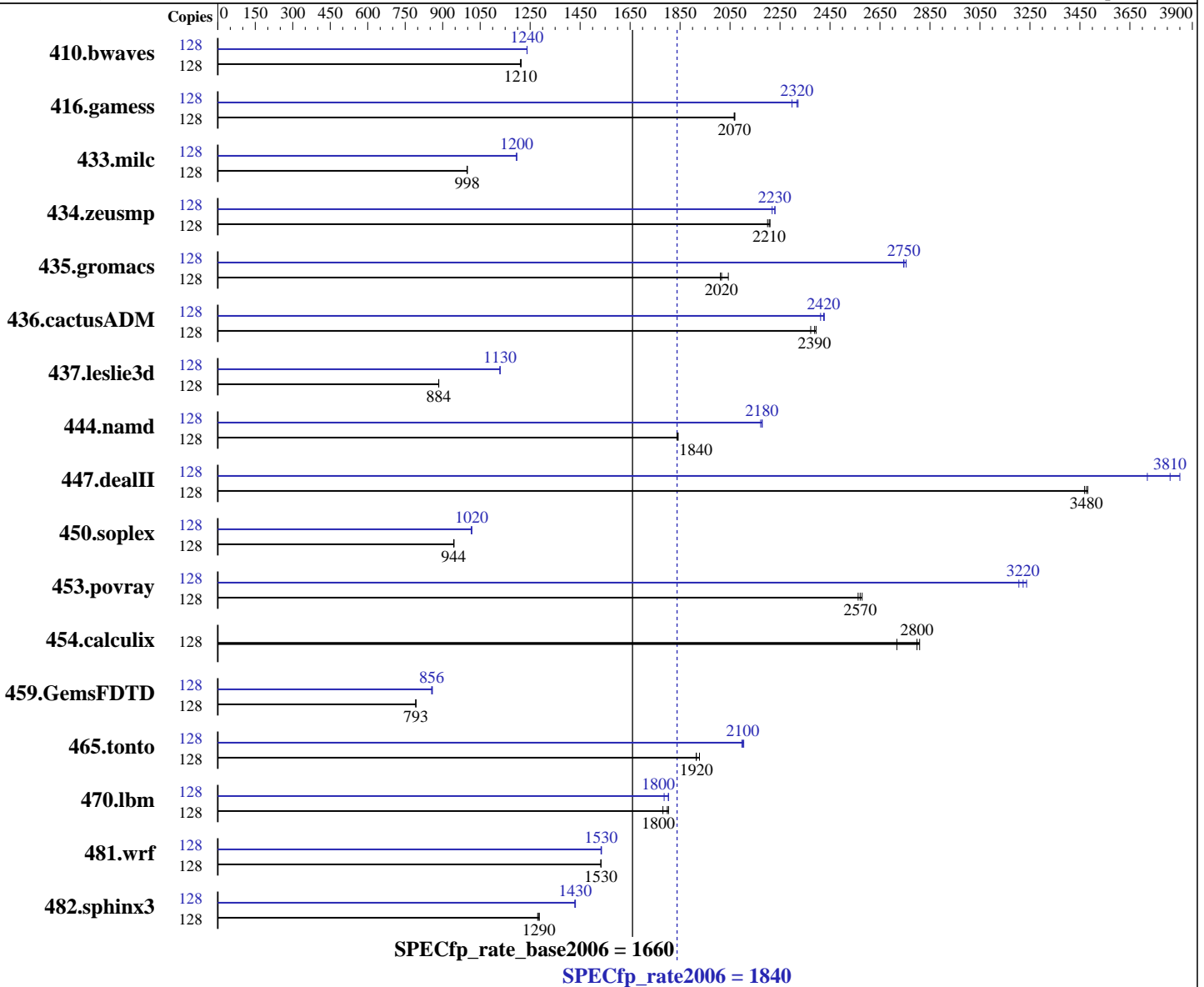
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: May-2017

Hardware Availability: Jun-2017

Software Availability: Apr-2016



Hardware

CPU Name: AMD EPYC 7601
 CPU Characteristics: AMD Turbo CORE technology up to 3.20 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 64 cores, 2 chips, 32 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 32 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

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Software

Operating System: Ubuntu 16.04 LTS,
 Kernel 4.4.0-71.generic
 Compiler: C/C++/Fortran: Version 4.5.2.1 of x86 Open64
 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: 64 MB I+D on chip per chip, 8 MB shared / 4 cores
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2667V-R, running at 2400)
Disk Subsystem: 1 x 500 GB SSD
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	128	1432	1210	1435	1210	1435	1210	128	1406	1240	1405	1240	1406	1240
416.gamess	128	1212	2070	1213	2070	1211	2070	128	1079	2320	1091	2300	1081	2320
433.milc	128	1177	998	1177	998	1178	998	128	982	1200	982	1200	983	1200
434.zeusmp	128	527	2210	528	2210	529	2200	128	523	2230	525	2220	522	2230
435.gromacs	128	447	2040	454	2010	453	2020	128	332	2750	333	2750	333	2740
436.cactusADM	128	639	2390	645	2370	640	2390	128	631	2420	630	2430	634	2410
437.leslie3d	128	1360	885	1361	884	1361	884	128	1065	1130	1065	1130	1065	1130
444.namd	128	557	1840	557	1840	559	1840	128	471	2180	473	2170	471	2180
447.dealII	128	422	3470	421	3480	421	3480	128	384	3810	380	3850	394	3720
450.soplex	128	1130	944	1129	945	1130	944	128	1052	1010	1051	1020	1050	1020
453.povray	128	264	2580	265	2570	266	2560	128	212	3210	211	3220	210	3240
454.calculix	128	389	2720	376	2810	377	2800	128	389	2720	376	2810	377	2800
459.GemsFDTD	128	1713	793	1714	792	1713	793	128	1581	859	1586	856	1586	856
465.tonto	128	658	1920	653	1930	658	1910	128	599	2100	601	2100	598	2100
470.lbm	128	987	1780	975	1800	978	1800	128	975	1800	975	1800	985	1790
481.wrf	128	933	1530	932	1530	932	1530	128	932	1530	931	1540	932	1530
482.sphinx3	128	1939	1290	1941	1290	1949	1280	128	1743	1430	1746	1430	1743	1430

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory

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Operating System Notes (Continued)

sync then drop_caches=3 to reset caches before invoking runspec
Transparent huge pages were enabled for this run (OS default)

Platform Notes

The Linux run level was 3; sysinfo run-level is incorrect.
The dmidecode memory speed information is incorrect.

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/work/cpu2006/amd1603-rate-libs-revA/32:/root/work/cpu2006/amd1603-rate-libs-revA/64"

The binaries were built with the AMD supported x86 Open64 Compiler Suite,
which is only available from AMD at

<http://developer.amd.com/tools-and-sdks/cpu-development/x86-open64-compiler-suite/>

Binaries were compiled on a system with 2x AMD Opteron 6378 chips + 128GB Memory using RHEL 6.3

Submitted_by: "Smith, Van" <Van.Smith@amd.com>

Submitted: Mon Jun 5 12:36:13 EDT 2017

Submission: cpu2006-20170529-47126.sub

Submitted_by: "Smith, Van" <Van.Smith@amd.com>

Submitted: Thu Jun 8 15:42:49 EDT 2017

Submission: cpu2006-20170529-47126.sub

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore

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Base Portability Flags (Continued)

```

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
        -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm
-WB, -Wl, -z,muldefs

```

C++ benchmarks:

```

-Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on
-HP:bd=2m:heap=2m -D__OPEN64_FAST_SET -march=bdver2 -mno-fma4
-mno-xop -mno-tbm -WB, -Wl, -z,muldefs

```

Fortran benchmarks:

```

-Ofast -LNO:blocking=off -LNO:simd_peel_align=on -OPT:rsqrt=2
-OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso -march=bdver1 -mno-fma4
-mno-xop -mno-tbm -WB, -Wl, -z,muldefs

```

Benchmarks using both Fortran and C:

```

-Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000
-IPA:small_pu=100 -mso -march=bdver1 -mno-fma4 -mno-xop -mno-tbm
-WB, -Wl, -z,muldefs -LNO:blocking=off -LNO:simd_peel_align=on
-OPT:rsqrt=2 -OPT:unroll_size=256

```

Peak Compiler Invocation

C benchmarks:

openc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -Ofast -CG:movnti=1 -CG:locs_best=on -HP:bdt=2m:heap=2m
-IPA:plimit=7000 -IPA:callee_limit=1200
-OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso
-march=bdver1 -mno-fma4

```

```

470.lbm: -Ofast -CG:cmp_peep=on -OPT:keep_ext=on -HP:bdt=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -march=bdver1 -mno-fma4
-mso

```

```

482.sphinx3: -Ofast -m32 -IPA:plimit=1000 -OPT:malloc_alg=2
-CG:cmp_peep=on -CG:p2align=0 -CG:load_exe=1 -CG:dsched=on
-INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch_ahead=4
-mso -march=bdver2 -WB, -mno-fma4 -mno-tbm -mno-xop

```

C++ benchmarks:

```

444.namd: -Ofast -IPA:plimit=3000 -LNO:ignore_feedback=off
-CG:local_sched_alg=0 -CG:load_exe=0 -OPT:unroll_size=256
-fno-exceptions -HP:bdt=2m:heap=2m -LNO:if_select_conv=1
-OPT:alias=disjoint -LNO:psimd_iso_unroll=ON -march=bdver2
-mno-fma4 -WB, -mno-xop -mno-tbm

```

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Peak Optimization Flags (Continued)

447.dealIII: -Ofast -D__OPEN64_FAST_SET -static -INLINE:aggressive=on
-LNO:opt=1 -LNO:simd=2 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -CG:movext_icmp=off -TENV:frame_pointer=off
-march=bdver1 -mno-fma4

450.soplex: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-LNO:ignore_feedback=off -INLINE:aggressive=on -OPT:RO=1
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -fno-exceptions -CG:p2align=0
-m32 -mno-fma4 -HP:bdt=2m:heap=2m -WOPT:sib=on
-march=bdver1

453.povray: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-CG:pre_local_sched=off -CG:p2align=0 -CG:p2align_split=on
-CG:dsched=on -INLINE:aggressive=on -HP:bd=2m:heap=2m
-OPT:transform=2 -OPT:alias=disjoint -WOPT:aggcm=0
-march=bdver2 -mno-fma4 -WB, -mno-xop -mno-tbm -Wl,
-z,muldefs

Fortran benchmarks:

410.bwaves: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:Ofast -OPT:treeheight=on -LNO:blocking=off
-LNO:ignore_feedback=off -LNO:fu=4 -LNO:loop_model_simd=on
-LNO:simd_rm_unity_remainder=on -WOPT:aggstr=0
-HP:bdt=2m:heap=2m -CG:cmp_peep=on -march=bdver2 -mno-fma4

416.gamess: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:fu=6 -LNO:blocking=0 -LNO:simd=2 -OPT:ro=3
-OPT:recip=on -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m
-WOPT:sib=on -march=bdver1 -mno-fma4

434.zeusmp: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:interchange=off -IPA:plimit=1500
-HP:bdt=2m:heap=2m -march=bdver2 -mno-fma4

437.leslie3d: -Ofast -CG:pre_minreg_level=2 -LNO:simd=0 -LNO:fusion=2
-HP:bdt=2m:heap=2m -mso -march=bdver1 -mno-fma4

459.GemsFDTD: -Ofast -IPA:plimit=1500 -OPT:unroll_size=1024
-OPT:unroll_times_max=16 -LNO:fission=2
-CG:local_sched_alg=2 -HP -march=bdver1 -mno-fma4

465.tonto: -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -CG:local_sched_alg=3 -IPA:plimit=525
-HP:bdt=2m:heap=2m -march=bdver2 -WB, -mno-fma4 -mno-tbm
-mno-xop

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m
-CG:local_sched_alg=2 -CG:load_exe=3 -GRA:unspill=on
-march=bdver2 -mno-fma4 -LNO:simd=3

436.cactusADM: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:blocking=off -LNO:prefetch=2 -LNO:pf2=0
-LNO:prefetch_ahead=4 -HP -CG:locs_shallow_depth=1
-CG:load_exe=0 -CG:dsched=on -WOPT:sib=on -march=bdver2
-mno-fma4

454.calculix: basepeak = yes

481.wrf: -Ofast -LNO:blocking=off -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -HP
-WOPT:sib=on -march=bdver1 -mno-fma4

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.xml>

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For other inquiries, please contact webmaster@spec.org.

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