



# SPEC® CFP2006 Result

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## NEC Corporation

### SPECfp®\_rate2006 = 214

### Express5800/T110i-S (Intel Xeon E3-1270 v6)

### SPECfp\_rate\_base2006 = 206

CPU2006 license: 9006

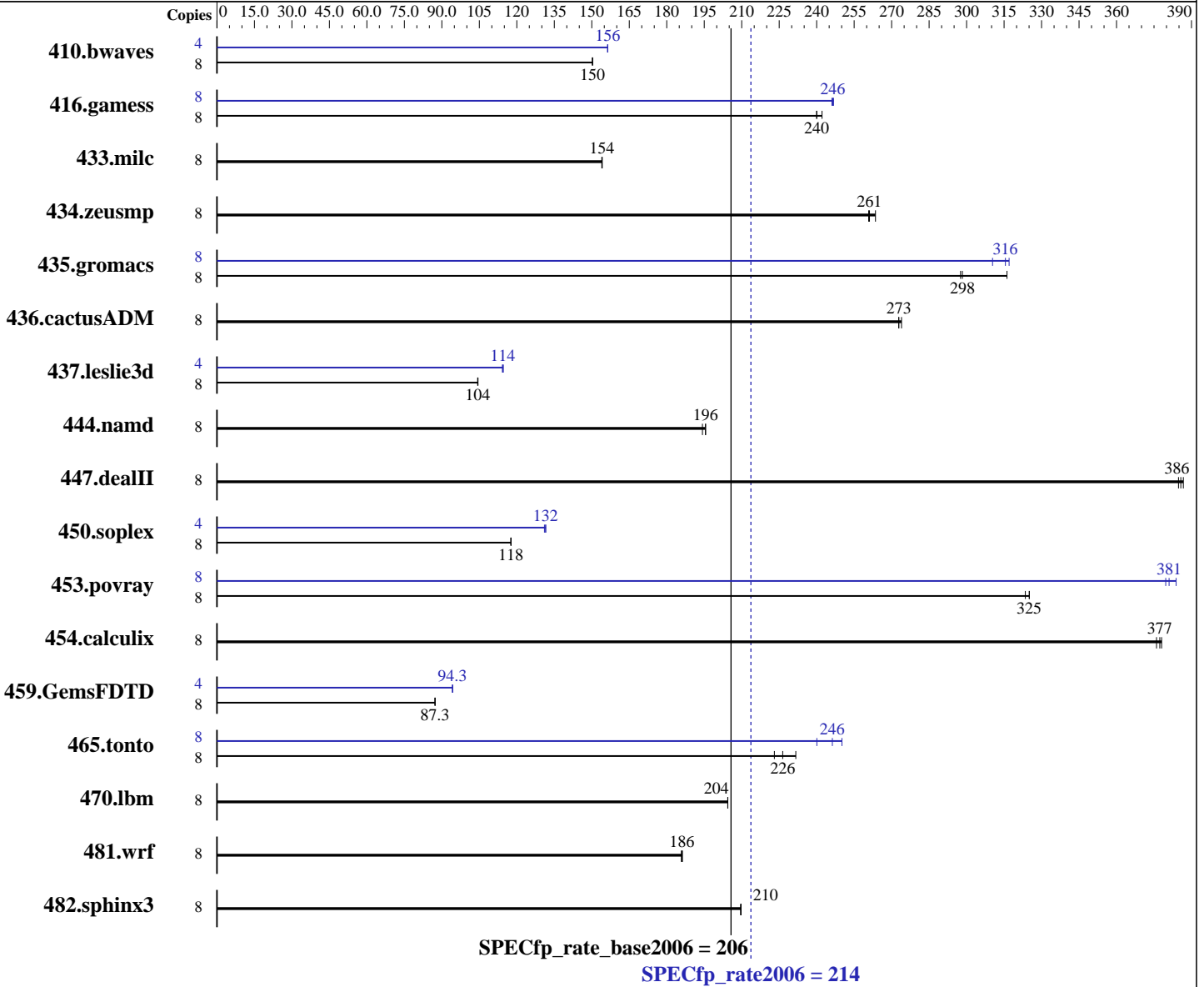
Test date: Mar-2017

Test sponsor: NEC Corporation

Hardware Availability: Apr-2017

Tested by: NEC Corporation

Software Availability: Jan-2017



#### Hardware

CPU Name: Intel Xeon E3-1270 v6  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz  
 CPU MHz: 3800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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#### Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Kernel 3.10.0-514.6.1.el7.x86\_64  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2400T-E)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<u>723</u>	<u>150</u>	723	150	723	150	4	348	156	<u>348</u>	<u>156</u>	348	156
416.gamess	8	647	242	<u>653</u>	<u>240</u>	653	240	8	<u>636</u>	<u>246</u>	636	246	635	247
433.milc	8	476	154	477	154	<u>477</u>	<u>154</u>	8	476	154	477	154	<u>477</u>	<u>154</u>
434.zeusmp	8	279	261	276	264	<u>279</u>	<u>261</u>	8	279	261	276	264	<u>279</u>	<u>261</u>
435.gromacs	8	<u>191</u>	<u>298</u>	181	316	192	298	8	<u>181</u>	<u>316</u>	180	317	184	310
436.cactusADM	8	349	274	350	273	<u>350</u>	<u>273</u>	8	349	274	350	273	<u>350</u>	<u>273</u>
437.leslie3d	8	<u>720</u>	<u>104</u>	720	104	721	104	4	<u>329</u>	<u>114</u>	328	115	329	114
444.namd	8	328	196	330	194	<u>328</u>	<u>196</u>	8	328	196	330	194	<u>328</u>	<u>196</u>
447.dealII	8	237	387	238	385	<u>237</u>	<u>386</u>	8	237	387	238	385	<u>237</u>	<u>386</u>
450.soplex	8	<u>567</u>	<u>118</u>	567	118	567	118	4	255	131	254	132	<u>254</u>	<u>132</u>
453.povray	8	<u>131</u>	<u>325</u>	131	325	132	324	8	112	380	<u>112</u>	<u>381</u>	111	384
454.calculix	8	<u>175</u>	<u>377</u>	176	376	175	378	8	<u>175</u>	<u>377</u>	176	376	175	378
459.GemsFDTD	8	973	87.3	972	87.3	<u>973</u>	<u>87.3</u>	4	450	94.2	<u>450</u>	<u>94.3</u>	450	94.3
465.tonto	8	353	223	340	232	<u>348</u>	<u>226</u>	8	<u>320</u>	<u>246</u>	328	240	315	250
470.lbm	8	538	204	<u>538</u>	<u>204</u>	538	204	8	538	204	<u>538</u>	<u>204</u>	538	204
481.wrf	8	480	186	481	186	<u>480</u>	<u>186</u>	8	480	186	481	186	<u>480</u>	<u>186</u>
482.sphinx3	8	<u>743</u>	<u>210</u>	744	210	743	210	8	<u>743</u>	<u>210</u>	744	210	743	210

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Power Management Policy: Custom  
Energy Performance: Performance

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## Platform Notes (Continued)

DCU Streamer Prefetcher: Disabled

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled by default

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

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## Peak Portability Flags (Continued)

459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-malloc-options=3  
 -qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
 -qopt-malloc-options=3

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110i-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110i-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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