



# SPEC<sup>®</sup> CFP2006 Result

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## Cisco Systems

Cisco UCS B460 M4 (Intel Xeon CPU E7-8891 v4 2.80 GHz)

SPECfp<sup>®</sup>\_rate2006 = 1700

SPECfp\_rate\_base2006 = 1660

CPU2006 license: 9019

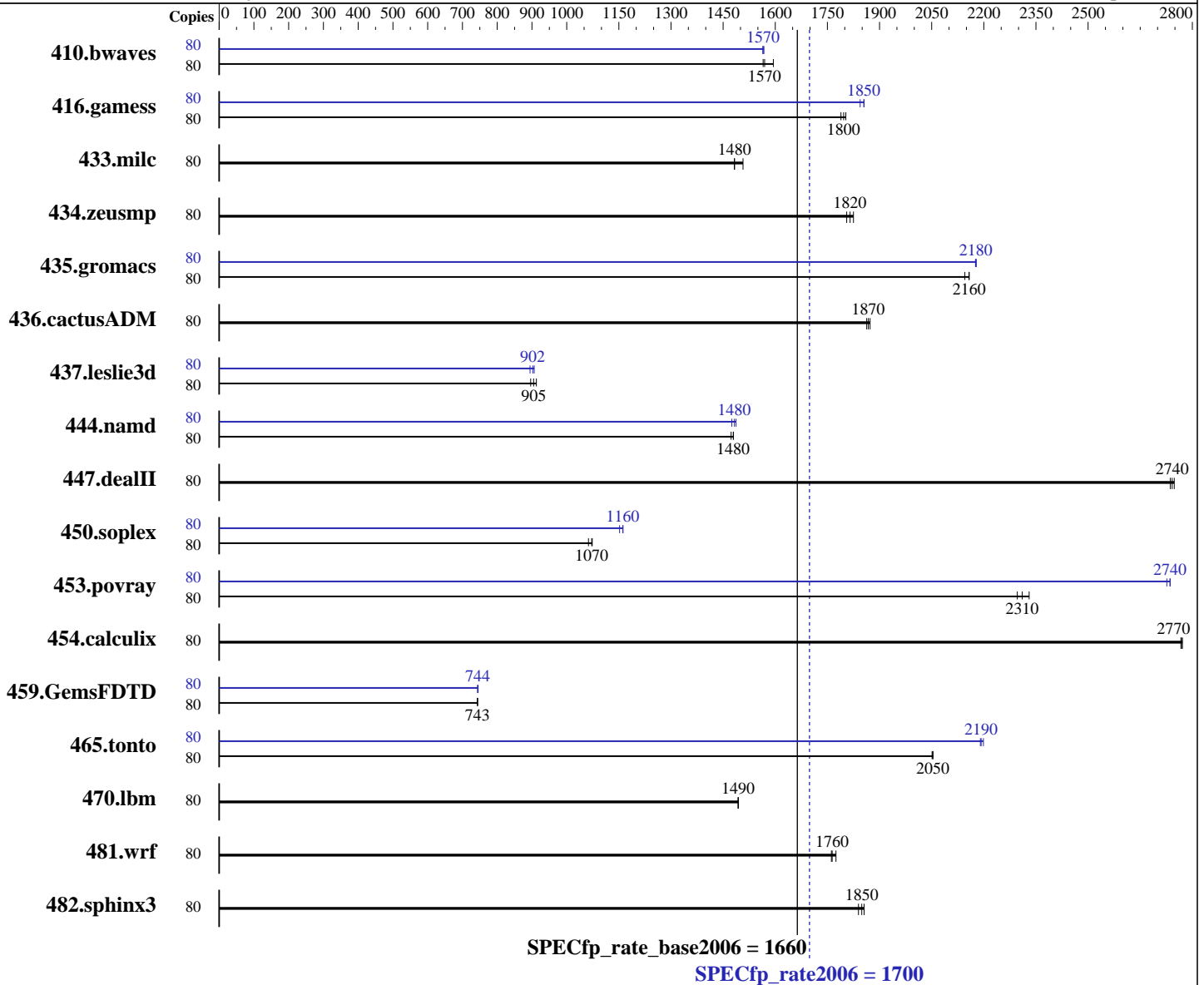
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Apr-2017

Hardware Availability: Apr-2016

Software Availability: Sep-2016



### Hardware

CPU Name: Intel Xeon E7-8891 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64) 3.12.49-11-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 60 MB I+D on chip per chip  
Other Cache: None  
Memory: 1 TB (32 x 32 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
Disk Subsystem: 1 x 400 GB SAS SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	695	1570	682	1590	<b>693</b>	<b>1570</b>	80	695	1560	<b>695</b>	<b>1570</b>	694	1570
416.gamess	80	<b>872</b>	<b>1800</b>	869	1800	876	1790	80	849	1840	844	1860	<b>845</b>	<b>1850</b>
433.milc	80	<b>495</b>	<b>1480</b>	487	1510	495	1480	80	<b>495</b>	<b>1480</b>	487	1510	495	1480
434.zeusmp	80	399	1830	403	1810	<b>401</b>	<b>1820</b>	80	399	1830	403	1810	<b>401</b>	<b>1820</b>
435.gromacs	80	<b>265</b>	<b>2160</b>	266	2150	265	2160	80	262	2180	<b>262</b>	<b>2180</b>	262	2180
436.cactusADM	80	<b>512</b>	<b>1870</b>	510	1870	513	1860	80	<b>512</b>	<b>1870</b>	510	1870	513	1860
437.leslie3d	80	<b>831</b>	<b>905</b>	839	896	824	913	80	830	906	841	894	<b>833</b>	<b>902</b>
444.namd	80	<b>434</b>	<b>1480</b>	434	1480	436	1470	80	435	1480	431	1490	<b>433</b>	<b>1480</b>
447.dealII	80	334	2740	<b>334</b>	<b>2740</b>	333	2750	80	334	2740	<b>334</b>	<b>2740</b>	333	2750
450.soplex	80	622	1070	<b>622</b>	<b>1070</b>	628	1060	80	579	1150	<b>574</b>	<b>1160</b>	574	1160
453.povray	80	185	2300	<b>184</b>	<b>2310</b>	183	2330	80	156	2730	<b>156</b>	<b>2740</b>	156	2740
454.calculix	80	<b>238</b>	<b>2770</b>	238	2770	239	2770	80	<b>238</b>	<b>2770</b>	238	2770	239	2770
459.GemsFDTD	80	1142	744	<b>1142</b>	<b>743</b>	1142	743	80	<b>1141</b>	<b>744</b>	1141	744	1139	745
465.tonto	80	<b>383</b>	<b>2050</b>	384	2050	383	2050	80	<b>359</b>	<b>2190</b>	358	2200	359	2190
470.lbm	80	736	1490	<b>736</b>	<b>1490</b>	736	1490	80	736	1490	<b>736</b>	<b>1490</b>	736	1490
481.wrf	80	504	1770	<b>507</b>	<b>1760</b>	507	1760	80	504	1770	<b>507</b>	<b>1760</b>	507	1760
482.sphinx3	80	<b>843</b>	<b>1850</b>	840	1860	848	1840	80	<b>843</b>	<b>1850</b>	840	1860	848	1840

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Test date: Apr-2017

Hardware Availability: Apr-2016

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## Platform Notes

### BIOS Settings:

CPU performance set to Enterprise  
 Power Technology set to Energy Efficient  
 Energy Performance set to Balanced Performance  
 Memory RAS configuration set to Maximum Performance  
 Memory Power Saving Mode set to Disabled  
 QPI Snoop Mode set to Cluster-on-Die  
 Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-3y2r Thu Apr 13 15:16:26 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E7-8891 v4 @ 2.80GHz
 4 "physical id"s (chips)
 80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 10
  siblings    : 20
  physical 0: cores 5 9 10 11 13 18 24 26 28 29
  physical 1: cores 5 9 10 11 13 18 24 26 28 29
  physical 2: cores 5 9 10 11 13 18 24 26 28 29
  physical 3: cores 5 9 10 11 13 18 24 26 28 29
cache size    : 30720 KB

```

### From /proc/meminfo

```

MemTotal:      1058506524 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

### From /etc/\*release\* /etc/\*version\*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

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### Platform Notes (Continued)

```
uname -a:
Linux linux-3y2r 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 13 04:25
```

```
SPEC is set to: /opt/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       xfs   373G  19G  354G   5% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Cisco Systems, Inc. EXM4.3.1.2c.0.080220161434 08/02/2016
Memory:
32x 0xCE00 M393A4K40BB0-CPB 32 GB 2 rank 2133 MHz, configured at 1600 MHz
64x NO DIMM NO DIMM 2400 MHz
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh10.2"

```
Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -fno-alias -auto-ilp32  
 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.20170404.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.20170404.xml>

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