



# SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

**SPECint®\_rate2006 = 281**

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

**SPECint\_rate\_base2006 = 270**

CPU2006 license: 19

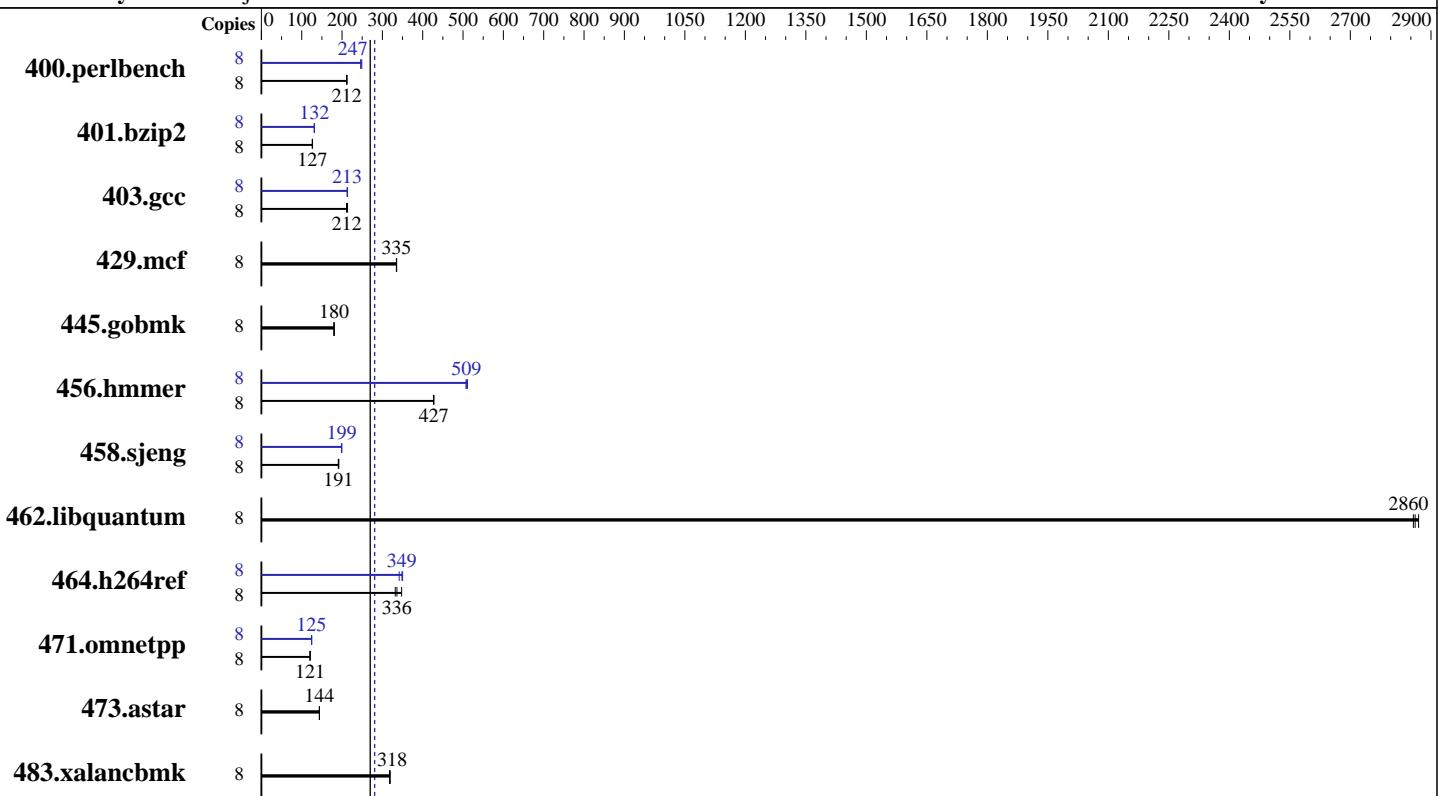
Test date: Mar-2017

Test sponsor: Fujitsu

Hardware Availability: May-2017

Tested by: Fujitsu

Software Availability: Nov-2016



## Hardware

CPU Name: Intel Xeon E3-1280 v6  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz  
 CPU MHz: 3900  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2400T-E)  
 Disk Subsystem: 1 x 1TB, SATA III, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64) 4.4.21-68-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

**SPECint\_rate2006 = 281**

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

**SPECint\_rate\_base2006 = 270**

CPU2006 license: 19

Test date: Mar-2017

Test sponsor: Fujitsu

Hardware Availability: May-2017

Tested by: Fujitsu

Software Availability: Nov-2016

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	369	212	369	212	<b><u>369</u></b>	<b><u>212</u></b>	8	<b><u>317</u></b>	<b><u>247</u></b>	317	246	314	249
401.bzip2	8	<b><u>610</u></b>	<b><u>127</u></b>	609	127	612	126	8	<b><u>587</u></b>	<b><u>132</u></b>	590	131	587	132
403.gcc	8	<b><u>304</u></b>	<b><u>212</u></b>	305	211	301	214	8	<b><u>302</u></b>	<b><u>213</u></b>	303	213	302	214
429.mcf	8	<b><u>218</u></b>	<b><u>335</u></b>	218	335	218	335	8	<b><u>218</u></b>	<b><u>335</u></b>	218	335	218	335
445.gobmk	8	<b><u>466</u></b>	<b><u>180</u></b>	466	180	465	180	8	<b><u>466</u></b>	<b><u>180</u></b>	466	180	465	180
456.hammer	8	175	427	<b><u>175</u></b>	<b><u>427</u></b>	175	427	8	146	511	<b><u>147</u></b>	<b><u>509</u></b>	147	507
458.sjeng	8	<b><u>507</u></b>	<b><u>191</u></b>	507	191	504	192	8	<b><u>486</u></b>	<b><u>199</u></b>	486	199	488	199
462.libquantum	8	57.8	2870	58.0	2860	<b><u>57.9</u></b>	<b><u>2860</u></b>	8	57.8	2870	58.0	2860	<b><u>57.9</u></b>	<b><u>2860</u></b>
464.h264ref	8	509	348	533	332	<b><u>527</u></b>	<b><u>336</u></b>	8	<b><u>508</u></b>	<b><u>349</u></b>	518	342	506	350
471.omnetpp	8	<b><u>414</u></b>	<b><u>121</u></b>	414	121	413	121	8	402	124	<b><u>402</u></b>	<b><u>125</u></b>	401	125
473.astar	8	<b><u>390</u></b>	<b><u>144</u></b>	390	144	391	144	8	<b><u>390</u></b>	<b><u>144</u></b>	390	144	391	144
483.xalancbmk	8	173	319	<b><u>173</u></b>	<b><u>318</u></b>	174	318	8	173	319	<b><u>173</u></b>	<b><u>318</u></b>	174	318

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with :
cpupower -c all frequency-set -g performance
cpupower idle-set -d 2
cpupower idle-set -d 3
cpupower idle-set -d 4
echo always > /sys/kernel/mm/transparent_hugepage/enabled
echo 1 > /proc/sys/vm/drop_caches
echo 1000000000 > /proc/sys/kernel/sched_min_granularity_ns
echo 1500000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
```

## Platform Notes

Sysinfo program /home/benchmark/speccpu-20160922-updated/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-lrfj Sat Mar 4 07:00:44 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

SPECint\_rate2006 = 281

SPECint\_rate\_base2006 = 270

CPU2006 license: 19

Test date: Mar-2017

Test sponsor: Fujitsu

Hardware Availability: May-2017

Tested by: Fujitsu

Software Availability: Nov-2016

## Platform Notes (Continued)

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E3-1280 v6 @ 3.90GHz
        1 "physical id"s (chips)
        8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 4
    siblings   : 8
    physical 0: cores 0 1 2 3
    cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:       65834268 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-1rfj 4.4.21-68-default #1 SMP Tue Oct 18 18:19:37 UTC 2016
(63cf368) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 3 16:45
```

```
SPEC is set to: /home/benchmark/speccpu-20160922-updated
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        xfs   890G  16G  874G   2%  /home
```

```
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.0.0 for D3373-B1x
02/20/2017

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

SPECint\_rate2006 = 281

SPECint\_rate\_base2006 = 270

Test date: Mar-2017

Hardware Availability: May-2017

Software Availability: Nov-2016

## Platform Notes (Continued)

Memory:

4x Samsung M391A2K43BB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/benchmark/speccpu-20160922-updated/libs/32:/home/benchmark/speccpu-20160922-updated/libs/64:/home/benchmark/speccpu-20160922-updated/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

This result was measured on the PRIMERGY TX1320 M3. The PRIMERGY TX1320 M3  
and the PRIMERGY TX1330 M3 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hammer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

SPECint\_rate2006 = 281

SPECint\_rate\_base2006 = 270

Test date: Mar-2017

Hardware Availability: May-2017

Software Availability: Nov-2016

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
403.gcc: -D_FILE_OFFSET_BITS=64
```

```
429.mcf: -D_FILE_OFFSET_BITS=64
```

```
445.gobmk: -D_FILE_OFFSET_BITS=64
```

```
456.hmmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

```
464.h264ref: -D_FILE_OFFSET_BITS=64
```

```
471.omnetpp: -D_FILE_OFFSET_BITS=64
```

```
473.astar: -D_FILE_OFFSET_BITS=64
```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

SPECint\_rate2006 = 281

SPECint\_rate\_base2006 = 270

Test date: Mar-2017

Hardware Availability: May-2017

Software Availability: Nov-2016

## Peak Portability Flags (Continued)

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto-ilp32  
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)  
-qopt-ra-region-strategy=block  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/sh10.2 -lsmartheap

473.astar: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX1330 M3, Intel Xeon E3-1280 v6, 3.9GHz

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

SPECint\_rate2006 = 281

SPECint\_rate\_base2006 = 270

Test date: Mar-2017

Hardware Availability: May-2017

Software Availability: Nov-2016

## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevE.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Mar 29 16:29:05 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 29 March 2017.