



# SPEC<sup>®</sup> CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL580 Gen 9  
(2.20GHz Intel Xeon E7-8880 v4)

SPECfp<sup>®</sup>\_rate2006 = 2320

SPECfp\_rate\_base2006 = 2250

CPU2006 license: 3

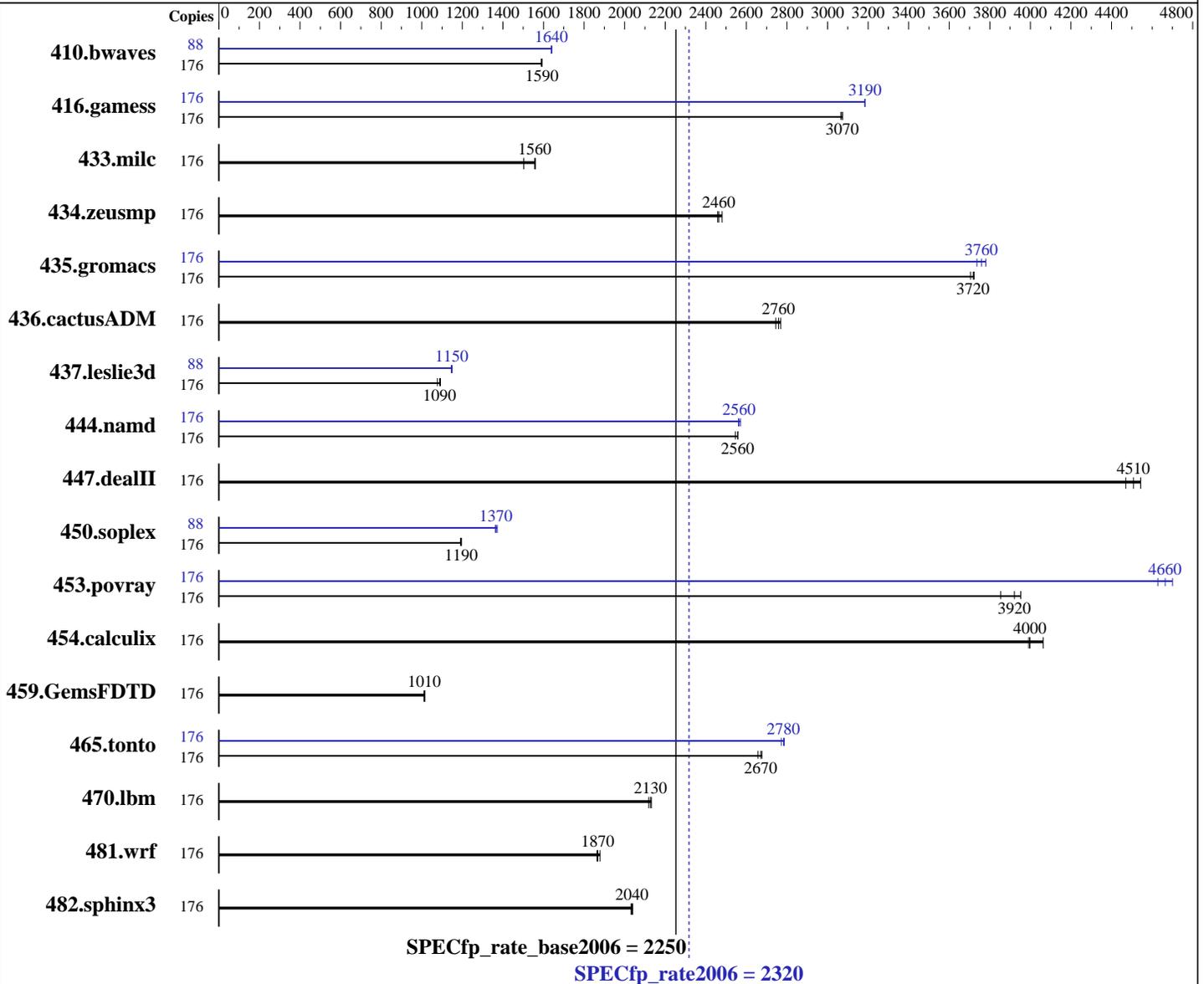
Test sponsor: HPE

Tested by: HPE

Test date: Jan-2017

Hardware Availability: May-2016

Software Availability: Nov-2016



## Hardware

CPU Name: Intel Xeon E7-8880 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 88 cores, 4 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

## Software

Operating System: SUSE Linux Enterprise Server 12 SP1  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 55 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)  
Disk Subsystem: 2 x 400 GB SAS SSD RAID 1  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	176	1501	1590	1506	1590	<b>1502</b>	<b>1590</b>	88	<b>730</b>	<b>1640</b>	729	1640	730	1640
416.gamess	176	1124	3070	1121	3070	<b>1121</b>	<b>3070</b>	176	<b>1082</b>	<b>3190</b>	1083	3180	1082	3190
433.milc	176	1075	1500	<b>1038</b>	<b>1560</b>	1035	1560	176	1075	1500	<b>1038</b>	<b>1560</b>	1035	1560
434.zeusmp	176	646	2480	<b>650</b>	<b>2460</b>	652	2460	176	646	2480	<b>650</b>	<b>2460</b>	652	2460
435.gromacs	176	338	3720	<b>338</b>	<b>3720</b>	339	3700	176	332	3780	336	3740	<b>334</b>	<b>3760</b>
436.cactusADM	176	760	2770	<b>762</b>	<b>2760</b>	766	2740	176	760	2770	<b>762</b>	<b>2760</b>	766	2740
437.leslie3d	176	1515	1090	<b>1520</b>	<b>1090</b>	1536	1080	88	722	1150	719	1150	<b>721</b>	<b>1150</b>
444.namd	176	<b>552</b>	<b>2560</b>	552	2560	554	2550	176	549	2570	551	2560	<b>551</b>	<b>2560</b>
447.dealII	176	450	4470	<b>447</b>	<b>4510</b>	443	4540	176	450	4470	<b>447</b>	<b>4510</b>	443	4540
450.soplex	176	<b>1229</b>	<b>1190</b>	1227	1200	1232	1190	88	535	1370	539	1360	<b>537</b>	<b>1370</b>
453.povray	176	243	3850	237	3950	<b>239</b>	<b>3920</b>	176	202	4630	199	4700	<b>201</b>	<b>4660</b>
454.calculix	176	<b>363</b>	<b>4000</b>	357	4060	364	3990	176	<b>363</b>	<b>4000</b>	357	4060	364	3990
459.GemsFDTD	176	1839	1020	<b>1843</b>	<b>1010</b>	1847	1010	176	1839	1020	<b>1843</b>	<b>1010</b>	1847	1010
465.tonto	176	652	2660	647	2680	<b>648</b>	<b>2670</b>	176	625	2770	621	2790	<b>622</b>	<b>2780</b>
470.lbm	176	1134	2130	1141	2120	<b>1136</b>	<b>2130</b>	176	1134	2130	1141	2120	<b>1136</b>	<b>2130</b>
481.wrf	176	1046	1880	<b>1053</b>	<b>1870</b>	1055	1860	176	1046	1880	<b>1053</b>	<b>1870</b>	1055	1860
482.sphinx3	176	1689	2030	1682	2040	<b>1685</b>	<b>2040</b>	176	1689	2030	1682	2040	<b>1685</b>	<b>2040</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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Tested by: HPE

Test date: Jan-2017

Hardware Availability: May-2016

Software Availability: Nov-2016

## Platform Notes

BIOS Configuration  
 Power Profile set to Custom  
 Power Regulator set to Static High Performance Mode  
 Minimum Processor Idle Power Core C-State set to C1E State  
 Minimum Processor Idle Power Package C-State set to No Package State  
 Energy/Performance Bias set to Maximum Performance  
 Collaborative Power Control set to Disabled  
 QPI Snoop Configuration set to Cluster on Die  
 Thermal Configuration set to Maximum Cooling  
 Processor Power and Utilization Monitoring set to Disabled  
 Memory Refresh set to 1x Refresh  
 Sysinfo program /home/cpu2006/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-9w8f Fri Jan 20 02:45:00 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz
 4 "physical id"s (chips)
176 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 22
  siblings  : 44
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
  physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
  physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
cache size : 28160 KB
```

```
From /proc/meminfo
MemTotal:      529305692 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
Continued on next page
```



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## Platform Notes (Continued)

```

release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux linux-9w8f 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 19 13:33

SPEC is set to: /home/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   331G  155G  176G  47% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 05/16/2016
Memory:
64x UNKNOWN NOT AVAILABLE
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 512 GB and the dmidecode description should have one line reading as:
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 1600 MHz

```

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3



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## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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