SPEC® CFP2006 Result
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Huawei

Huawei XH321 V3 (Intel Xeon E5-2699 v3)

$SPECfp®\_rate2006 = 947$
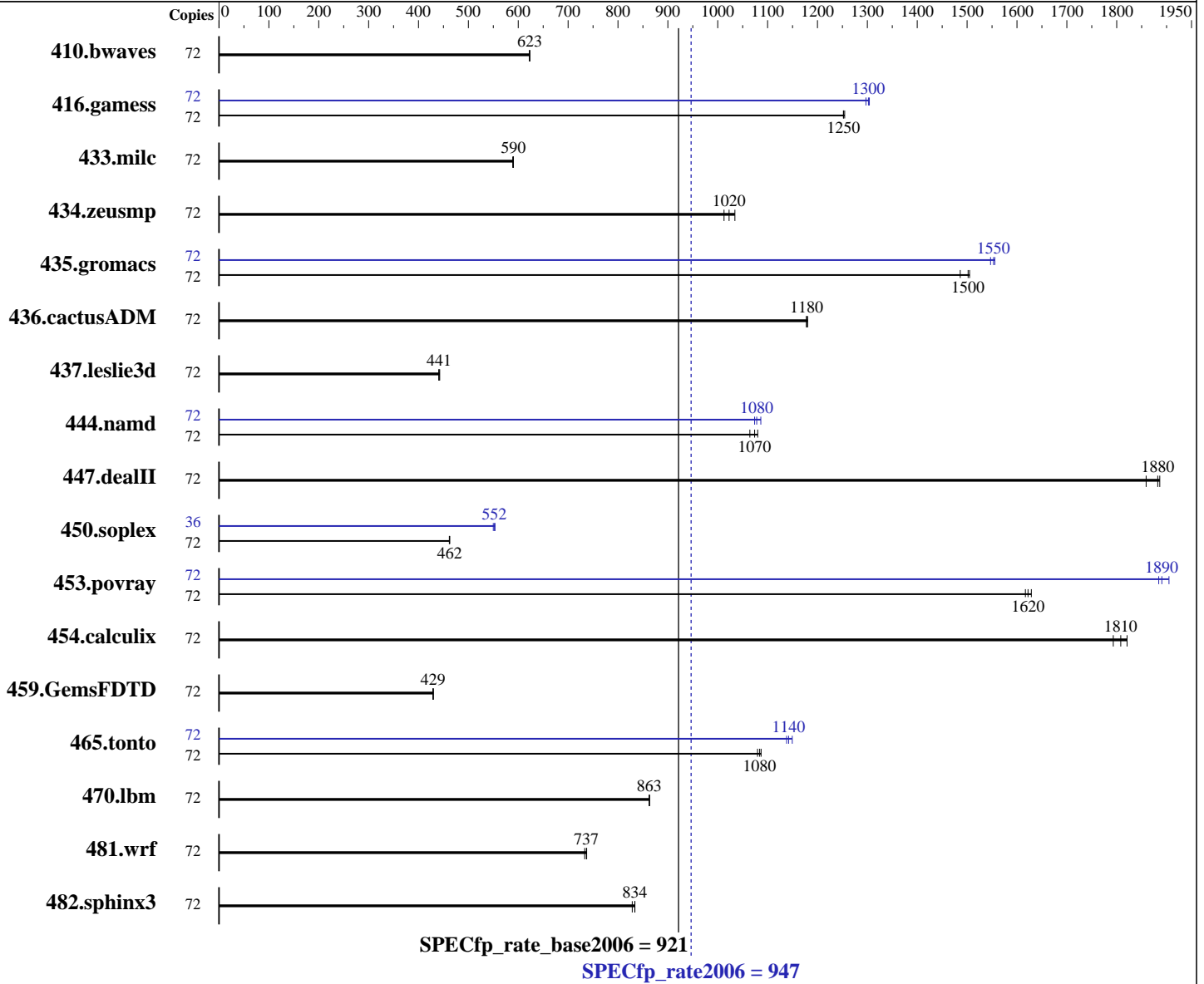
$SPECfp\_rate\_base2006 = 921$

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Dec-2016
Hardware Availability: Nov-2016
Software Availability: Dec-2015

Benchmark	Copies	Result
410.bwaves	72	623
416.gamess	72	1300
416.gamess	72	1250
433.milc	72	590
434.zeusmp	72	1020
435.gromacs	72	1550
435.gromacs	72	1500
436.cactusADM	72	1180
437.leslie3d	72	441
444.namd	72	1080
444.namd	72	1070
447.dealII	72	1880
450.soplex	36	552
450.soplex	72	462
453.povray	72	1890
453.povray	72	1620
454.calculix	72	1810
459.GemsFDTD	72	429
465.tonto	72	1140
465.tonto	72	1080
470.lbm	72	863
481.wrf	72	737
482.sphinx3	72	834

SPECfp_rate_base2006 = 921
SPECfp_rate2006 = 947

Hardware

CPU Name: Intel Xeon E5-2699 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1
3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache:	45 MB I+D on chip per chip	Base Pointers:	32/64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)	Other Software:	None
Disk Subsystem:	1 x 1000 GB SATA, 7200 RPM		
Other Hardware:	None		

Results Table

Benchmark		Base							Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	72	**_1570_**	**_623_**	1572	622	1570	623	72	**_1570_**	**_623_**	1572	622	1570	623
416.gamess	72	1124	1250	1126	1250	**_1126_**	**_1250_**	72	1081	1300	1087	1300	**_1083_**	**_1300_**
433.milc	72	1122	589	1120	590	**_1120_**	**_590_**	72	1122	589	1120	590	**_1120_**	**_590_**
434.zeusmp	72	647	1010	633	1030	**_641_**	**_1020_**	72	647	1010	633	1030	**_641_**	**_1020_**
435.gromacs	72	342	1500	346	1490	**_342_**	**_1500_**	72	330	1560	**_331_**	**_1550_**	332	1550
436.cactusADM	72	**_730_**	**_1180_**	729	1180	730	1180	72	**_730_**	**_1180_**	729	1180	730	1180
437.leslie3d	72	1536	441	**_1533_**	**_441_**	1529	443	72	1536	441	**_1533_**	**_441_**	1529	443
444.namd	72	535	1080	**_538_**	**_1070_**	542	1060	72	532	1090	**_536_**	**_1080_**	538	1070
447.dealII	72	**_438_**	**_1880_**	437	1890	443	1860	72	**_438_**	**_1880_**	437	1890	443	1860
450.soplex	72	1298	463	1300	462	**_1299_**	**_462_**	36	**_544_**	**_552_**	546	550	542	553
453.povray	72	235	1630	**_236_**	**_1620_**	237	1620	72	201	1900	203	1880	**_203_**	**_1890_**
454.calculix	72	326	1820	**_329_**	**_1810_**	331	1790	72	326	1820	**_329_**	**_1810_**	331	1790
459.GemsFDTD	72	1777	430	1780	429	**_1780_**	**_429_**	72	1777	430	1780	429	**_1780_**	**_429_**
465.tonto	72	652	1090	656	1080	**_654_**	**_1080_**	72	616	1150	**_620_**	**_1140_**	622	1140
470.lbm	72	1147	863	1146	863	**_1147_**	**_863_**	72	1147	863	1146	863	**_1147_**	**_863_**
481.wrf	72	1091	737	1097	733	**_1092_**	**_737_**	72	1091	737	1097	733	**_1092_**	**_737_**
482.sphinx3	72	1694	829	**_1683_**	**_834_**	1683	834	72	1694	829	**_1683_**	**_834_**	1683	834

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode

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Platform Notes (Continued)

```
Set Patrol Scrub to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-6392 Fri Dec 23 03:15:13 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz
      2 "physical id"s (chips)
      72 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 18
      siblings  : 36
      physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
      physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
   cache size : 23040 KB

From /proc/meminfo
   MemTotal:       528840668 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-6392 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 22 15:00 last=5
```

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Platform Notes (Continued)

```
SPEC is set to: /spec16
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/md126p2   xfs   455G   22G  434G   5% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS Insyde Corp. 3.31 08/22/2016
   Memory:
   16x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc   -m64 ifort -m64
```

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Base Portability Flags

```
       410.bwaves: -DSPEC_CPU_LP64
       416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
       434.zeusmp: -DSPEC_CPU_LP64
      435.gromacs: -DSPEC_CPU_LP64  -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64  -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
       450.soplex: -DSPEC_CPU_LP64
       453.povray: -DSPEC_CPU_LP64
      454.calculix: -DSPEC_CPU_LP64  -nofor_main
     459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64
         470.lbm: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks (except as noted below):
```
icpc  -m64
```

```
 450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -D_FILE_OFFSET_BITS=64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
    433.milc: basepeak = yes

    470.lbm: basepeak = yes

  482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
   444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -fno-alias -auto-ilp32

   447.dealII: basepeak = yes
```

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Peak Optimization Flags (Continued)

```
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

  465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
            -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

    481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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