



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **101**

Huawei CH220 V3(Intel Xeon E5-2620 v4)

SPECfp\_base2006 = **95.9**

CPU2006 license: 3175

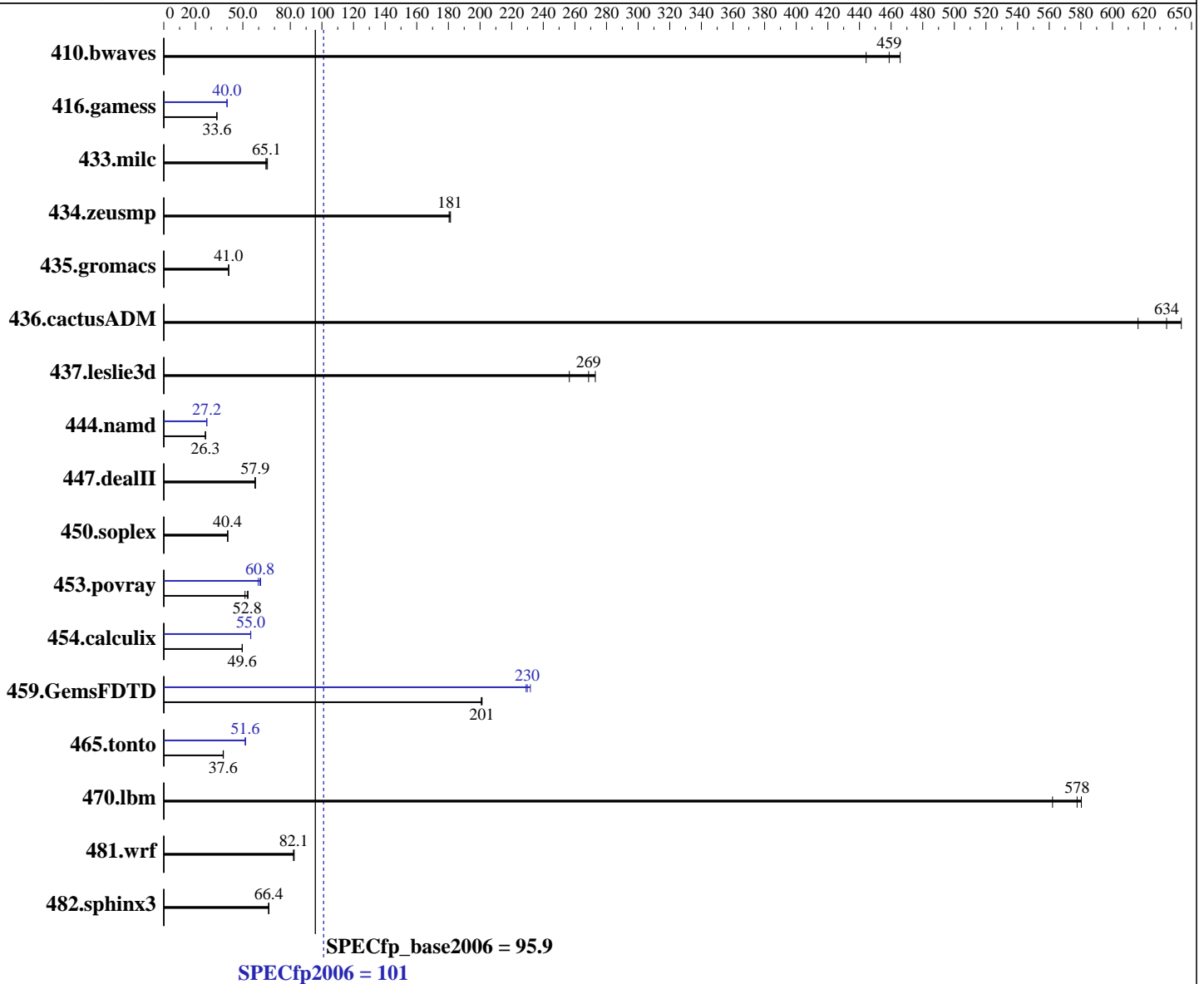
Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



**Hardware**

CPU Name: Intel Xeon E5-2620 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	29.2	466	<b>29.6</b>	<b>459</b>	30.6	444	29.2	466	<b>29.6</b>	<b>459</b>	30.6	444
416.gamess	585	33.5	<b>583</b>	<b>33.6</b>	583	33.6	489	40.0	<b>489</b>	<b>40.0</b>	490	40.0
433.milc	142	64.6	140	65.6	<b>141</b>	<b>65.1</b>	142	64.6	140	65.6	<b>141</b>	<b>65.1</b>
434.zeusmp	50.2	181	50.4	180	<b>50.3</b>	<b>181</b>	50.2	181	50.4	180	<b>50.3</b>	<b>181</b>
435.gromacs	174	41.0	<b>174</b>	<b>41.0</b>	174	41.0	174	41.0	<b>174</b>	<b>41.0</b>	174	41.0
436.cactusADM	<b>18.8</b>	<b>634</b>	19.4	616	18.6	644	<b>18.8</b>	<b>634</b>	19.4	616	18.6	644
437.leslie3d	34.4	273	<b>35.0</b>	<b>269</b>	36.6	256	34.4	273	<b>35.0</b>	<b>269</b>	36.6	256
444.namd	<b>304</b>	<b>26.3</b>	304	26.4	304	26.3	295	27.2	<b>295</b>	<b>27.2</b>	295	27.2
447.dealII	<b>198</b>	<b>57.9</b>	197	57.9	199	57.6	<b>198</b>	<b>57.9</b>	197	57.9	199	57.6
450.soplex	206	40.5	207	40.3	<b>207</b>	<b>40.4</b>	206	40.5	207	40.3	<b>207</b>	<b>40.4</b>
453.povray	104	51.4	99.8	53.3	<b>101</b>	<b>52.8</b>	89.1	59.7	86.9	61.2	<b>87.5</b>	<b>60.8</b>
454.calculix	166	49.7	166	49.6	<b>166</b>	<b>49.6</b>	150	55.0	<b>150</b>	<b>55.0</b>	150	54.9
459.GemsFDTD	52.7	201	<b>52.7</b>	<b>201</b>	52.9	201	<b>46.2</b>	<b>230</b>	46.3	229	45.8	232
465.tonto	262	37.5	261	37.7	<b>262</b>	<b>37.6</b>	191	51.5	191	51.6	<b>191</b>	<b>51.6</b>
470.lbm	<b>23.8</b>	<b>578</b>	24.4	562	23.7	580	<b>23.8</b>	<b>578</b>	24.4	562	23.7	580
481.wrf	<b>136</b>	<b>82.1</b>	135	82.5	136	82.1	<b>136</b>	<b>82.1</b>	135	82.5	136	82.1
482.sphinx3	<b>294</b>	<b>66.4</b>	293	66.5	294	66.3	<b>294</b>	<b>66.4</b>	293	66.5	294	66.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to HS mode  
 Set Patrol Scrub to Disable  
 Set Hyper-Threading to Disable  
 Sysinfo program /spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-j81m Fri Dec 2 05:46:12 2016

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores      : 8
  siblings       : 8
  physical 0:    cores 0 1 2 3 4 5 6 7
  physical 1:    cores 0 1 2 3 4 5 6 7
cache size      : 20480 KB

```

```

From /proc/meminfo
MemTotal:        528842892 kB
HugePages_Total: 0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-j81m 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 2 00:21

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  884G  46G  837G   6% /

```

Additional information from dmidecode:

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Micron 36ASF4G72PZ-2G3B1 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```



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Software Availability: Dec-2015

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Software Availability: Dec-2015

## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Wed Dec 28 10:51:53 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 27 December 2016.

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