



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = 108

Huawei XH321 V3 (Intel Xeon E5-2695 v4)

SPECfp\_base2006 = 102

CPU2006 license: 3175

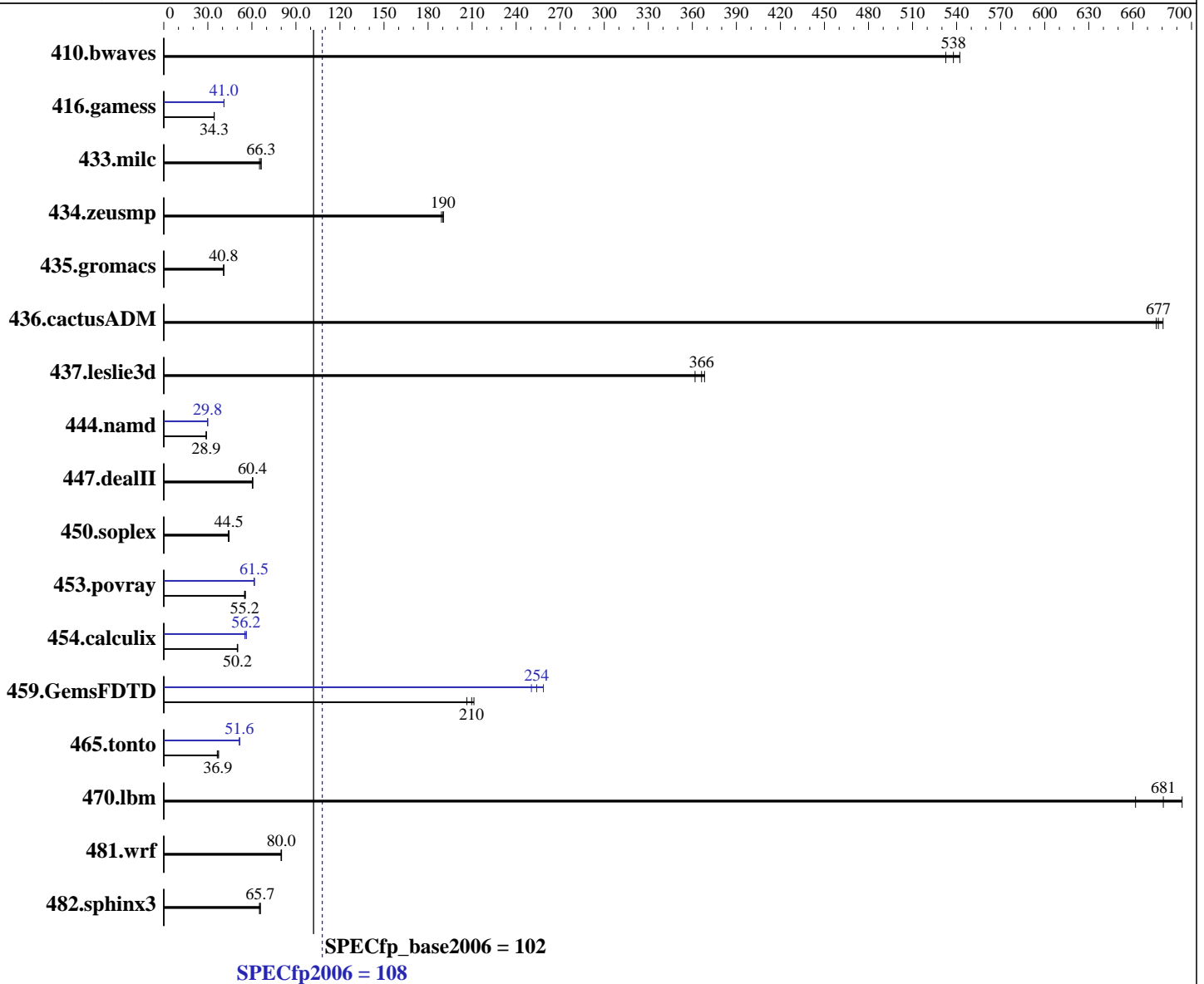
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Nov-2016

Software Availability: Dec-2015



**Hardware**

CPU Name: Intel Xeon E5-2695 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)  
 Disk Subsystem: 1 x 800 GB SATA SSD  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>25.3</u>	<b>538</b>	25.5	533	25.1	542	<u>25.3</u>	<b>538</b>	25.5	533	25.1	542
416.gamess	569	34.4	572	34.3	<b>571</b>	<b>34.3</b>	<u>477</u>	<b>41.0</b>	478	41.0	477	41.0
433.milc	141	65.2	138	66.3	<b>139</b>	<b>66.3</b>	141	65.2	138	66.3	<b>139</b>	<b>66.3</b>
434.zeusmp	48.1	189	<b>47.9</b>	<b>190</b>	47.8	190	48.1	189	<b>47.9</b>	<b>190</b>	47.8	190
435.gromacs	175	40.9	175	40.7	<b>175</b>	<b>40.8</b>	175	40.9	175	40.7	<b>175</b>	<b>40.8</b>
436.cactusADM	<b>17.6</b>	<b>677</b>	17.7	676	17.6	681	<b>17.6</b>	<b>677</b>	17.7	676	17.6	681
437.leslie3d	26.0	362	25.5	368	<b>25.7</b>	<b>366</b>	26.0	362	25.5	368	<b>25.7</b>	<b>366</b>
444.namd	277	28.9	<b>277</b>	<b>28.9</b>	277	29.0	268	29.9	<b>269</b>	<b>29.8</b>	269	29.8
447.dealII	189	60.7	190	60.3	<b>189</b>	<b>60.4</b>	189	60.7	190	60.3	<b>189</b>	<b>60.4</b>
450.soplex	<b>188</b>	<b>44.5</b>	187	44.5	190	43.9	<b>188</b>	<b>44.5</b>	187	44.5	190	43.9
453.povray	<b>96.4</b>	<b>55.2</b>	95.6	55.6	96.8	55.0	<b>86.5</b>	<b>61.5</b>	86.5	61.5	86.1	61.8
454.calculix	164	50.3	<b>164</b>	<b>50.2</b>	165	50.0	150	55.1	147	56.2	<b>147</b>	<b>56.2</b>
459.GemsFDTD	51.4	206	<b>50.6</b>	<b>210</b>	50.2	211	<b>41.8</b>	<b>254</b>	41.0	259	42.4	250
465.tonto	270	36.4	263	37.4	<b>267</b>	<b>36.9</b>	191	51.6	<b>191</b>	<b>51.6</b>	191	51.5
470.lbm	19.8	694	20.8	662	<b>20.2</b>	<b>681</b>	19.8	694	20.8	662	<b>20.2</b>	<b>681</b>
481.wrf	139	80.2	140	79.9	<b>140</b>	<b>80.0</b>	139	80.2	140	79.9	<b>140</b>	<b>80.0</b>
482.sphinx3	297	65.7	<b>297</b>	<b>65.7</b>	299	65.1	297	65.7	<b>297</b>	<b>65.7</b>	299	65.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to ES mode  
 Set Patrol Scrub to Disable  
 Set Hyper-Threading to Disable  
 Sysinfo program /spec/spec16/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-c3qu Mon Nov 28 16:22:38 2016

This section contains SUT (System Under Test) info as seen by  
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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz
  2 "physical id"s (chips)
  36 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 18
    siblings  : 18
    physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
    physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  cache size : 46080 KB

```

```

From /proc/meminfo
MemTotal:      264059648 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-c3qu 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 28 06:27

```

SPEC is set to: /spec/spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   641G  7.6G  634G   2% /spec
Additional information from dmidecode:

```

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Samsung M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec/spec16/libs/32:/spec/spec16/libs/64:/spec/spec16/sh"

OMP\_NUM\_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

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Tested by: Huawei

Software Availability: Dec-2015

## Base Portability Flags (Continued)

```
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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