



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **121**

Huawei XH622 V3 (Intel Xeon E5-2667 v4)

SPECfp\_base2006 = **117**

CPU2006 license: 3175

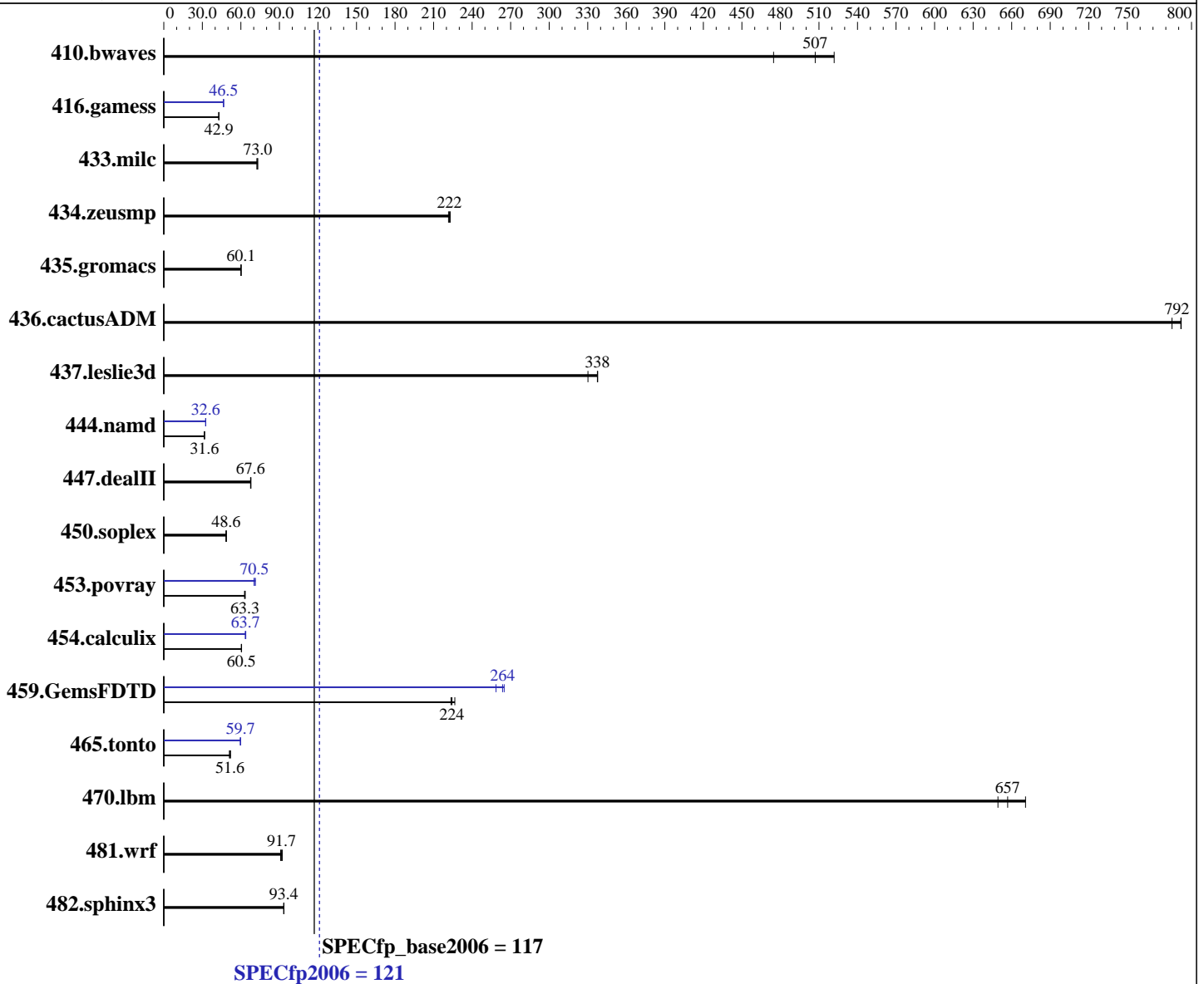
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2667 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)  
Disk Subsystem: 1 x 800 GB SATA SSD  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b><u>26.8</u></b>	<b><u>507</u></b>	26.0	522	28.6	475	<b><u>26.8</u></b>	<b><u>507</u></b>	26.0	522	28.6	475
416.gamess	456	42.9	<b><u>456</u></b>	<b><u>42.9</u></b>	458	42.8	<b><u>421</u></b>	<b><u>46.5</u></b>	421	46.5	421	46.5
433.milc	126	73.0	127	72.4	<b><u>126</u></b>	<b><u>73.0</u></b>	126	73.0	127	72.4	<b><u>126</u></b>	<b><u>73.0</u></b>
434.zeusmp	40.8	223	41.1	222	<b><u>40.9</u></b>	<b><u>222</u></b>	40.8	223	41.1	222	<b><u>40.9</u></b>	<b><u>222</u></b>
435.gromacs	<b><u>119</u></b>	<b><u>60.1</u></b>	119	60.2	119	60.1	<b><u>119</u></b>	<b><u>60.1</u></b>	119	60.2	119	60.1
436.cactusADM	<b><u>15.1</u></b>	<b><u>792</u></b>	15.1	792	15.2	785	<b><u>15.1</u></b>	<b><u>792</u></b>	15.1	792	15.2	785
437.leslie3d	27.8	338	<b><u>27.9</u></b>	<b><u>338</u></b>	28.5	330	27.8	338	<b><u>27.9</u></b>	<b><u>338</u></b>	28.5	330
444.namd	253	31.7	<b><u>253</u></b>	<b><u>31.6</u></b>	253	31.6	246	32.6	246	32.6	<b><u>246</u></b>	<b><u>32.6</u></b>
447.dealII	<b><u>169</u></b>	<b><u>67.6</u></b>	169	67.6	169	67.9	<b><u>169</u></b>	<b><u>67.6</u></b>	169	67.6	169	67.9
450.soplex	172	48.4	<b><u>171</u></b>	<b><u>48.6</u></b>	171	48.7	172	48.4	<b><u>171</u></b>	<b><u>48.6</u></b>	171	48.7
453.povray	84.7	62.8	<b><u>84.1</u></b>	<b><u>63.3</u></b>	84.0	63.3	75.7	70.3	74.6	71.3	<b><u>75.5</u></b>	<b><u>70.5</u></b>
454.calculix	<b><u>136</u></b>	<b><u>60.5</u></b>	137	60.4	136	60.5	<b><u>130</u></b>	<b><u>63.7</u></b>	129	63.8	130	63.4
459.GemsFDTD	<b><u>47.3</u></b>	<b><u>224</u></b>	47.5	224	46.8	227	40.0	265	<b><u>40.2</u></b>	<b><u>264</u></b>	41.0	259
465.tonto	<b><u>191</u></b>	<b><u>51.6</u></b>	189	52.0	193	51.0	165	59.7	<b><u>165</u></b>	<b><u>59.7</u></b>	165	59.7
470.lbm	<b><u>20.9</u></b>	<b><u>657</u></b>	21.2	649	20.5	671	<b><u>20.9</u></b>	<b><u>657</u></b>	21.2	649	20.5	671
481.wrf	<b><u>122</u></b>	<b><u>91.7</u></b>	123	90.9	121	92.2	<b><u>122</u></b>	<b><u>91.7</u></b>	123	90.9	121	92.2
482.sphinx3	<b><u>209</u></b>	<b><u>93.4</u></b>	208	93.5	209	93.3	<b><u>209</u></b>	<b><u>93.4</u></b>	208	93.5	209	93.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Snoop Mode to HS mode  
Set Patrol Scrub to Disable  
Set Hyper-Threading to Disable  
Sysinfo program /spec/spec16/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-c3qu Sat Nov 26 05:14:21 2016

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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Hardware Availability: Mar-2016

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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2667 v4@ 3.20GHz
  2 "physical id"s (chips)
  16 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 8
    physical 0: cores 0 2 3 4 8 10 11 12
    physical 1: cores 0 2 3 4 8 10 11 12
  cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      264062240 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-c3qu 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 26 00:59

```

SPEC is set to: /spec/spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   641G  7.6G  634G   2% /spec
Additional information from dmidecode:

```

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Samsung M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec/spec16/libs/32:/spec/spec16/libs/64:/spec/spec16/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.

The results have been measured on a Huawei XH620 V3 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```



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Software Availability: Dec-2015

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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## Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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