



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = 116

Huawei CH220 V3 (Intel Xeon E5-2699 v4)

SPECfp_base2006 = 109

CPU2006 license: 3175

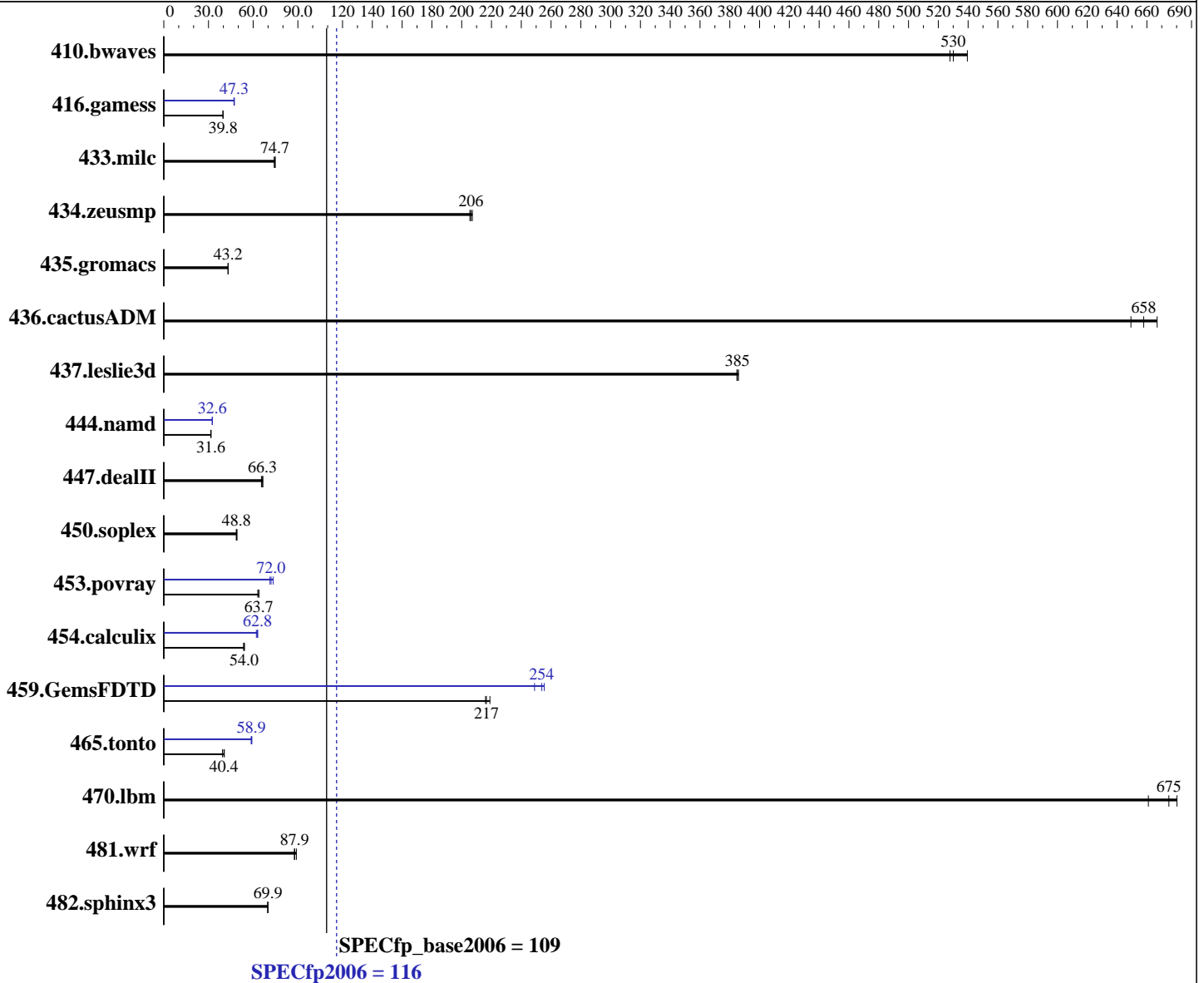
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Apr-2016

Tested by: Huawei

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2699 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 55 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.2	540	<u>25.6</u>	<u>530</u>	25.7	528	25.2	540	<u>25.6</u>	<u>530</u>	25.7	528
416.gamess	492	39.8	<u>492</u>	<u>39.8</u>	493	39.7	<u>414</u>	<u>47.3</u>	414	47.3	416	47.1
433.milc	124	74.1	123	74.8	<u>123</u>	<u>74.7</u>	124	74.1	123	74.8	<u>123</u>	<u>74.7</u>
434.zeusmp	<u>44.1</u>	<u>206</u>	44.3	205	43.9	207	<u>44.1</u>	<u>206</u>	44.3	205	43.9	207
435.gromacs	<u>165</u>	<u>43.2</u>	165	43.2	165	43.2	<u>165</u>	<u>43.2</u>	165	43.2	165	43.2
436.cactusADM	17.9	667	<u>18.2</u>	<u>658</u>	18.4	649	17.9	667	<u>18.2</u>	<u>658</u>	18.4	649
437.leslie3d	24.4	386	24.4	385	<u>24.4</u>	<u>385</u>	24.4	386	24.4	385	<u>24.4</u>	<u>385</u>
444.namd	<u>254</u>	<u>31.6</u>	254	31.6	253	31.7	246	32.6	<u>246</u>	<u>32.6</u>	247	32.5
447.dealII	175	65.5	172	66.5	<u>173</u>	<u>66.3</u>	175	65.5	172	66.5	<u>173</u>	<u>66.3</u>
450.soplex	<u>171</u>	<u>48.8</u>	171	48.7	170	49.2	<u>171</u>	<u>48.8</u>	171	48.7	170	49.2
453.povray	84.3	63.1	83.3	63.9	<u>83.5</u>	<u>63.7</u>	<u>73.9</u>	<u>72.0</u>	72.4	73.4	74.6	71.3
454.calculix	152	54.1	154	53.5	<u>153</u>	<u>54.0</u>	<u>131</u>	<u>62.8</u>	131	63.1	133	62.1
459.GemsFDTD	49.1	216	<u>49.0</u>	<u>217</u>	48.5	219	42.6	249	<u>41.8</u>	<u>254</u>	41.5	255
465.tonto	<u>244</u>	<u>40.4</u>	250	39.4	242	40.6	166	59.1	168	58.7	<u>167</u>	<u>58.9</u>
470.lbm	<u>20.4</u>	<u>675</u>	20.8	661	20.2	680	<u>20.4</u>	<u>675</u>	20.8	661	20.2	680
481.wrf	125	89.0	<u>127</u>	<u>87.9</u>	127	87.6	125	89.0	<u>127</u>	<u>87.9</u>	127	87.6
482.sphinx3	279	69.8	279	69.9	<u>279</u>	<u>69.9</u>	279	69.8	279	69.9	<u>279</u>	<u>69.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS mode
 Set Patrol Scrub to Disable
 Set Hyper-Threading to Disable
 Sysinfo program /spec16/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-6392 Mon Nov 7 21:01:43 2016

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
 2 "physical id"s (chips)
 44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores     : 22
  siblings      : 22
  physical 0:   cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
  physical 1:   cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size      : 56320 KB

```

From /proc/meminfo

```

MemTotal:      528844588 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

uname -a:

Linux linux-6392 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 7 16:29 last=5

SPEC is set to: /spec16

```

Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/md126p2    xfs       455G  23G  432G   5% /

```

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Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

16x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP_NUM_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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