



# SPEC® CINT2006 Result

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## Huawei

**SPECint®2006 = 71.6**

Huawei XH622 V3 (Intel Xeon E5-2643 v4)

**SPECint\_base2006 = 68.0**

CPU2006 license: 3175

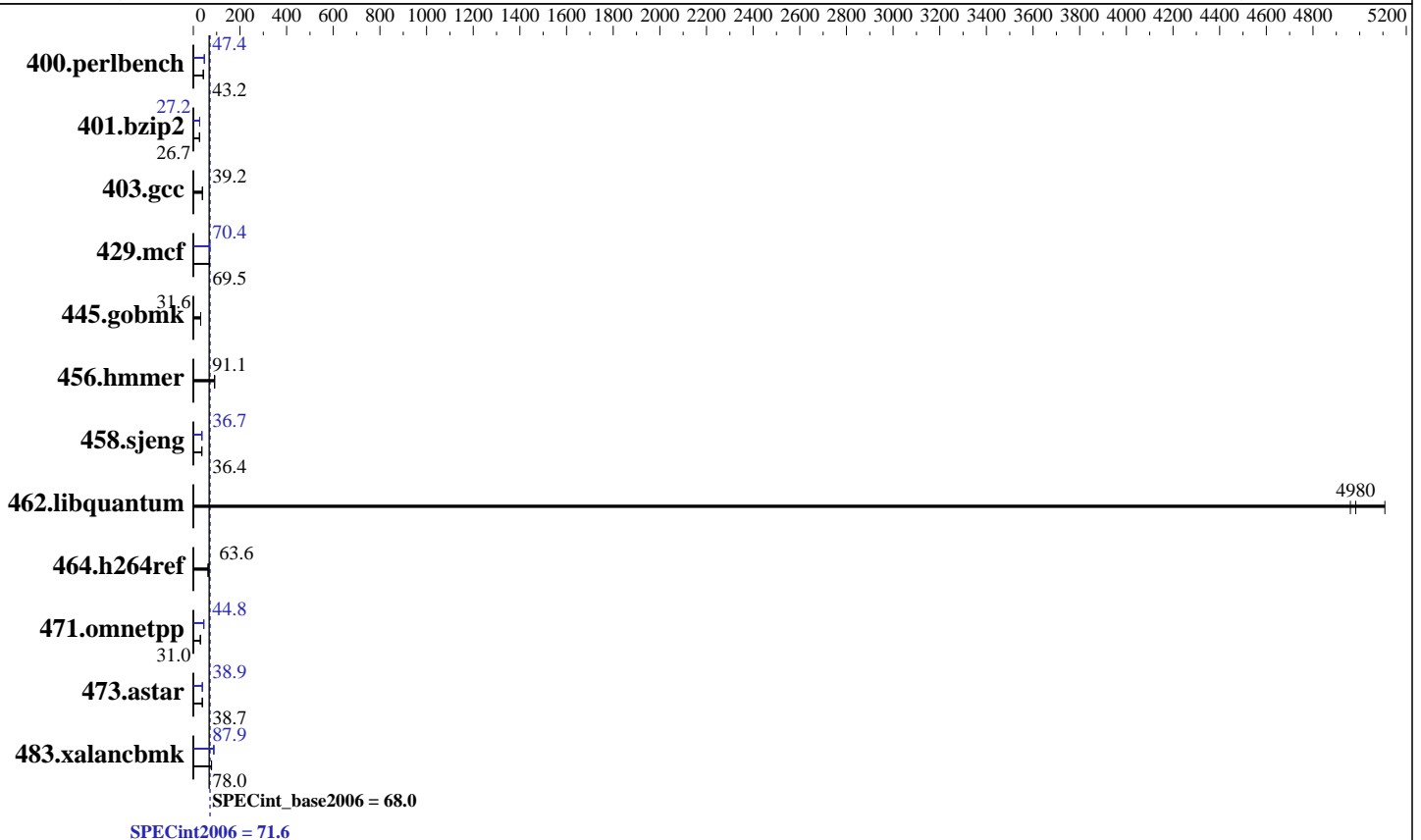
Test date: Oct-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Mar-2016



### Hardware

CPU Name: Intel Xeon E5-2643 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (8 x 32 GB 2Rx8 PC4-2400T-R)  
 Disk Subsystem: 1 x 600 GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	226	43.3	<b><u>226</u></b>	<b><u>43.2</u></b>	228	42.9	206	47.4	<b><u>206</u></b>	<b><u>47.4</u></b>	206	47.4
401.bzip2	362	26.6	361	26.7	<b><u>362</u></b>	<b><u>26.7</u></b>	355	27.2	<b><u>355</u></b>	<b><u>27.2</u></b>	355	27.2
403.gcc	206	39.1	205	39.2	<b><u>205</u></b>	<b><u>39.2</u></b>	206	39.1	205	39.2	<b><u>205</u></b>	<b><u>39.2</u></b>
429.mcf	130	70.0	132	69.2	<b><u>131</u></b>	<b><u>69.5</u></b>	<b><u>130</u></b>	<b><u>70.4</u></b>	129	70.8	130	70.1
445.gobmk	<b><u>332</u></b>	<b><u>31.6</u></b>	332	31.6	332	31.6	<b><u>332</u></b>	<b><u>31.6</u></b>	332	31.6	332	31.6
456.hammer	102	91.1	<b><u>102</u></b>	<b><u>91.1</u></b>	102	91.3	102	91.1	<b><u>102</u></b>	<b><u>91.1</u></b>	102	91.3
458.sjeng	333	36.4	333	36.4	<b><u>333</u></b>	<b><u>36.4</u></b>	<b><u>329</u></b>	<b><u>36.7</u></b>	329	36.7	330	36.7
462.libquantum	<b><u>4.16</u></b>	<b><u>4980</u></b>	4.06	5110	4.18	4960	<b><u>4.16</u></b>	<b><u>4980</u></b>	4.06	5110	4.18	4960
464.h264ref	347	63.8	<b><u>348</u></b>	<b><u>63.6</u></b>	348	63.6	347	63.8	<b><u>348</u></b>	<b><u>63.6</u></b>	348	63.6
471.omnetpp	213	29.3	199	31.4	<b><u>201</u></b>	<b><u>31.0</u></b>	139	44.9	<b><u>139</u></b>	<b><u>44.8</u></b>	140	44.8
473.astar	<b><u>181</u></b>	<b><u>38.7</u></b>	181	38.7	181	38.9	181	38.9	<b><u>181</u></b>	<b><u>38.9</u></b>	181	38.7
483.xalancbmk	<b><u>88.5</u></b>	<b><u>78.0</u></b>	88.9	77.6	88.3	78.2	<b><u>78.5</u></b>	<b><u>87.9</u></b>	78.7	87.7	78.5	87.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-lg2g Wed Oct 26 21:52:22 2016

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
 2 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6

```

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## Platform Notes (Continued)

```

siblings      : 6
physical 0:   cores 0 1 2 3 6 7
physical 1:   cores 0 1 2 3 6 7
cache size   : 20480 KB

```

```

From /proc/meminfo
MemTotal:      264078020 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-1g2g 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 26 11:42

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext4  542G  15G  526G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS Insyde Corp. 3.31 08/22/2016
Memory:
8x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,compact,1,0"  
 LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"  
 OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
 runspec command invoked through numactl i.e.:  
 numactl --interleave=all runspec <etc>  
 The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.  
 The results have been measured on a Huawei XH620 V3 model.

## Base Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
 -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmarheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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