SPEC® CFP2006 Result
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HITACHI

$SPECfp^{®}\_rate2006 =$ 4250

Compute Blade 520X (Intel Xeon E7-8880 v4)

$SPECfp\_rate\_base2006 =$ 4140
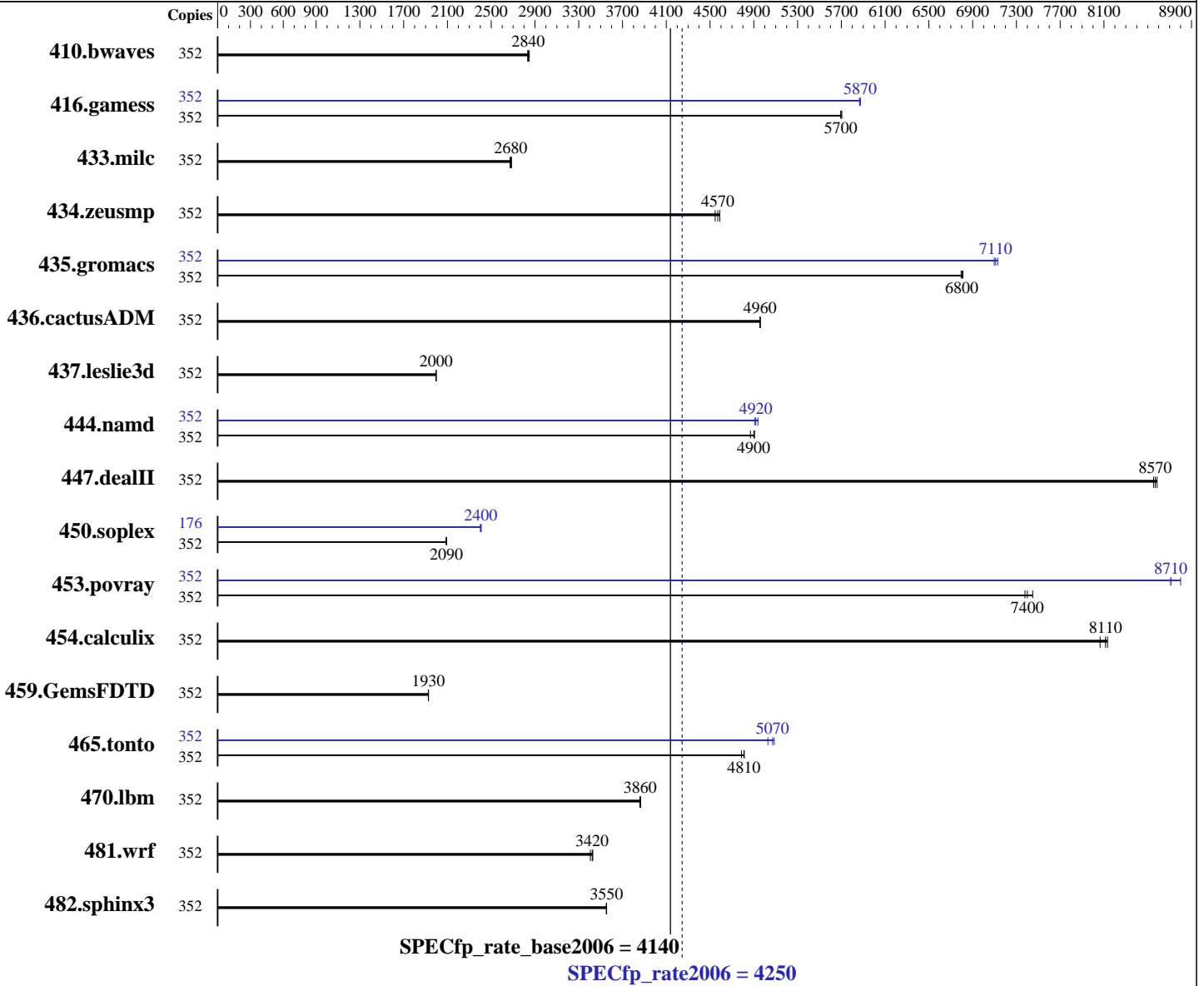
CPU2006 license: 35
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Sep-2016
Hardware Availability: Jun-2016
Software Availability: Mar-2016

Benchmark	Copies	Result
410.bwaves	352	2840
416.gamess	352	5870
416.gamess	352	5700
433.milc	352	2680
434.zeusmp	352	4570
435.gromacs	352	7110
435.gromacs	352	6800
436.cactusADM	352	4960
437.leslie3d	352	2000
444.namd	352	4920
444.namd	352	4900
447.dealII	352	8570
450.soplex	176	2400
450.soplex	352	2090
453.povray	352	8710
453.povray	352	7400
454.calculix	352	8110
459.GemsFDTD	352	1930
465.tonto	352	5070
465.tonto	352	4810
470.lbm	352	3860
481.wrf	352	3420
482.sphinx3	352	3550

SPECfp_rate_base2006 = 4140
SPECfp_rate2006 = 4250

Hardware

CPU Name: Intel Xeon E7-8880 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 176 cores, 8 chips, 22 cores/chip, 2 threads/core
CPU(s) orderable: 2,4,8 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: tmpfs

Continued on next page

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L3 Cache: 55 MB I+D on chip per chip
Other Cache: None
Memory: 2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 2 x 600 GB SAS, 15000 RPM, RAID1
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	352	**1685**	**2840**	1688	2830	1679	2850	352	**1685**	**2840**	1688	2830	1679	2850
416.gamess	352	**1210**	**5700**	1210	5690	1208	5700	352	**1174**	**5870**	1173	5870	1175	5870
433.milc	352	1203	2690	**1206**	**2680**	1209	2670	352	1203	2690	**1206**	**2680**	1209	2670
434.zeusmp	352	698	4590	704	4550	**701**	**4570**	352	698	4590	704	4550	**701**	**4570**
435.gromacs	352	**369**	**6800**	370	6790	369	6810	352	354	7100	353	7130	**353**	**7110**
436.cactusADM	352	848	4960	848	4960	**848**	**4960**	352	848	4960	848	4960	**848**	**4960**
437.leslie3d	352	1656	2000	1658	2000	**1657**	**2000**	352	1656	2000	1658	2000	**1657**	**2000**
444.namd	352	**576**	**4900**	575	4910	580	4870	352	572	4940	**574**	**4920**	575	4910
447.dealII	352	**470**	**8570**	469	8590	471	8550	352	**470**	**8570**	469	8590	471	8550
450.soplex	352	1403	2090	1407	2090	**1403**	**2090**	176	**611**	**2400**	611	2400	609	2410
453.povray	352	254	7380	251	7450	**253**	**7400**	352	213	8800	215	8710	**215**	**8710**
454.calculix	352	360	8060	**358**	**8110**	357	8130	352	360	8060	**358**	**8110**	357	8130
459.GemsFDTD	352	1937	1930	**1939**	**1930**	1939	1930	352	1937	1930	**1939**	**1930**	1939	1930
465.tonto	352	**720**	**4810**	720	4810	723	4790	352	681	5090	**683**	**5070**	689	5030
470.lbm	352	**1253**	**3860**	1251	3870	1253	3860	352	**1253**	**3860**	1251	3870	1253	3860
481.wrf	352	**1149**	**3420**	1154	3410	1147	3430	352	**1149**	**3420**	1154	3410	1147	3430
482.sphinx3	352	1930	3550	1931	3550	**1931**	**3550**	352	1930	3550	1931	3550	**1931**	**3550**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    BIOS configuration:
     Memory Power Management = Automatic
     Active Energy Manager = "Capping Disabled"

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Compute Blade 520X (Intel Xeon E7-8880 v4)

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CPU2006 license: 35
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Test date: Sep-2016
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Platform Notes (Continued)

```
Platform Controlled Type = "Maximum Performance"
C1 Enhanced Mode = Disable
C-States =  Disable
ProcessorPerformanceStates = Disable
Sysinfo program /home/shm/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on rhel7264 Fri Sep 30 20:00:58 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz
      8 "physical id"s (chips)
      352 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 22
      siblings  : 44
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 4: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 5: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 6: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 7: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
   cache size : 56320 KB

From /proc/meminfo
   MemTotal:       2112861176 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
```

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HITACHI

Compute Blade 520X (Intel Xeon E7-8880 v4)

SPECfp_rate2006 =　　4250

SPECfp_rate_base2006 =　4140

CPU2006 license: 35		**Test date:**	Sep-2016
Test sponsor: HITACHI		**Hardware Availability:**	Jun-2016
Tested by: HITACHI		**Software Availability:**	Mar-2016

Platform Notes (Continued)

```
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
   Linux rhel7264 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 29 14:21

SPEC is set to: /home/shm/cpu2006
   Filesystem      Type   Size  Used Avail Use% Mounted on
   tmpfs          tmpfs 1000G  7.9G  993G   1% /home/shm
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

 BIOS HITACHI 11-04 08/29/2016
 Memory:
  52x 0x0000 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
  6x 0x0003 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
  2x 0x0004 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
  2x 0x0201 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
  2x 0x5C00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
  64x NO DIMM Unknown
  64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/shm/cpu2006/libs/32:/home/shm/cpu2006/libs/64:/home/shm/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
 memory using RedHat EL 7.2 glibc 2.17
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Hitachi Compute Blade 520X and BladeSymphony BS2500 are electronically equivalent.
 The results have been measured on a Hitachi Compute Blade 520X.
```

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Compute Blade 520X (Intel Xeon E7-8880 v4)

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CPU2006 license: 35
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Test date: Sep-2016
Hardware Availability: Jun-2016
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Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

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Compute Blade 520X (Intel Xeon E7-8880 v4)

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Tested by: HITACHI		**Software Availability:**	Mar-2016

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
  444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
   470.lbm: -DSPEC_CPU_LP64
   481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

Continued on next page

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Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

HITACHI

Compute Blade 520X (Intel Xeon E7-8880 v4)

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Test sponsor: HITACHI
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Test date: Sep-2016
Hardware Availability: Jun-2016
Software Availability: Mar-2016

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.7.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.7.xml

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