



# SPEC<sup>®</sup> CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen9

(2.20 GHz, Intel Xeon E5-4650 v4)

SPECfp<sup>®</sup>\_rate2006 = 1760

SPECfp\_rate\_base2006 = 1720

CPU2006 license: 3

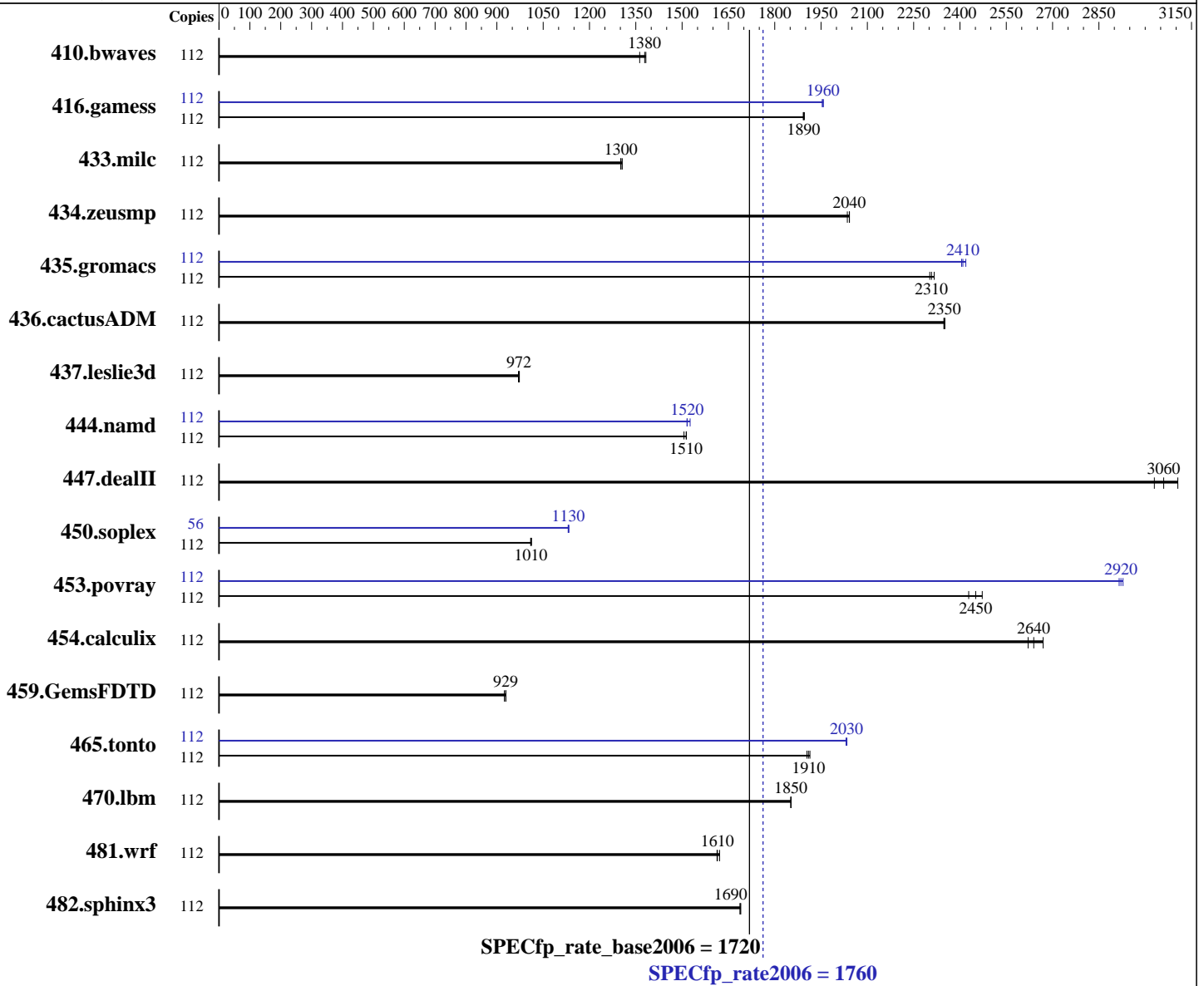
Test sponsor: HPE

Tested by: HPE

Test date: Aug-2016

Hardware Availability: Jul-2016

Software Availability: Dec-2015



## Hardware

CPU Name: Intel Xeon E5-4650 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1, Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	112	1117	1360	<b><u>1104</u></b>	<b><u>1380</u></b>	1101	1380	112	1117	1360	<b><u>1104</u></b>	<b><u>1380</u></b>	1101	1380
416.gamess	112	<b><u>1158</u></b>	<b><u>1890</u></b>	1159	1890	1157	1900	112	1120	1960	1122	1950	<b><u>1121</u></b>	<b><u>1960</u></b>
433.milc	112	<b><u>790</u></b>	<b><u>1300</u></b>	787	1310	790	1300	112	<b><u>790</u></b>	<b><u>1300</u></b>	787	1310	790	1300
434.zeusmp	112	<b><u>499</u></b>	<b><u>2040</u></b>	499	2040	501	2040	112	<b><u>499</u></b>	<b><u>2040</u></b>	499	2040	501	2040
435.gromacs	112	347	2300	<b><u>346</u></b>	<b><u>2310</u></b>	345	2320	112	331	2420	332	2410	<b><u>332</u></b>	<b><u>2410</u></b>
436.cactusADM	112	569	2350	<b><u>570</u></b>	<b><u>2350</u></b>	570	2350	112	569	2350	<b><u>570</u></b>	<b><u>2350</u></b>	570	2350
437.leslie3d	112	1082	973	<b><u>1083</u></b>	<b><u>972</u></b>	1085	970	112	1082	973	<b><u>1083</u></b>	<b><u>972</u></b>	1085	970
444.namd	112	<b><u>594</u></b>	<b><u>1510</u></b>	593	1510	596	1510	112	589	1530	<b><u>592</u></b>	<b><u>1520</u></b>	593	1520
447.dealII	112	413	3110	<b><u>419</u></b>	<b><u>3060</u></b>	423	3030	112	413	3110	<b><u>419</u></b>	<b><u>3060</u></b>	423	3030
450.soplex	112	<b><u>924</u></b>	<b><u>1010</u></b>	925	1010	923	1010	56	<b><u>413</u></b>	<b><u>1130</u></b>	412	1130	413	1130
453.povray	112	<b><u>243</u></b>	<b><u>2450</u></b>	245	2430	241	2470	112	204	2920	203	2930	<b><u>204</u></b>	<b><u>2920</u></b>
454.calculix	112	346	2670	<b><u>350</u></b>	<b><u>2640</u></b>	353	2620	112	346	2670	<b><u>350</u></b>	<b><u>2640</u></b>	353	2620
459.GemsFDTD	112	1286	924	1279	929	<b><u>1280</u></b>	<b><u>929</u></b>	112	1286	924	1279	929	<b><u>1280</u></b>	<b><u>929</u></b>
465.tonto	112	576	1910	579	1900	<b><u>577</u></b>	<b><u>1910</u></b>	112	542	2030	543	2030	<b><u>542</u></b>	<b><u>2030</u></b>
470.lbm	112	831	1850	831	1850	<b><u>831</u></b>	<b><u>1850</u></b>	112	831	1850	831	1850	<b><u>831</u></b>	<b><u>1850</u></b>
481.wrf	112	<b><u>775</u></b>	<b><u>1610</u></b>	776	1610	772	1620	112	<b><u>775</u></b>	<b><u>1610</u></b>	776	1610	772	1620
482.sphinx3	112	1293	1690	<b><u>1293</u></b>	<b><u>1690</u></b>	1292	1690	112	1293	1690	<b><u>1293</u></b>	<b><u>1690</u></b>	1292	1690

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```



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**SPECfp\_rate\_base2006 = 1720**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Aug-2016

**Hardware Availability:** Jul-2016

**Software Availability:** Dec-2015

## Platform Notes

### BIOS Configuration:

HP Power Profile set to Balanced Power and Performance  
QPI Snoop Configuration set to Cluster on Die  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on sles12biswadl560 Wed Aug 17 04:31:26 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-4650 v4 @ 2.20GHz

4 "physical id"s (chips)

112 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 14

siblings : 28

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

cache size : 17920 KB

From /proc/meminfo

MemTotal: 529302712 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 1

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP1"

VERSION\_ID="12.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

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## Platform Notes (Continued)

uname -a:

```
Linux sles12biswadl560 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Aug 17 04:29

SPEC is set to: /home/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   331G   53G  278G  16% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P85 07/01/2016

Memory:

16x UNKNOWN NOT AVAILABLE

32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

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## Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

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