



SPEC[®] CFP2006 Result

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NEC Corporation

SPECfp[®]2006 = **62.2**

Express5800/GT110h (Intel Celeron G3900)

SPECfp_base2006 = **61.6**

CPU2006 license: 9006

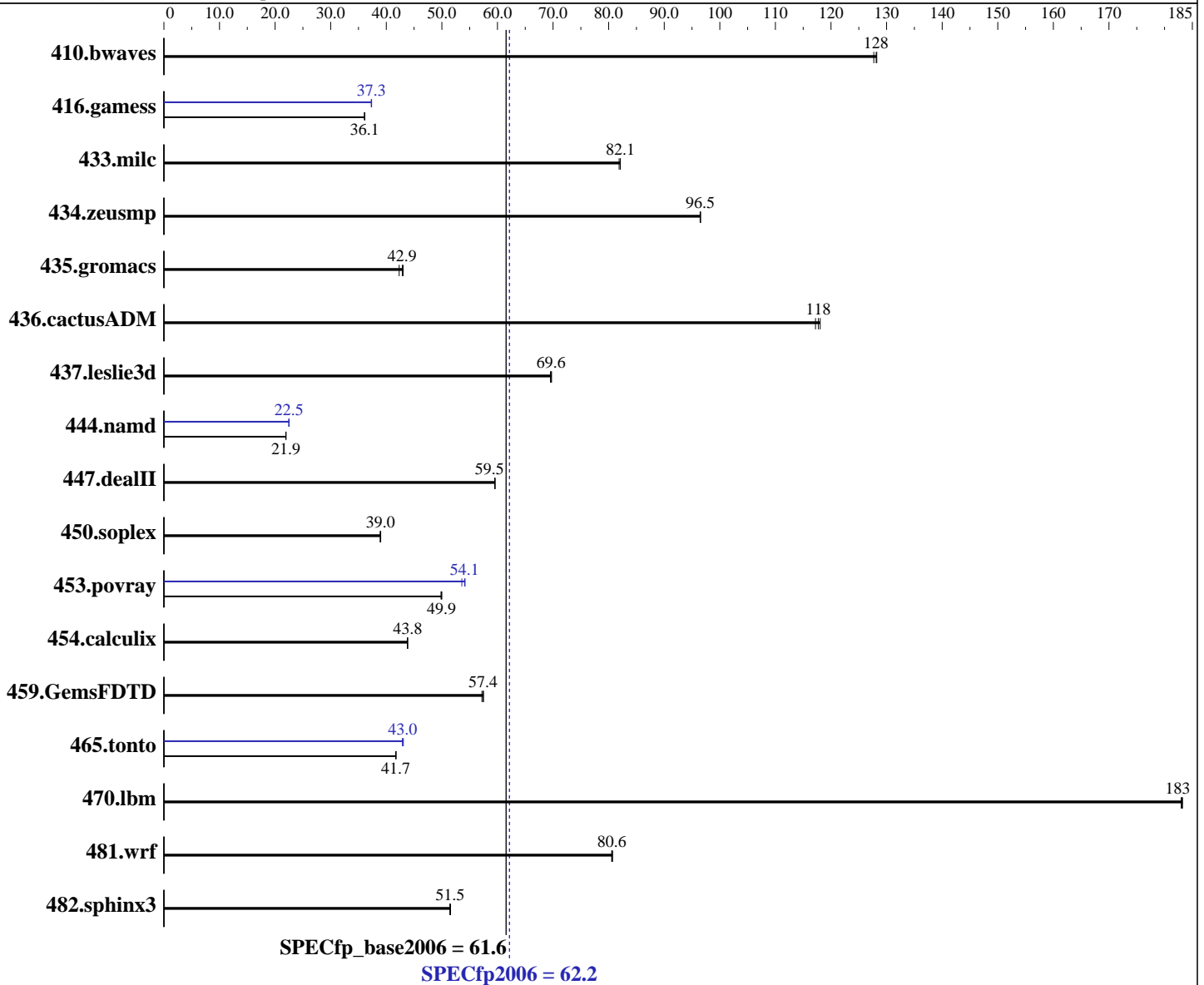
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2016

Hardware Availability: Jul-2016

Software Availability: Jan-2016



Hardware

CPU Name: Intel Celeron G3900
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Kernel 3.10.0-327.4.5.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E)
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	106	128	106	128	106	128	106	128	106	128	106	128
416.gamess	542	36.1	542	36.1	543	36.1	525	37.3	525	37.3	524	37.4
433.milc	112	81.9	112	82.1	112	82.1	112	81.9	112	82.1	112	82.1
434.zeusmp	94.2	96.6	94.3	96.5	94.3	96.5	94.2	96.6	94.3	96.5	94.3	96.5
435.gromacs	167	42.9	166	43.1	169	42.3	167	42.9	166	43.1	169	42.3
436.cactusADM	101	118	102	118	102	117	101	118	102	118	102	117
437.leslie3d	135	69.7	135	69.5	135	69.6	135	69.7	135	69.5	135	69.6
444.namd	365	21.9	365	22.0	366	21.9	356	22.5	357	22.5	356	22.5
447.dealII	192	59.5	192	59.5	192	59.6	192	59.5	192	59.5	192	59.6
450.soplex	214	39.0	214	39.0	214	38.9	214	39.0	214	39.0	214	38.9
453.povray	107	49.8	106	50.0	107	49.9	99.3	53.6	98.2	54.2	98.4	54.1
454.calculix	188	43.9	188	43.8	188	43.8	188	43.9	188	43.8	188	43.8
459.GemsFDTD	185	57.4	185	57.5	185	57.2	185	57.4	185	57.5	185	57.2
465.tonto	236	41.7	236	41.7	236	41.8	228	43.1	229	42.9	229	43.0
470.lbm	75.0	183	75.1	183	75.0	183	75.0	183	75.1	183	75.0	183
481.wrf	138	80.7	139	80.6	139	80.6	138	80.7	139	80.6	139	80.6
482.sphinx3	378	51.6	378	51.5	379	51.4	378	51.6	378	51.5	379	51.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact,1,0"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

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General Notes (Continued)

OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.lelie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)
-prof-use(pass 2) -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)
-prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1)
-prof-use(pass 2) -inline-calloc -opt-malloc-options=3
-auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.xml>



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For other inquiries, please contact webmaster@spec.org.

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