



SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint®2006 = 57.2

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = 55.4

CPU2006 license: 35

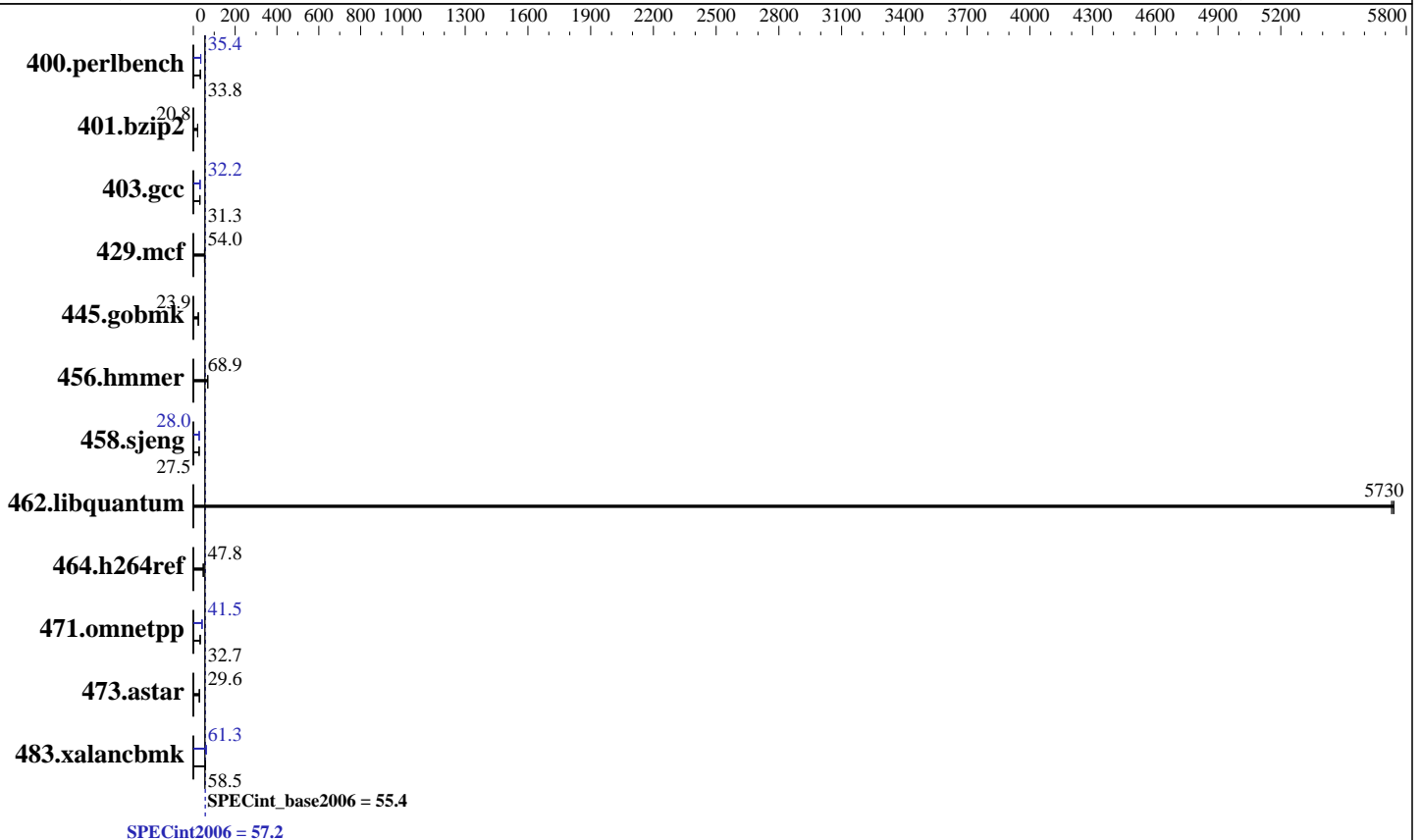
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2650 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 2 x 300 GB SAS, 15000 RPM, RAID1
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.0 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = **57.2**

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = **55.4**

CPU2006 license: 35
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jun-2016
Hardware Availability: Apr-2016
Software Availability: Nov-2015

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>289</u>	<u>33.8</u>	289	33.8	291	33.6	273	35.8	<u>276</u>	<u>35.4</u>	277	35.3
401.bzip2	466	20.7	464	20.8	<u>464</u>	<u>20.8</u>	466	20.7	464	20.8	<u>464</u>	<u>20.8</u>
403.gcc	<u>257</u>	<u>31.3</u>	256	31.4	258	31.2	251	32.0	<u>250</u>	<u>32.2</u>	249	32.3
429.mcf	172	53.2	167	54.5	<u>169</u>	<u>54.0</u>	172	53.2	167	54.5	<u>169</u>	<u>54.0</u>
445.gobmk	439	23.9	441	23.8	<u>439</u>	<u>23.9</u>	439	23.9	441	23.8	<u>439</u>	<u>23.9</u>
456.hmmer	<u>135</u>	<u>68.9</u>	135	68.9	135	69.1	<u>135</u>	<u>68.9</u>	135	68.9	135	69.1
458.sjeng	436	27.8	440	27.5	<u>440</u>	<u>27.5</u>	<u>432</u>	<u>28.0</u>	435	27.8	430	28.2
462.libquantum	3.61	5740	<u>3.61</u>	<u>5730</u>	3.62	5730	3.61	5740	<u>3.61</u>	<u>5730</u>	3.62	5730
464.h264ref	<u>463</u>	<u>47.8</u>	464	47.7	461	48.0	<u>463</u>	<u>47.8</u>	464	47.7	461	48.0
471.omnetpp	<u>191</u>	<u>32.7</u>	192	32.6	191	32.8	149	42.0	<u>151</u>	<u>41.5</u>	151	41.3
473.astar	250	28.1	<u>237</u>	<u>29.6</u>	236	29.7	250	28.1	<u>237</u>	<u>29.6</u>	236	29.7
483.xalancbmk	119	58.0	<u>118</u>	<u>58.5</u>	118	58.6	113	60.8	112	61.5	<u>112</u>	<u>61.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Patrol Scrub = Disable
Per Core P-state = Disable

Sysinfo program /home/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on rhel722 Sun Jun 12 21:58:45 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
2 "physical id"s (chips)
48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 57.2

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = 55.4

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Nov-2015

Platform Notes (Continued)

```

cpu cores : 12
siblings  : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

```

From /proc/meminfo

```

MemTotal:      527320468 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

uname -a:

```

Linux rhel722 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jun 12 21:55

SPEC is set to: /home/cpu2006

```

Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      225G   24G  201G  11% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HITACHI 10-00 01/29/2016

Memory:

```

8x NO DIMM Unknown
16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

```

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 57.2

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = 55.4

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Nov-2015

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 are electronically equivalent.

The results have been measured on a Hitachi Compute Blade 520H.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -DSPEC_CPU_LP64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 57.2

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = 55.4

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Nov-2015

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: basepeak = yes

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 57.2

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = 55.4

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Nov-2015

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.xml>



SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

HITACHI

SPECint2006 = 57.2

Compute Blade 520H (Intel Xeon E5-2650 v4)

SPECint_base2006 = 55.4

CPU2006 license: 35

Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jun-2016

Hardware Availability: Apr-2016

Software Availability: Nov-2015

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 28 17:31:28 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 June 2016.