



# SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp<sup>®</sup>\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

CPU2006 license: 55

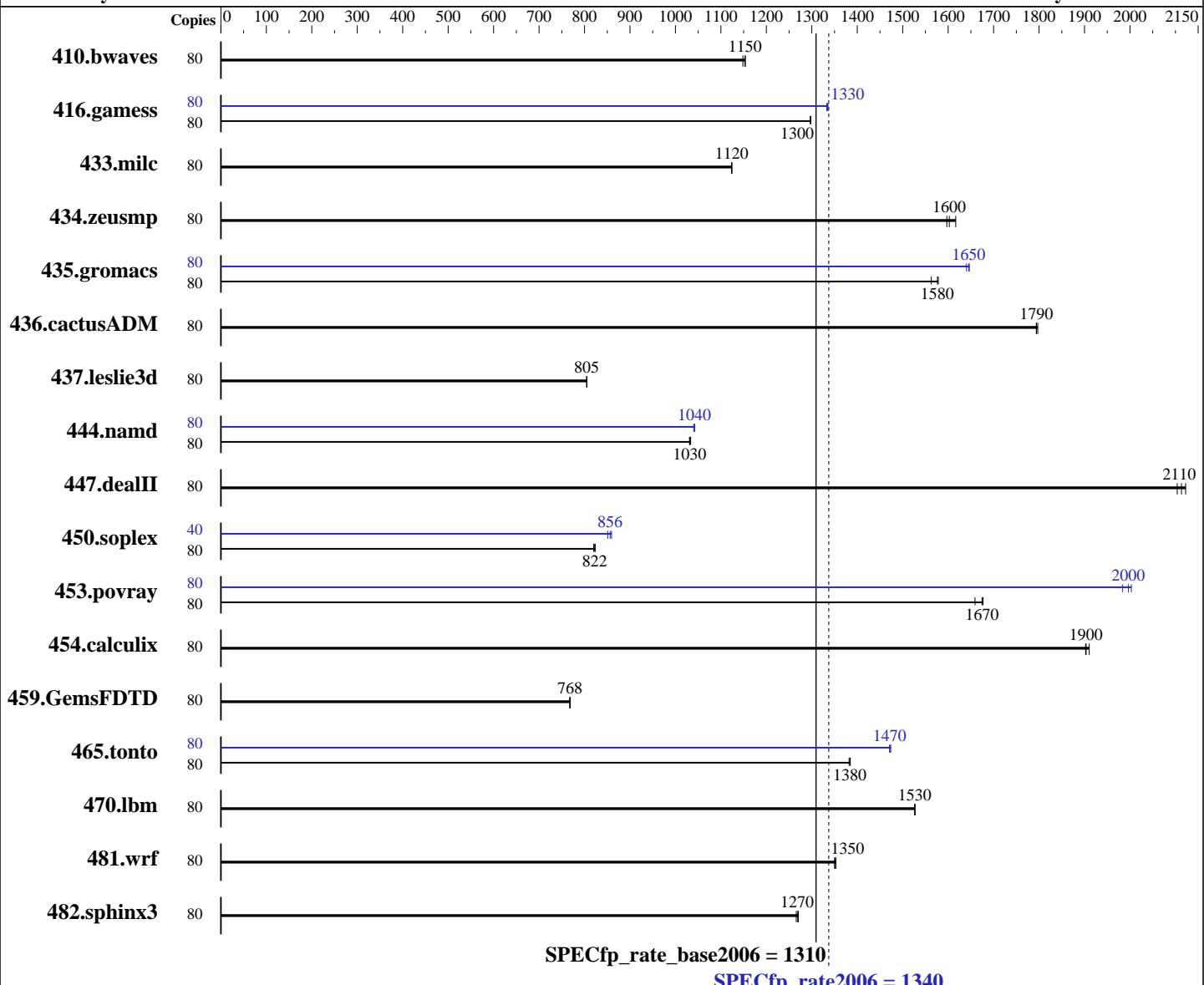
**Test date:** May-2016

Test sponsor: Dell Inc.

**Hardware Availability:** Jun-2016

Tested by: Dell Inc.

**Software Availability:** Mar-2016



## Hardware

CPU Name: Intel Xeon E5-4620 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz  
CPU MHz: 2100  
FPU: integrated  
CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
CPU(s) orderable: 2,4 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: btrfs  
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 800 GB SATA SSD  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	80	942	1150	947	1150	<u>943</u>	<u>1150</u>	80	942	1150	947	1150	<u>943</u>	<u>1150</u>
416.gamess	80	1207	1300	1208	1300	<u>1207</u>	<u>1300</u>	80	1173	1340	1175	1330	<u>1173</u>	<u>1330</u>
433.milc	80	<b>653</b>	<b>1120</b>	653	1120	654	1120	80	<b>653</b>	<b>1120</b>	653	1120	654	1120
434.zeusmp	80	450	1620	456	1600	<b>454</b>	<b>1600</b>	80	450	1620	456	1600	<b>454</b>	<b>1600</b>
435.gromacs	80	<b>362</b>	<b>1580</b>	362	1580	365	1560	80	<b>347</b>	<b>1650</b>	347	1650	348	1640
436.cactusADM	80	533	1790	532	1800	<u>533</u>	<u>1790</u>	80	533	1790	532	1800	<u>533</u>	<u>1790</u>
437.leslie3d	80	934	805	934	805	<b>934</b>	<b>805</b>	80	934	805	934	805	<b>934</b>	<b>805</b>
444.namd	80	<b>622</b>	<b>1030</b>	621	1030	622	1030	80	615	1040	<b>616</b>	<b>1040</b>	617	1040
447.dealII	80	431	2120	435	2100	<b>433</b>	<b>2110</b>	80	431	2120	435	2100	<b>433</b>	<b>2110</b>
450.soplex	80	810	823	814	820	<b>811</b>	<b>822</b>	40	392	851	<b>390</b>	<b>856</b>	388	860
453.povray	80	254	1680	<b>254</b>	<b>1670</b>	257	1660	80	<b>213</b>	<b>2000</b>	212	2000	215	1980
454.calculix	80	347	1900	<b>347</b>	<b>1900</b>	345	1910	80	347	1900	<b>347</b>	<b>1900</b>	345	1910
459.GemsFDTD	80	1106	767	<b>1105</b>	<b>768</b>	1104	769	80	1106	767	<b>1105</b>	<b>768</b>	1104	769
465.tonto	80	570	1380	<b>569</b>	<b>1380</b>	569	1380	80	<b>535</b>	<b>1470</b>	535	1470	534	1470
470.lbm	80	720	1530	<b>720</b>	<b>1530</b>	720	1530	80	<b>720</b>	<b>1530</b>	<b>720</b>	<b>1530</b>	720	1530
481.wrf	80	660	1350	662	1350	<b>661</b>	<b>1350</b>	80	660	1350	662	1350	<b>661</b>	<b>1350</b>
482.sphinx3	80	<b>1229</b>	<b>1270</b>	1232	1270	1227	1270	80	<b>1229</b>	<b>1270</b>	1232	1270	1227	1270

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:

Snoop Mode set to Home Snoop

Virtualization Technology disabled

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

**CPU2006 license:** 55

**Test sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test date:** May-2016

**Hardware Availability:** Jun-2016

**Software Availability:** Mar-2016

## Platform Notes (Continued)

System Profile set to custom  
CPU Performance set to Hardware P States  
C States set to Autonomous  
C1E disabled  
Energy Efficient Turbo disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Balanced Performance  
Memory Patrol Scrub disabled  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-t2sb Fri May 13 14:12:22 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-4620 v4 @ 2.10GHz  
 4 "physical id"s (chips)  
 80 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
 cpu cores : 10  
 siblings : 20  
 physical 0: cores 0 1 2 3 4 8 9 10 11 12  
 physical 1: cores 0 1 2 3 4 8 9 10 11 12  
 physical 2: cores 0 1 2 3 4 8 9 10 11 12  
 physical 3: cores 0 1 2 3 4 8 9 10 11 12  
cache size : 25600 KB

From /proc/meminfo  
MemTotal: 529326748 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
SUSE Linux Enterprise Server 12 SP1

From /etc/\*release\* /etc/\*version\*  
SuSE-release:  
 SUSE Linux Enterprise Server 12 (x86\_64)  
VERSION = 12  
PATCHLEVEL = 1  
# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.  
os-release:  
 NAME="SLES"  
 VERSION="12-SP1"  
 VERSION\_ID="12.1"  
 PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

## Platform Notes (Continued)

```
ID="sles"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:  
Linux linux-t2sb 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015  
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 13 01:37
```

```
SPEC is set to: /root/cpu2006-1.2  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/sda3        btrfs  461G  9.5G  449G   3% /  
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS Dell Inc. 1.0.0 04/26/2016
```

```
Memory:
```

```
 31x 00AD063200AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz, configured at 2133  
MHz  
 1x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133  
MHz  
 16x Not Specified Not Specified
```

```
(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
```

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB  
memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

fort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

CPU2006 license: 55

Test date: May-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016

## Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1340**

PowerEdge R830 (Intel Xeon E5-4620 v4, 2.10 GHz)

**SPECfp\_rate\_base2006 = 1310**

**CPU2006 license:** 55

**Test date:** May-2016

**Test sponsor:** Dell Inc.

**Hardware Availability:** Jun-2016

**Tested by:** Dell Inc.

**Software Availability:** Mar-2016

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jun 28 17:30:34 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 28 June 2016.