



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant ML350 Gen9**

(2.20 GHz, Intel Xeon E5-2698 v4)

**SPECint®2006 =**

**72.7**

**SPECint\_base2006 =**

**70.4**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** May-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015



## Hardware

**CPU Name:** Intel Xeon E5-2698 v4  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.60 GHz  
**CPU MHz:** 2200  
**FPU:** Integrated  
**CPU(s) enabled:** 40 cores, 2 chips, 20 cores/chip  
**CPU(s) orderable:** 1, 2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 50 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
**Disk Subsystem:** 1 x 800 GB SAS SSD, RAID 0  
**Other Hardware:** None

## Software

**Operating System:** SUSE Linux Enterprise Server 12 (x86\_64) SP1  
Kernel 3.12.49-11-default  
**Compiler:** C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>234</u></b>	<b><u>41.8</u></b>	234	41.7	234	41.8	<b><u>213</u></b>	<b><u>45.8</u></b>	213	45.9	213	45.8
401.bzip2	388	24.9	392	24.6	<b><u>389</u></b>	<b><u>24.8</u></b>	<b><u>382</u></b>	<b><u>25.3</u></b>	382	25.3	382	25.3
403.gcc	210	38.3	210	38.4	<b><u>210</u></b>	<b><u>38.4</u></b>	210	38.3	210	38.4	<b><u>210</u></b>	<b><u>38.4</u></b>
429.mcf	146	62.3	<b><u>147</u></b>	<b><u>61.9</u></b>	148	61.7	<b><u>144</u></b>	<b><u>63.3</u></b>	147	62.1	144	63.4
445.gobmk	340	30.9	341	30.8	<b><u>340</u></b>	<b><u>30.8</u></b>	340	30.9	341	30.8	<b><u>340</u></b>	<b><u>30.8</u></b>
456.hammer	106	88.3	106	88.2	<b><u>106</u></b>	<b><u>88.2</u></b>	106	88.3	106	88.2	<b><u>106</u></b>	<b><u>88.2</u></b>
458.sjeng	<b><u>338</u></b>	<b><u>35.8</u></b>	338	35.8	337	35.9	333	36.3	333	36.3	<b><u>333</u></b>	<b><u>36.3</u></b>
462.libquantum	2.73	7580	<b><u>2.71</u></b>	<b><u>7640</u></b>	2.69	7710	2.73	7580	<b><u>2.71</u></b>	<b><u>7640</u></b>	2.69	7710
464.h264ref	<b><u>385</u></b>	<b><u>57.5</u></b>	384	57.6	386	57.4	<b><u>385</u></b>	<b><u>57.5</u></b>	384	57.6	386	57.4
471.omnetpp	129	48.5	130	48.1	<b><u>129</u></b>	<b><u>48.4</u></b>	111	56.3	111	56.4	<b><u>111</u></b>	<b><u>56.3</u></b>
473.astar	191	36.7	<b><u>191</u></b>	<b><u>36.8</u></b>	189	37.1	<b><u>191</u></b>	<b><u>36.8</u></b>	191	36.7	190	36.9
483.xalancbmk	88.2	78.3	<b><u>88.3</u></b>	<b><u>78.2</u></b>	90.1	76.5	80.1	86.2	<b><u>79.7</u></b>	<b><u>86.6</u></b>	79.5	86.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Platform Notes

BIOS Configuration:

Intel Hyperthreading Option set to Disabled  
Power Profile set to Custom  
Power Regulator set to Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C1E State  
Minimum Processor Idle Power Package C-State set to No Package State  
Collaborative Power Control set to Disabled  
QPI Snoop Configuration set to Home Snoop  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Double Refresh Rate set to 1x Refresh  
Energy Performance Bias set to Maximum Performance

Sysinfo program /home/specuser/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-7m51 Fri May 13 16:20:30 2016

This section contains SUT (System Under Test) info as seen by  
Continued on next page



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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 20
    siblings  : 20
    physical 0: cores 0 2 3 4 9 10 11 12 16 17 18 19 20 24 25 26 27 28
    physical 1: cores 0 2 3 4 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  cache size : 51200 KB

```

```

From /proc/meminfo
MemTotal:      529092452 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-7m51 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 13 16:19

```

SPEC is set to: /home/specuser/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   703G  13G  690G   2% /home
Additional information from dmidecode:

```

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## Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P92 04/12/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/specuser/cpu2006/libs/32:/home/specuser/cpu2006/libs/64:/home/specuser/cpu2006/sh"

OMP\_NUM\_THREADS = "40"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

Continued on next page

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Page 4



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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

Continued on next page

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Page 5



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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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