



# SPEC® CINT2006 Result

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## NEC Corporation

**SPECint®2006 = 60.4**

Express5800/R120g-1M (Intel Xeon E5-2650 v4)

**SPECint\_base2006 = 58.0**

CPU2006 license: 9006

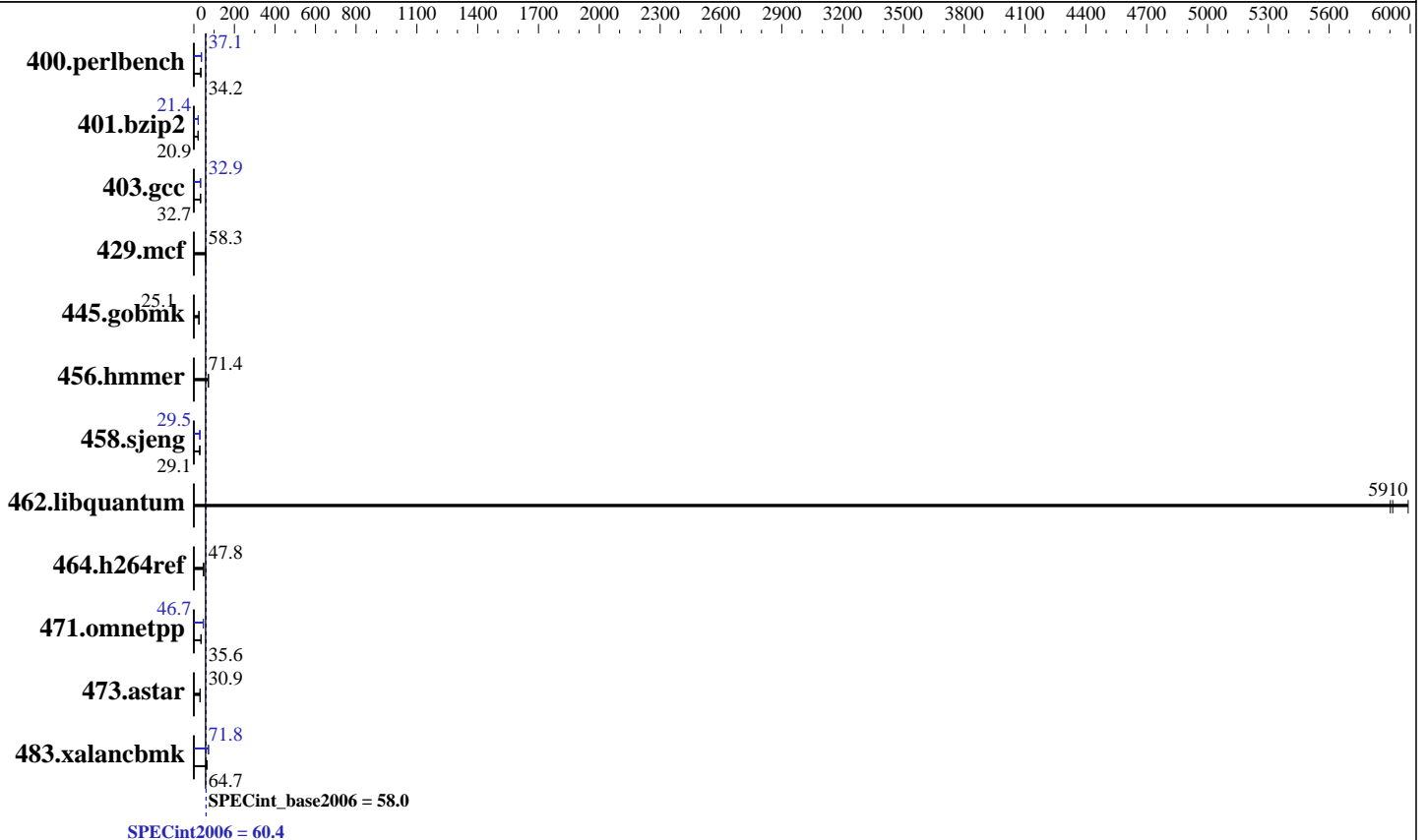
Test date: May-2016

Test sponsor: NEC Corporation

Hardware Availability: Apr-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



### Hardware

CPU Name: Intel Xeon E5-2650 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.4.5.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	285	34.2	286	34.2	<b><u>286</u></b>	<b><u>34.2</u></b>	262	37.3	<b><u>263</u></b>	<b><u>37.1</u></b>	264	37.0
401.bzip2	457	21.1	<b><u>461</u></b>	<b><u>20.9</u></b>	462	20.9	452	21.4	<b><u>452</u></b>	<b><u>21.4</u></b>	451	21.4
403.gcc	<b><u>246</u></b>	<b><u>32.7</u></b>	246	32.7	246	32.7	<b><u>245</u></b>	<b><u>32.9</u></b>	245	32.9	248	32.5
429.mcf	157	58.0	<b><u>156</u></b>	<b><u>58.3</u></b>	156	58.4	157	58.0	<b><u>156</u></b>	<b><u>58.3</u></b>	156	58.4
445.gobmk	<b><u>418</u></b>	<b><u>25.1</u></b>	418	25.1	418	25.1	<b><u>418</u></b>	<b><u>25.1</u></b>	418	25.1	418	25.1
456.hammer	<b><u>131</u></b>	<b><u>71.4</u></b>	131	71.4	131	71.4	<b><u>131</u></b>	<b><u>71.4</u></b>	131	71.4	131	71.4
458.sjeng	415	29.1	416	29.1	<b><u>415</u></b>	<b><u>29.1</u></b>	411	29.5	410	29.5	<b><u>411</u></b>	<b><u>29.5</u></b>
462.libquantum	3.46	5990	<b><u>3.50</u></b>	<b><u>5910</u></b>	3.51	5900	3.46	5990	<b><u>3.50</u></b>	<b><u>5910</u></b>	3.51	5900
464.h264ref	463	47.8	461	48.0	<b><u>463</u></b>	<b><u>47.8</u></b>	463	47.8	461	48.0	<b><u>463</u></b>	<b><u>47.8</u></b>
471.omnetpp	177	35.2	<b><u>175</u></b>	<b><u>35.6</u></b>	175	35.7	<b><u>134</u></b>	<b><u>46.7</u></b>	135	46.5	134	46.7
473.astar	227	30.9	<b><u>228</u></b>	<b><u>30.9</u></b>	228	30.8	227	30.9	<b><u>228</u></b>	<b><u>30.9</u></b>	228	30.8
483.xalancbmk	106	64.8	<b><u>107</u></b>	<b><u>64.7</u></b>	115	59.8	96.0	71.9	96.1	71.8	<b><u>96.0</u></b>	<b><u>71.8</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Power Management Policy: Custom  
Energy Performance: Performance  
Patrol Scrub: Disabled  
Snoop Mode: Home Snoop with Directory  
Hyper-Threading: Disabled

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"  
OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

The Express5800/R120g-1M (Intel Xeon E5-2650 v4) and

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## General Notes (Continued)

the Express5800/R120g-2M (Intel Xeon E5-2650 v4) models are electronically equivalent. The results have been measured on the Express5800/R120g-2M (Intel Xeon E5-2650 v4) model.

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
 -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

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## Peak Optimization Flags (Continued)

456.hmmcr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120g-RevC.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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