



# SPEC® CINT2006 Result

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## Supermicro

SuperServer 6028R-TR  
(X10DRi , Intel Xeon E5-2695 v4)

SPECint®2006 = 67.4

SPECint\_base2006 = 65.7

CPU2006 license: 001176

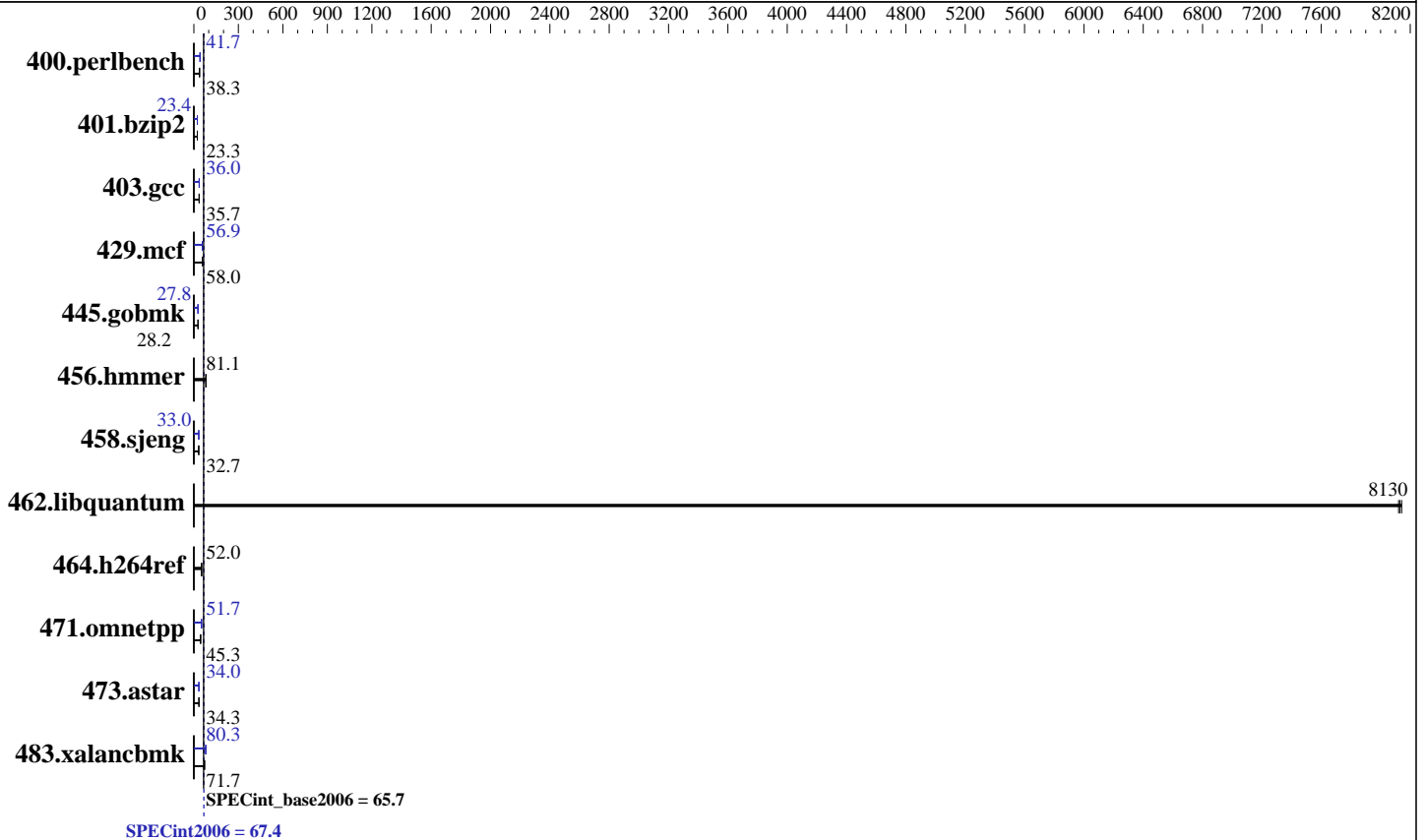
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Xeon E5-2695 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 300 GB SATA III , 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64), Kernel 3.12.49-11-default  
 Compiler: C/C++; Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 5  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	260	37.6	254	38.4	<u>255</u>	<u>38.3</u>	234	41.7	<u>235</u>	<u>41.7</u>	235	41.6
401.bzip2	414	23.3	<u>414</u>	<u>23.3</u>	415	23.2	<u>413</u>	<u>23.4</u>	413	23.4	413	23.4
403.gcc	<u>225</u>	<u>35.7</u>	225	35.7	226	35.7	223	36.1	224	36.0	<u>224</u>	<u>36.0</u>
429.mcf	<u>157</u>	<u>58.0</u>	157	58.2	159	57.4	160	57.1	<u>160</u>	<u>56.9</u>	161	56.8
445.gobmk	<u>372</u>	<u>28.2</u>	372	28.2	371	28.3	377	27.8	378	27.8	<u>378</u>	<u>27.8</u>
456.hammer	<u>115</u>	<u>81.1</u>	115	81.3	115	81.0	<u>115</u>	<u>81.1</u>	115	81.3	115	81.0
458.sjeng	370	32.7	<u>370</u>	<u>32.7</u>	371	32.7	<u>366</u>	<u>33.0</u>	366	33.0	367	32.9
462.libquantum	2.55	8120	<u>2.55</u>	<u>8130</u>	2.54	8140	2.55	8120	<u>2.55</u>	<u>8130</u>	2.54	8140
464.h264ref	<u>425</u>	<u>52.0</u>	425	52.1	426	52.0	<u>425</u>	<u>52.0</u>	425	52.1	426	52.0
471.omnetpp	139	45.0	<u>138</u>	<u>45.3</u>	137	45.5	121	51.7	121	51.9	<u>121</u>	<u>51.7</u>
473.astar	204	34.4	205	34.3	<u>205</u>	<u>34.3</u>	208	33.8	<u>207</u>	<u>34.0</u>	206	34.0
483.xalancbmk	96.0	71.9	96.5	71.5	<u>96.3</u>	<u>71.7</u>	86.5	79.7	85.9	80.3	<u>85.9</u>	<u>80.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Early Snoop = Disable

Home Dir Snoop with IVT- Style OSB = Disable

Enforce POR = Disabled

Hyper-Threading (ALL) = Disable

Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1

running on 113-172 Wed Mar 30 18:35:09 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2695 v4 @ 2.10GHz

2 "physical id"s (chips)

36 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

Continued on next page

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info@spec.org

<http://www.spec.org/>



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### Platform Notes (Continued)

```
caution.)
  cpu cores : 18
  siblings  : 18
  physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

```
From /proc/meminfo
MemTotal:      264363960 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux 113-172 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 Mar 30 17:30
```

```
SPEC is set to: /home/SPEC2K6/SPEC2006-V12
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/md126p4    xfs   237G  7.3G  230G   4% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 2.0 12/30/2015

Memory:  
16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,3"

LD\_LIBRARY\_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

OMP\_NUM\_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalanbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2 (continued):  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>

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