



# SPEC® CINT2006 Result

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## HITACHI

**SPECint®2006 = 70.1**

BladeSymphony BS520H (Intel Xeon E5-2699 v4)

**SPECint\_base2006 = 68.1**

CPU2006 license: 35

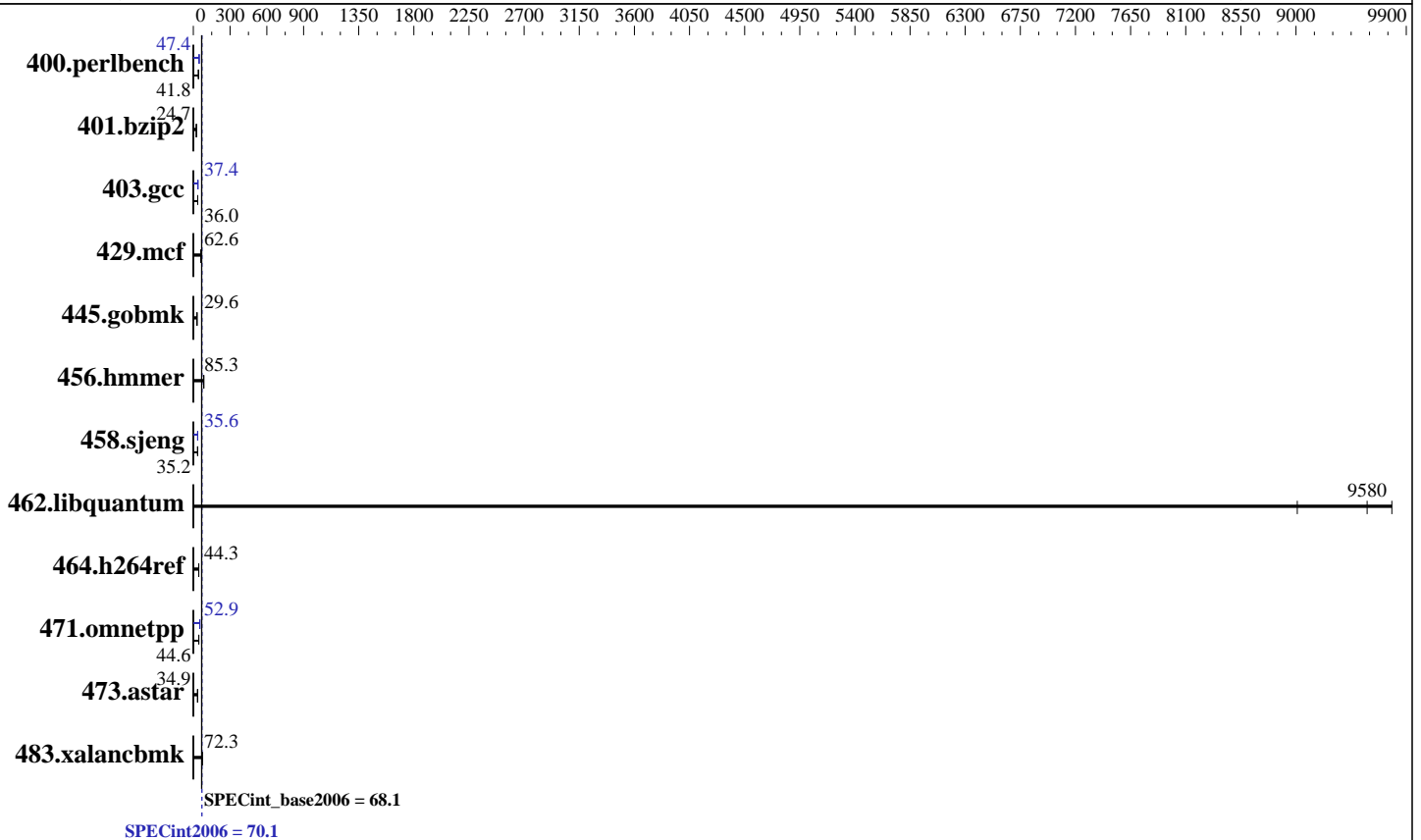
Test date: Mar-2016

Test sponsor: HITACHI

Hardware Availability: Jun-2016

Tested by: HITACHI

Software Availability: Nov-2015



### Hardware

CPU Name: Intel Xeon E5-2699 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 55 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 2 x 300 GB SAS, 15000 RPM, RAID1  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	232	42.1	234	41.7	<b><u>234</u></b>	<b><u>41.8</u></b>	206	47.4	<b><u>206</u></b>	<b><u>47.4</u></b>	208	46.9
401.bzip2	391	24.7	<b><u>390</u></b>	<b><u>24.7</u></b>	390	24.8	391	24.7	<b><u>390</u></b>	<b><u>24.7</u></b>	390	24.8
403.gcc	224	35.9	<b><u>224</u></b>	<b><u>36.0</u></b>	224	36.0	<b><u>215</u></b>	<b><u>37.4</u></b>	215	37.4	215	37.5
429.mcf	146	62.6	<b><u>146</u></b>	<b><u>62.6</u></b>	146	62.5	146	62.6	<b><u>146</u></b>	<b><u>62.6</u></b>	146	62.5
445.gobmk	<b><u>354</u></b>	<b><u>29.6</u></b>	354	29.6	345	30.4	<b><u>354</u></b>	<b><u>29.6</u></b>	354	29.6	345	30.4
456.hammer	110	84.5	109	85.5	<b><u>109</u></b>	<b><u>85.3</u></b>	110	84.5	109	85.5	<b><u>109</u></b>	<b><u>85.3</u></b>
458.sjeng	<b><u>344</u></b>	<b><u>35.2</u></b>	343	35.3	345	35.1	<b><u>340</u></b>	<b><u>35.6</u></b>	340	35.6	342	35.3
462.libquantum	<b><u>2.16</u></b>	<b><u>9580</u></b>	2.12	9780	2.30	9010	<b><u>2.16</u></b>	<b><u>9580</u></b>	2.12	9780	2.30	9010
464.h264ref	499	44.4	<b><u>499</u></b>	<b><u>44.3</u></b>	501	44.2	499	44.4	<b><u>499</u></b>	<b><u>44.3</u></b>	501	44.2
471.omnetpp	141	44.3	140	44.7	<b><u>140</u></b>	<b><u>44.6</u></b>	117	53.5	121	51.8	<b><u>118</u></b>	<b><u>52.9</u></b>
473.astar	<b><u>201</u></b>	<b><u>34.9</u></b>	202	34.8	200	35.1	<b><u>201</u></b>	<b><u>34.9</u></b>	202	34.8	200	35.1
483.xalancbmk	<b><u>95.4</u></b>	<b><u>72.3</u></b>	95.3	72.4	95.5	72.2	<b><u>95.4</u></b>	<b><u>72.3</u></b>	95.3	72.4	95.5	72.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Patrol Scrub = Disable  
Per Core P-state = Disable  
EarlySnoopPreference=Disable

Sysinfo program /home/speccpu2006/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on rhel7.2 Fri Mar 4 10:16:38 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz  
2 "physical id"s (chips)  
88 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

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### Platform Notes (Continued)

```

caution.)
  cpu cores : 22
  siblings  : 44
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
  cache size : 56320 KB

```

```

From /proc/meminfo
MemTotal:      527315512 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux rhel7.2 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 4 09:26

```

SPEC is set to: /home/speccpu2006/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs  225G  7.8G  217G   4% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS HITACHI 10-00 01/29/2016
Memory:
8x NO DIMM Unknown
16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/speccpu2006/cpu2006/libs/32:/home/speccpu2006/cpu2006/libs/64:/home/speccpu2006/cpu2006/sh"

OMP\_NUM\_THREADS = "44"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 are electronically equivalent.

The results have been measured on a Hitachi Compute Blade 520H.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: basepeak = yes

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-alloc  
-opt-malloc-options=3 -auto-ilp32

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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