SPEC® CFP2006 Result
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Supermicro

SuperServer 6028R-WTR
(X10DRW-i , Intel Xeon E5-2699 v4)

$SPECfp^{®}2006 = $ 124
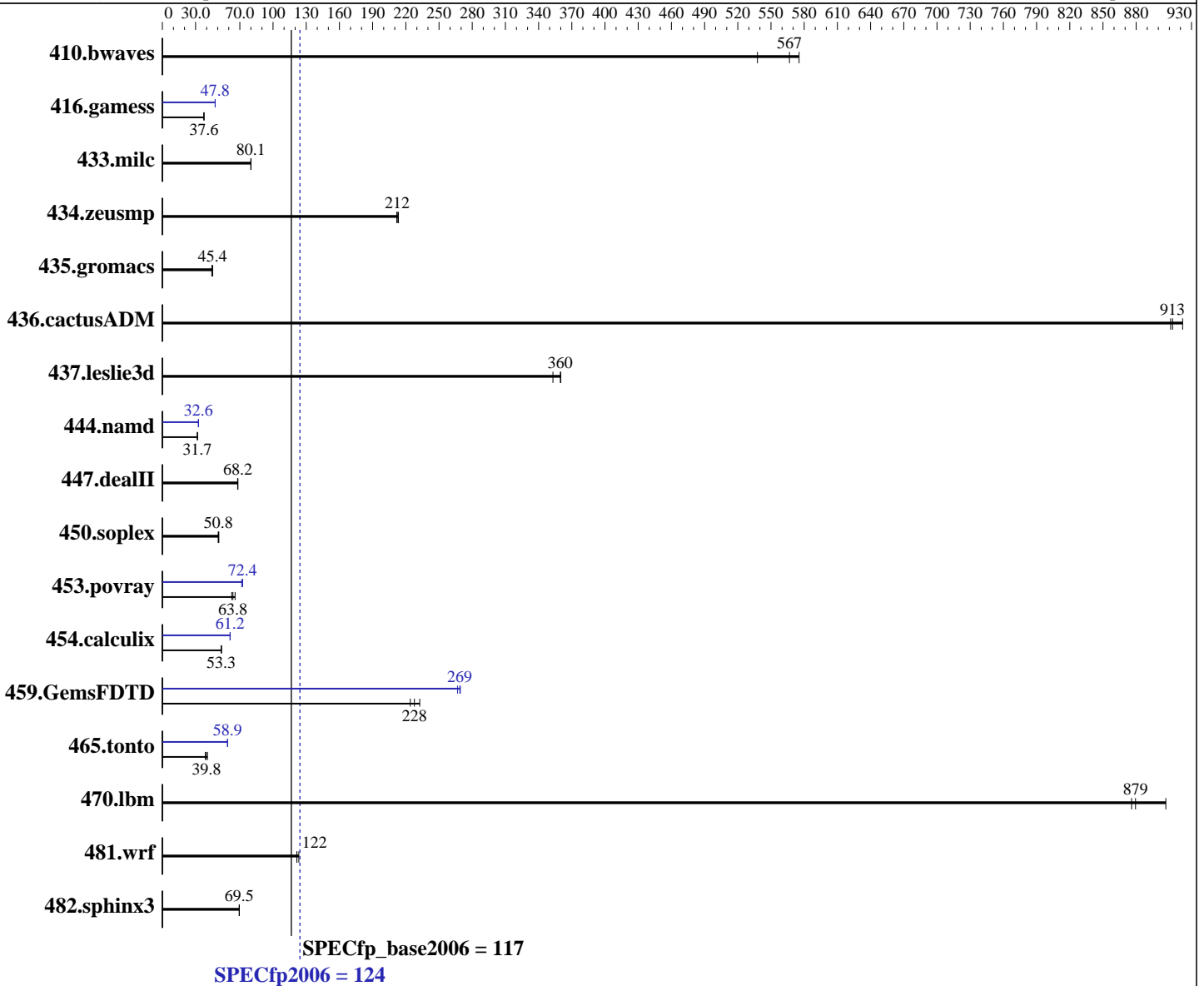
$SPECfp_base2006 = $ 117

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015

410.bwaves 567
416.gamess 47.8 / 37.6
433.milc 80.1
434.zeusmp 212
435.gromacs 45.4
436.cactusADM 913
437.leslie3d 360
444.namd 32.6 / 31.7
447.dealII 68.2
450.soplex 50.8
453.povray 72.4 / 63.8
454.calculix 61.2 / 53.3
459.GemsFDTD 269 / 228
465.tonto 58.9 / 39.8
470.lbm 879
481.wrf 122
482.sphinx3 69.5

SPECfp_base2006 = 117

SPECfp2006 = 124

Hardware

CPU Name: Intel Xeon E5-2699 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, Kernel 3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

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L3 Cache:	55 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)	Other Software:	None
Disk Subsystem:	1 x 200 GB SATA III SSD		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	23.6	575	25.3	538	**24.0**	**567**	23.6	575	25.3	538	**24.0**	**567**
416.gamess	521	37.6	521	37.6	**521**	**37.6**	410	47.8	410	47.7	**410**	**47.8**
433.milc	**115**	**80.1**	115	79.9	115	80.2	**115**	**80.1**	115	79.9	115	80.2
434.zeusmp	42.7	213	**42.9**	**212**	43.0	212	42.7	213	**42.9**	**212**	43.0	212
435.gromacs	157	45.5	160	44.6	**157**	**45.4**	157	45.5	160	44.6	**157**	**45.4**
436.cactusADM	13.1	911	13.0	922	**13.1**	**913**	13.1	911	13.0	922	**13.1**	**913**
437.leslie3d	26.1	360	**26.1**	**360**	26.6	353	26.1	360	**26.1**	**360**	26.6	353
444.namd	253	31.7	253	31.7	**253**	**31.7**	246	32.6	**246**	**32.6**	246	32.6
447.dealII	168	68.3	**168**	**68.2**	168	68.0	168	68.3	**168**	**68.2**	168	68.0
450.soplex	**164**	**50.8**	163	51.1	165	50.4	**164**	**50.8**	163	51.1	165	50.4
453.povray	80.9	65.7	84.9	62.7	**83.4**	**63.8**	**73.5**	**72.4**	73.3	72.6	74.2	71.7
454.calculix	154	53.4	155	53.2	**155**	**53.3**	135	61.3	135	61.2	**135**	**61.2**
459.GemsFDTD	47.4	224	45.6	233	**46.6**	**228**	39.8	267	**39.5**	**269**	39.4	269
465.tonto	241	40.8	253	38.8	**247**	**39.8**	167	59.0	**167**	**58.9**	167	58.9
470.lbm	**15.6**	**879**	15.2	907	15.7	876	**15.6**	**879**	15.2	907	15.7	876
481.wrf	**91.9**	**122**	90.6	123	91.9	121	**91.9**	**122**	90.6	123	91.9	121
482.sphinx3	**280**	**69.5**	280	69.5	280	69.6	**280**	**69.5**	280	69.5	280	69.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Early Snoop = Disable
Enforce POR = Disabled
Memory Frequency = 2400
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on X10DRW-01 Wed Feb 17 17:50:44 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

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Platform Notes (Continued)

```
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
      2 "physical id"s (chips)
      88 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 22
      siblings  : 44
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
      28
   cache size : 56320 KB

From /proc/meminfo
   MemTotal:       264033332 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
   Linux X10DRW-01 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 17 17:45

SPEC is set to: /usr/cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   183G  6.9G  176G   4% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.
```

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CPU2006 license: 001176	**Test date:** Feb-2016
Test sponsor: Supermicro	**Hardware Availability:** Mar-2016
Tested by: Supermicro	**Software Availability:** Sep-2015

Platform Notes (Continued)

```
     BIOS American Megatrends Inc. 2.0 12/17/2015
     Memory:
      16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

    (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "44"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
    icc   -m64
```

C++ benchmarks:
```
    icpc  -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
```

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Hardware Availability: Mar-2016

Software Availability: Sep-2015

Base Portability Flags (Continued)

```
459.GemsFDTD: -DSPEC_CPU_LP64
   465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks:
```
icpc   -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
Same as Base Portability Flags
```

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Hardware Availability: Mar-2016
Software Availability: Sep-2015

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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