



# SPEC® CINT2006 Result

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## Supermicro

SuperWorkstation 5039A-iL  
(X11SAE , Intel Core i5-6402P)

SPECint®\_rate2006 = 191

SPECint\_rate\_base2006 = 185

CPU2006 license: 001176

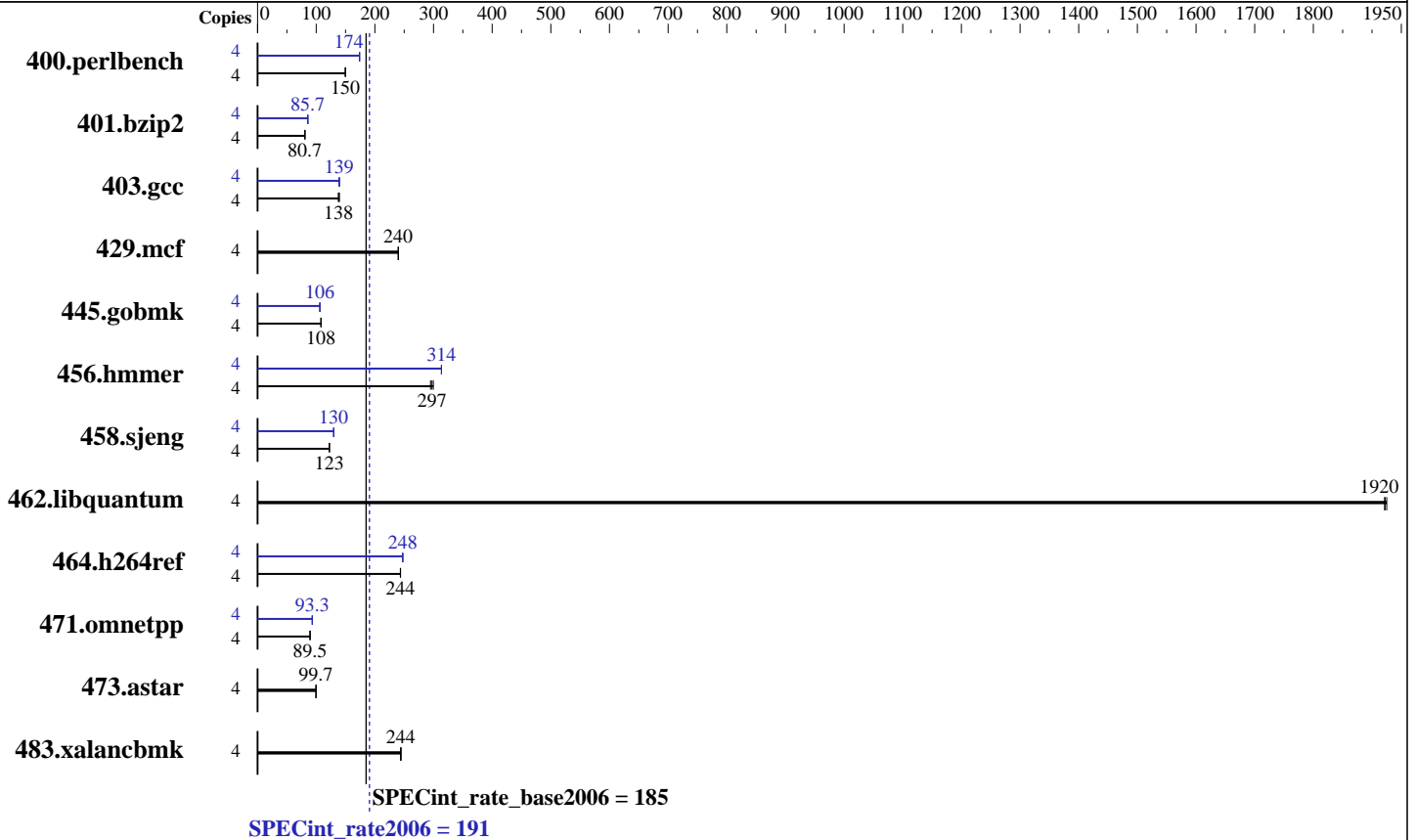
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Dec-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Core i5-6402P  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 6 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-E)  
 Disk Subsystem: 1 x 1000 GB SATA III, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>261</u>	<u>150</u>	261	150	261	150	4	<u>225</u>	<u>174</u>	224	174	225	174
401.bzip2	4	478	80.8	<u>478</u>	<u>80.7</u>	478	80.7	4	450	85.7	<u>450</u>	<u>85.7</u>	450	85.7
403.gcc	4	231	140	<u>233</u>	<u>138</u>	234	138	4	<u>231</u>	<u>139</u>	230	140	232	139
429.mcf	4	152	240	<u>152</u>	<u>240</u>	152	240	4	152	240	<u>152</u>	<u>240</u>	152	240
445.gobmk	4	<u>388</u>	<u>108</u>	387	108	389	108	4	<u>395</u>	<u>106</u>	396	106	395	106
456.hammer	4	<u>126</u>	<u>297</u>	127	295	125	300	4	119	314	<u>119</u>	<u>314</u>	119	313
458.sjeng	4	396	122	<u>395</u>	<u>123</u>	395	123	4	<u>373</u>	<u>130</u>	373	130	373	130
462.libquantum	4	43.1	1920	43.1	1930	<u>43.1</u>	<u>1920</u>	4	43.1	1920	43.1	1930	<u>43.1</u>	<u>1920</u>
464.h264ref	4	<u>363</u>	<u>244</u>	363	244	364	243	4	<u>358</u>	<u>248</u>	357	248	358	247
471.omnetpp	4	279	89.6	280	89.4	<u>279</u>	<u>89.5</u>	4	268	93.4	<u>268</u>	<u>93.3</u>	269	93.1
473.astar	4	<u>282</u>	<u>99.7</u>	281	99.8	282	99.6	4	<u>282</u>	<u>99.7</u>	281	99.8	282	99.6
483.xalancbmk	4	113	245	<u>113</u>	<u>244</u>	113	244	4	113	245	<u>113</u>	<u>244</u>	113	244

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Mon Jan 25 04:50:35 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Core(TM) i5-6402P CPU @ 2.80GHz  
1 "physical id"s (chips)  
4 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 4  
siblings : 4

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### Platform Notes (Continued)

physical 0: cores 0 1 2 3  
cache size : 6144 KB

From /proc/meminfo  
MemTotal: 65581696 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*  
os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.1 (Maipo)"  
ID="rhel"  
ID\_LIKE="fedora"  
VERSION\_ID="7.1"  
PRETTY\_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"  
ANSI\_COLOR="0;31"  
CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.1:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.1:ga:server

uname -a:  
Linux localhost.localdomain 3.10.0-229.el7.x86\_64 #1 SMP Thu Jan 29 18:37:38  
EST 2015 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jan 25 04:48

SPEC is set to: /home/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 850G 3.5G 846G 1% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 12/01/2015  
Memory:  
4x Samsung M391A2K43BB1-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

## Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div  
429.mcf: basepeak = yes

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## Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
-opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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