



SPEC[®] CFP2006 Result

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Supermicro

Supermicro C7Z170-SQ motherboard
(C7Z170-SQ , Intel Core i5-6600)

SPECfp[®]_rate2006 = 180

SPECfp_rate_base2006 = 176

CPU2006 license: 001176

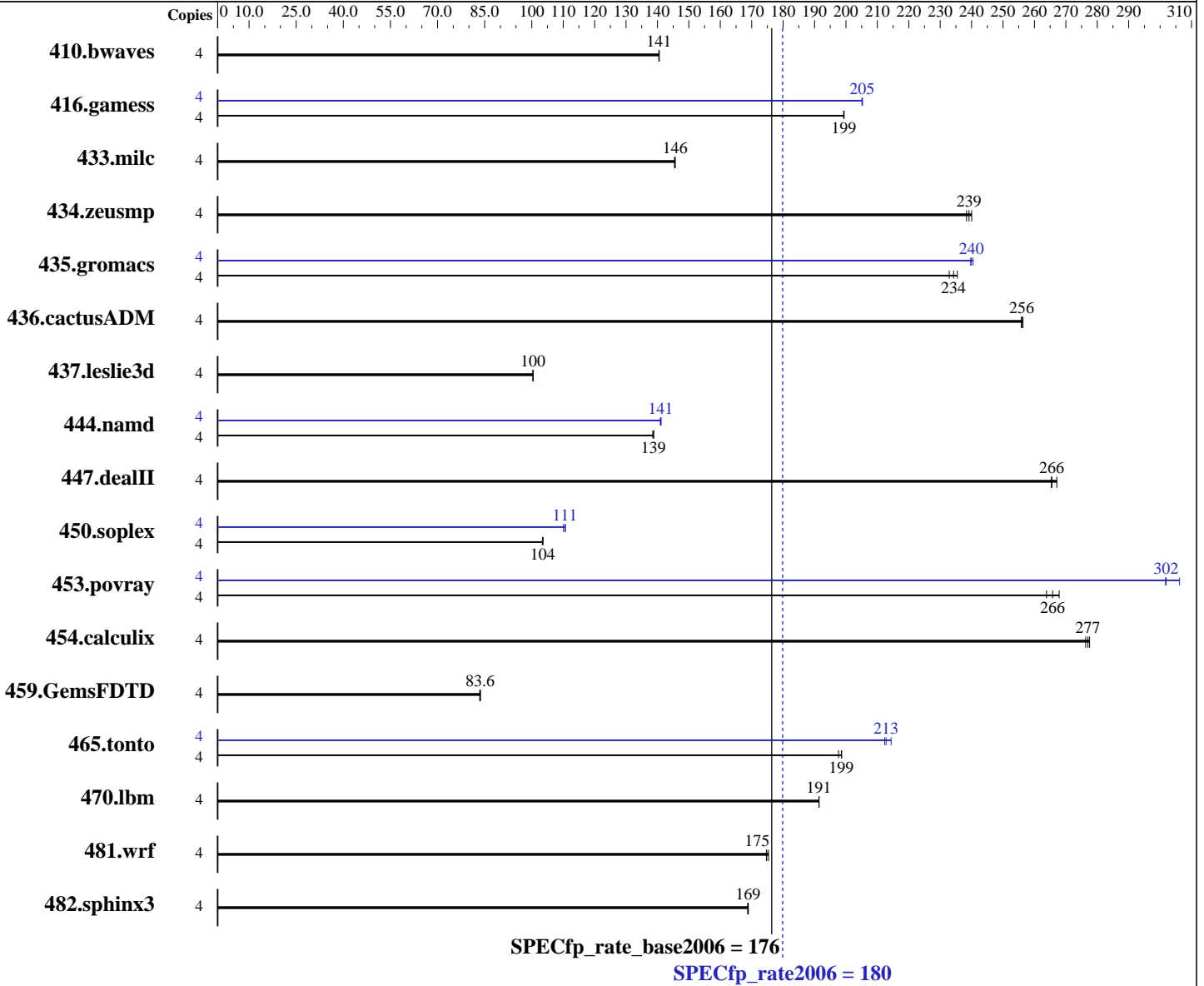
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2015

Hardware Availability: Sep-2015

Software Availability: Sep-2015



Hardware

CPU Name: Intel Core i5-6600
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2133P-U)
Disk Subsystem: 1 x 400 GB SATA III SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	387	141	387	140	<u>387</u>	<u>141</u>	4	387	141	387	140	<u>387</u>	<u>141</u>
416.gamess	4	393	199	<u>393</u>	<u>199</u>	393	199	4	<u>382</u>	<u>205</u>	382	205	382	205
433.milc	4	252	146	252	145	<u>252</u>	<u>146</u>	4	252	146	252	145	<u>252</u>	<u>146</u>
434.zeusmp	4	<u>152</u>	<u>239</u>	153	238	152	240	4	<u>152</u>	<u>239</u>	153	238	152	240
435.gromacs	4	121	235	<u>122</u>	<u>234</u>	123	233	4	119	240	<u>119</u>	<u>240</u>	119	240
436.cactusADM	4	<u>187</u>	<u>256</u>	187	256	186	256	4	<u>187</u>	<u>256</u>	187	256	186	256
437.leslie3d	4	374	100	375	100	<u>375</u>	<u>100</u>	4	374	100	375	100	<u>375</u>	<u>100</u>
444.namd	4	231	139	<u>231</u>	<u>139</u>	232	139	4	227	141	228	141	<u>227</u>	<u>141</u>
447.dealII	4	<u>172</u>	<u>266</u>	172	265	171	267	4	<u>172</u>	<u>266</u>	172	265	171	267
450.soplex	4	322	104	323	103	<u>322</u>	<u>104</u>	4	303	110	301	111	<u>302</u>	<u>111</u>
453.povray	4	79.4	268	80.6	264	<u>80.1</u>	<u>266</u>	4	69.5	306	<u>70.5</u>	<u>302</u>	70.5	302
454.calculix	4	<u>119</u>	<u>277</u>	119	276	119	278	4	<u>119</u>	<u>277</u>	119	276	119	278
459.GemsFDTD	4	507	83.6	509	83.4	<u>508</u>	<u>83.6</u>	4	507	83.6	509	83.4	<u>508</u>	<u>83.6</u>
465.tonto	4	198	199	<u>198</u>	<u>199</u>	199	198	4	185	212	<u>185</u>	<u>213</u>	184	214
470.lbm	4	287	191	<u>287</u>	<u>191</u>	287	191	4	287	191	<u>287</u>	<u>191</u>	287	191
481.wrf	4	256	175	255	175	<u>256</u>	<u>175</u>	4	256	175	255	175	<u>256</u>	<u>175</u>
482.sphinx3	4	<u>462</u>	<u>169</u>	462	169	461	169	4	<u>462</u>	<u>169</u>	462	169	461	169

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

As tested, the system used a Supermicro CSE-732G-903B chassis.
The chassis is configured with a PWS-903-PQ power supply, 1 SNK-P0051AP4 heatsink, as well as 1 FAN-0124L4 rear cooling fan.
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914

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Software Availability: Sep-2015

Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Dec 1 10:13:25 2015

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6600 CPU @ 3.30GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 0 1 2 3
 cache size : 6144 KB
```

```
From /proc/meminfo
MemTotal:      16169696 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38
EST 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 1 05:07
```

```
SPEC is set to: /usr/cpu2006
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        xfs       369G      164G  206G  45% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. T20151015150001 10/15/2015

Memory:

4x Micron 8ATF51264AZ-2G1A2 4 GB 1 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
         -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
         -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
         -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
          -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
          -par-num-threads=1(pass 1) -prof-use(pass 2)
          -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
          -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
          -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
          -ansi-alias

```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



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