



# SPEC® CFP2006 Result

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Hewlett-Packard Company

(Test Sponsor: Intel Corporation)

HP ENVY 15 Notebook PC 15t-j100 (Intel Core i5-4300M)

**SPECfp®2006 = 57.5**

**SPECfp\_base2006 = 56.4**

CPU2006 license: 13

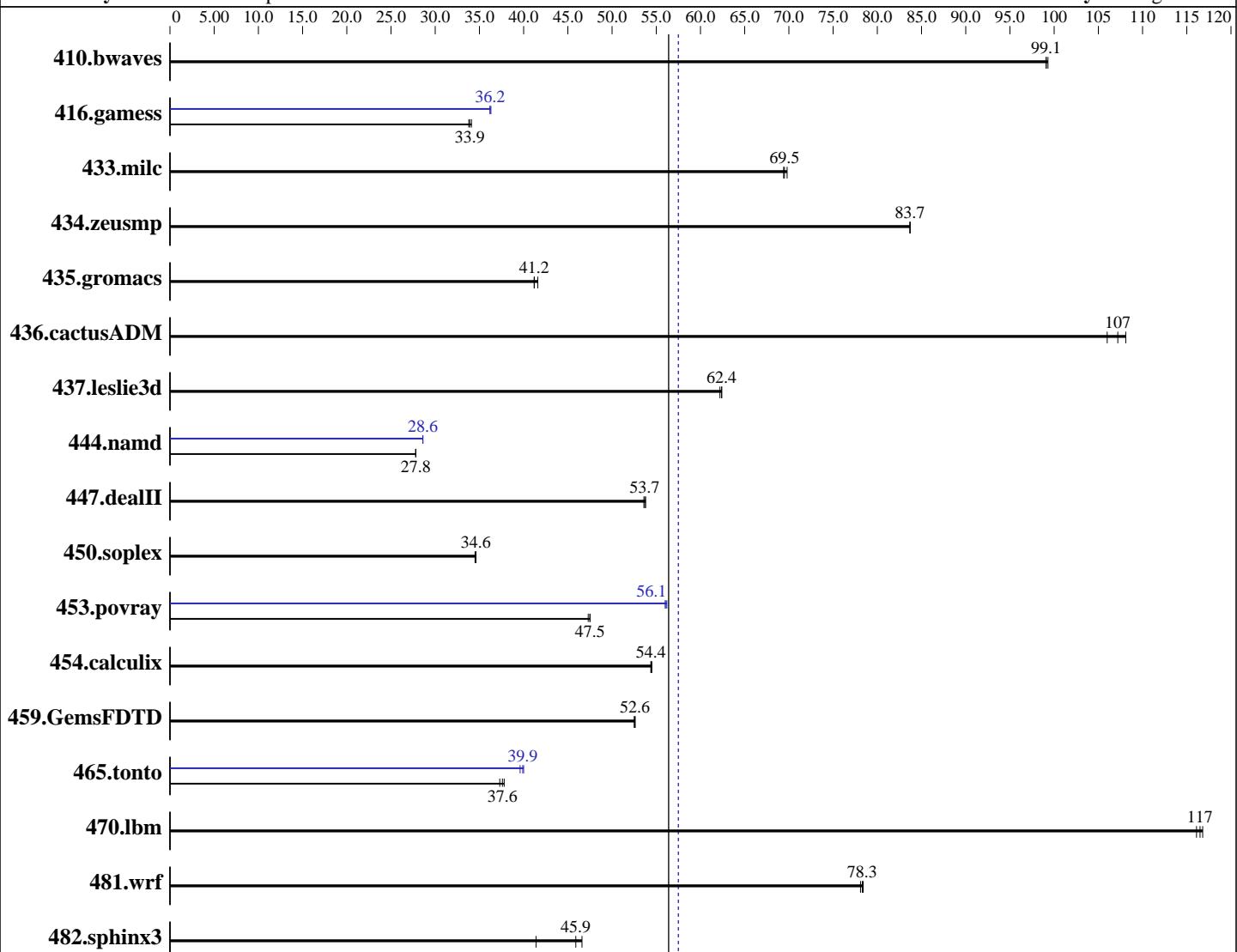
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2015

Hardware Availability: May-2014

Software Availability: Aug-2015



**SPECfp\_base2006 = 56.4**

**SPECfp2006 = 57.5**

## Hardware

CPU Name: Intel Core i5-4300M  
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
CPU MHz: 2600  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Microsoft Windows 10 Pro 10.0.10240 N/A Build 10240  
Compiler: C/C++: Version 16.0.0.110 of Intel C++ Studio XE for Windows;  
Fortran: Version 16.0.0.110 of Intel Fortran Studio XE for Windows;  
Libraries: Version 18.00.30723 of Microsoft Visual Studio 2013  
Auto Parallel: Yes

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
Trying 'systeminfo'
OS Name      : Microsoft Windows 10 Pro
OS Version   : 10.0.10240 N/A Build 10240
System Manufacturer: Hewlett-Packard
System Model  : HP ENVY 15 Notebook PC
Processor(s)  : 1 Processor(s) Installed.
                 [01]: Intel64 Family 6 Model 60 Stepping 3 GenuineIntel ~1600 Mhz
BIOS Version  : Insyde F.35, 10/3/2013
Total Physical Memory: 8,128 MB
```

```
Trying 'wmic cpu get /value'
DeviceID     : CPU0
L2CacheSize  : 256
L3CacheSize  : 3072
MaxClockSpeed: 2601
Name         : Intel(R) Core(TM) i5-4300M CPU @ 2.60GHz
NumberOfCores: 2
NumberOfLogicalProcessors: 4
```

(End of data from sysinfo program)

## General Notes

450.soplex (base): "getline\_test" src.alt was used.

447.dealII (base): "max\_prototype" src.alt was used.  
447.dealII (base): "cxx11\_make\_pair" src.alt was used.

450.soplex (base): "getline\_test" src.alt was used.

447.dealII (base): "max\_prototype" src.alt was used.  
447.dealII (base): "cxx11\_make\_pair" src.alt was used.

OMP\_NUM\_THREADS set to number of processors cores  
KMP\_AFFINITY set to granularity=fine,scatter  
Binaries compiled on a system with 1x Intel Xeon E5-2699 v3 CPU  
+ 64GB memory using Windows 8.1 Enterprise 64-bit

## Base Compiler Invocation

C benchmarks:

  icl -Qvc12 -Qstd=c99

C++ benchmarks:

  icl -Qvc12

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc12 -Qstd=c99 ifort

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
    433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
    437.leslie3d: -DSPEC_CPU_P64
        444.namd: -DSPEC_CPU_P64 /TP
    447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
        -DSPEC_CPU_BOOST_CONFIG_MSC_VER -DSPEC_NEED_ALGORITHM
    450.soplex: -DSPEC_CPU_P64 -DSPEC_GETLINE_TEST
    453.povray: -DSPEC_CPU_P64
    454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
    465.tonto: -DSPEC_CPU_P64
    470.lbm: -DSPEC_CPU_P64
        481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
    482.sphinx3: -DSPEC_CPU_P64

```

## Base Optimization Flags

C benchmarks:

```
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch /F1000000000
```

C++ benchmarks:

```
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qcxx-features /F1000000000 shlw64M.lib
    -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch /F1000000000
```

Benchmarks using both Fortran and C:

```
-QxCORE-AVX2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch /F1000000000
```



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## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc12 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc12
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc12 -Qstd=c99 ifort
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -QxCORE-AVX2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Oa /F10000000000 shlw64M.lib
           -link /FORCE:MULTIPLE
```

```
447.dealII: basepeak = yes
```

```
450.soplex: basepeak = yes
```

```
453.povray: -QxCORE-AVX2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qunroll14 -Qansi-alias /F10000000000
            shlw64M.lib -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -QxCORE-AVX2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qunroll12 -Ob0 -Qansi-alias
            -Qscalar-rep- /F10000000000
```

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxCORE-AVX2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll14 -Qauto -Qinline-calloc  
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-windows.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-windows.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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