# SPEC® CFP2006 Result

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## Huawei

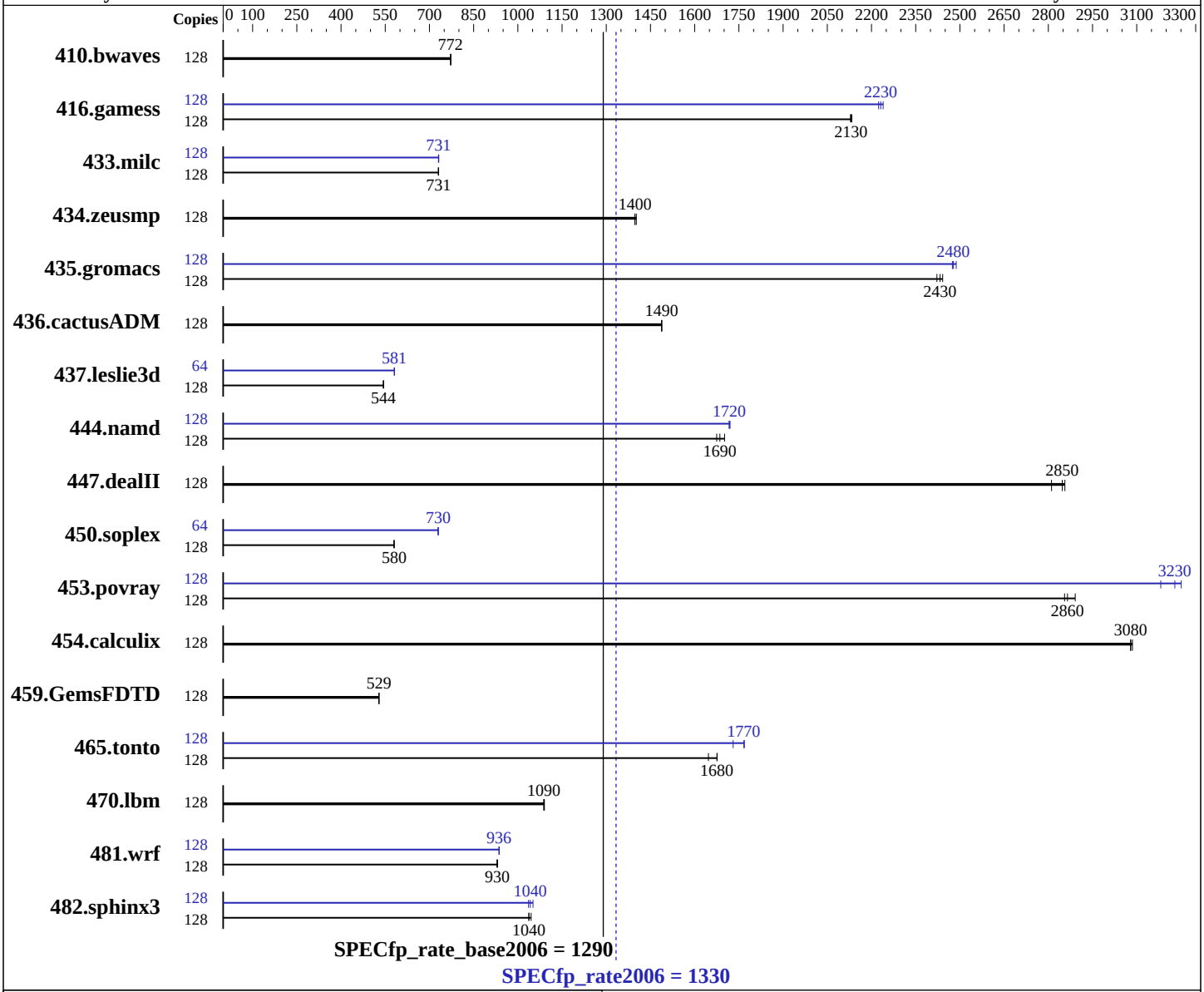
## Huawei RH5885 V3 (Intel Xeon E7-8867 v3)

**SPECfp®_rate2006 = 1330**

**SPECfp_rate_base2006 = 1290**

**CPU2006 license:** 3175
**Test sponsor:** Huawei
**Tested by:** Huawei

**Test date:** Nov-2015
**Hardware Availability:** May-2015
**Software Availability:** Oct-2014




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7-8867 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.30 GHz |
| CPU MHz: | 2500 |
| FPU: | Integrated |
| CPU(s) enabled: | 64 cores, 4 chips, 16 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 (x86_64) 3.12.28-4-default |
| Compiler: | C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 2 x 300 GB SAS, 10K RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 128 | 2252 | 772 | 2254 | 772 | **2254** | **772** | 128 | 2252 | 772 | 2254 | 772 | **2254** | **772** |
| 416.gamess | 128 | 1177 | 2130 | 1175 | 2130 | **1176** | **2130** | 128 | **1123** | **2230** | 1127 | 2220 | 1119 | 2240 |
| 433.milc | 128 | 1607 | 731 | 1610 | 730 | **1607** | **731** | 128 | 1608 | 731 | **1607** | **731** | 1606 | 732 |
| 434.zeusmp | 128 | **834** | **1400** | 834 | 1400 | 831 | 1400 | 128 | **834** | **1400** | 834 | 1400 | 831 | 1400 |
| 435.gromacs | 128 | 374 | 2440 | **376** | **2430** | 377 | 2420 | 128 | 369 | 2470 | 367 | 2490 | **369** | **2480** |
| 436.cactusADM | 128 | 1028 | 1490 | **1028** | **1490** | 1028 | 1490 | 128 | 1028 | 1490 | **1028** | **1490** | 1028 | 1490 |
| 437.leslie3d | 128 | 2210 | 544 | **2212** | **544** | 2214 | 543 | 64 | 1036 | 581 | **1035** | **581** | 1035 | 581 |
| 444.namd | 128 | 603 | 1700 | **609** | **1690** | 613 | 1670 | 128 | 598 | 1720 | 597 | 1720 | **598** | **1720** |
| 447.dealII | 128 | 521 | 2810 | **514** | **2850** | 513 | 2860 | 128 | 521 | 2810 | **514** | **2850** | 513 | 2860 |
| 450.soplex | 128 | 1839 | 580 | **1839** | **580** | 1843 | 579 | 64 | **731** | **730** | 732 | 729 | 731 | 730 |
| 453.povray | 128 | 236 | 2890 | **238** | **2860** | 239 | 2860 | 128 | 214 | 3180 | **211** | **3230** | 209 | 3250 |
| 454.calculix | 128 | 343 | 3080 | 342 | 3090 | **343** | **3080** | 128 | 343 | 3080 | 342 | 3090 | **343** | **3080** |
| 459.GemsFDTD | 128 | **2568** | **529** | 2572 | 528 | 2566 | 529 | 128 | **2568** | **529** | 2572 | 528 | 2566 | 529 |
| 465.tonto | 128 | 752 | 1680 | **752** | **1680** | 765 | 1650 | 128 | 728 | 1730 | **713** | **1770** | 712 | 1770 |
| 470.lbm | 128 | **1615** | **1090** | 1613 | 1090 | 1617 | 1090 | 128 | **1615** | **1090** | 1613 | 1090 | 1617 | 1090 |
| 481.wrf | 128 | **1537** | **930** | 1539 | 929 | 1535 | 931 | 128 | 1526 | 937 | 1528 | 936 | **1527** | **936** |
| 482.sphinx3 | 128 | 2387 | 1050 | **2405** | **1040** | 2405 | 1040 | 128 | 2408 | 1040 | 2373 | 1050 | **2394** | **1040** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set Memory Power Saving to disabled
Sysinfo program /zsn/spec1/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on RH5885V3 Mon Nov  2 08:40:13 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-8867 v3 @ 2.50GHz
      4 "physical id"s (chips)
      128 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 16
      siblings  : 32
      physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
      physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
      physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
      physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 20 24 25 27
   cache size : 46080 KB

From /proc/meminfo
   MemTotal:       529106988 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 0
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12"
      VERSION_ID="12"
      PRETTY_NAME="SUSE Linux Enterprise Server 12"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
   Linux RH5885V3 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
```

Continued on next page

## Platform Notes (Continued)

```
      (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Nov 1 18:07

   SPEC is set to: /zsn/spec1
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sdb1      ext4  823G  201G  580G  26% /zsn
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS American Megatrends Inc. BLISQ954 09/19/2015
     Memory:
      32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
      16x NO DIMM NO DIMM

   (End of data from sysinfo program)
   Regarding the sysinfo display about the memory installed, the correct amount of
   memory is 512 GB and the dmidecode description should have two lines reading as:
       32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
       16x NO DIMM NO DIMM
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/zsn/spec1/libs/32:/zsn/spec1/libs/64:/zsn/spec1/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Continued on next page

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

Continued on next page

## Peak Optimization Flags (Continued)

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.xml


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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Dec 1 17:41:05 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 December 2015.