



# SPEC® CINT2006 Result

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## Lenovo Group Limited

**SPECint®2006 = 63.4**

Lenovo System x3500 M5  
(Intel Xeon E5-2618L v3, 2.30 GHz)

**SPECint\_base2006 = 60.3**

CPU2006 license: 9017

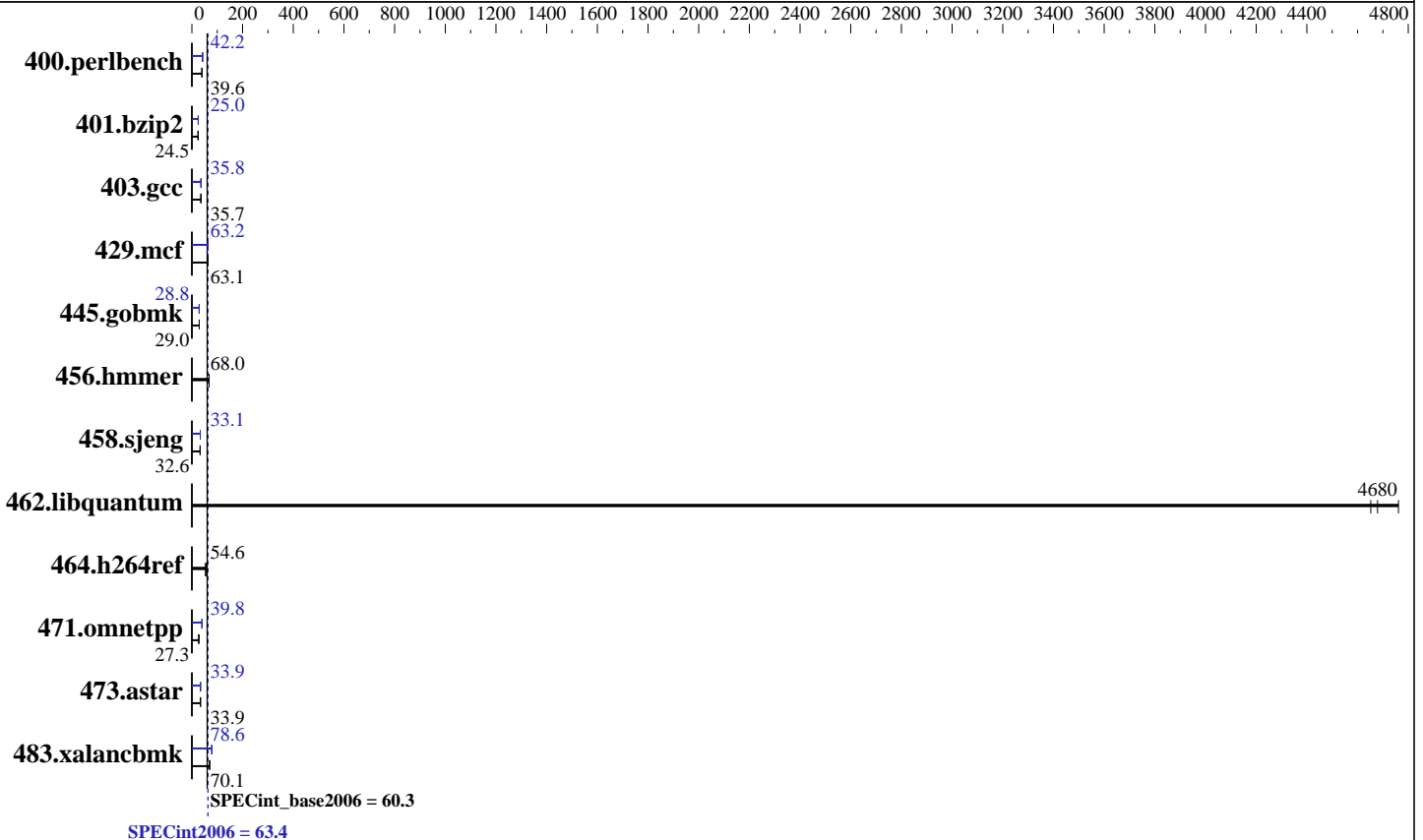
Test date: Sep-2015

Test sponsor: Lenovo Group Limited

Hardware Availability: Jan-2015

Tested by: Lenovo Group Limited

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2618L v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
 Disk Subsystem: 1 x 960 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.0 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	246	39.6	247	39.6	<b><u>247</u></b>	<b><u>39.6</u></b>	232	42.2	231	42.3	<b><u>231</u></b>	<b><u>42.2</u></b>
401.bzip2	394	24.5	<b><u>394</u></b>	<b><u>24.5</u></b>	395	24.4	<b><u>386</u></b>	<b><u>25.0</u></b>	386	25.0	386	25.0
403.gcc	225	35.7	<b><u>226</u></b>	<b><u>35.7</u></b>	226	35.6	225	35.8	<b><u>225</u></b>	<b><u>35.8</u></b>	224	35.9
429.mcf	144	63.3	145	62.9	<b><u>145</u></b>	<b><u>63.1</u></b>	146	62.7	<b><u>144</u></b>	<b><u>63.2</u></b>	144	63.4
445.gobmk	<b><u>361</u></b>	<b><u>29.0</u></b>	361	29.0	361	29.0	364	28.8	<b><u>364</u></b>	<b><u>28.8</u></b>	364	28.8
456.hammer	<b><u>137</u></b>	<b><u>68.0</u></b>	137	68.0	137	68.0	<b><u>137</u></b>	<b><u>68.0</u></b>	137	68.0	137	68.0
458.sjeng	373	32.4	<b><u>372</u></b>	<b><u>32.6</u></b>	371	32.6	365	33.1	366	33.1	<b><u>366</u></b>	<b><u>33.1</u></b>
462.libquantum	4.35	4760	4.45	4650	<b><u>4.43</u></b>	<b><u>4680</u></b>	4.35	4760	4.45	4650	<b><u>4.43</u></b>	<b><u>4680</u></b>
464.h264ref	406	54.5	<b><u>405</u></b>	<b><u>54.6</u></b>	405	54.7	406	54.5	<b><u>405</u></b>	<b><u>54.6</u></b>	405	54.7
471.omnetpp	<b><u>229</u></b>	<b><u>27.3</u></b>	229	27.3	229	27.3	157	39.8	<b><u>157</u></b>	<b><u>39.8</u></b>	158	39.6
473.astar	<b><u>207</u></b>	<b><u>33.9</u></b>	209	33.6	207	34.0	207	33.8	<b><u>207</u></b>	<b><u>33.9</u></b>	206	34.1
483.xalancbmk	<b><u>98.5</u></b>	<b><u>70.1</u></b>	98.5	70.1	98.7	69.9	<b><u>87.8</u></b>	<b><u>78.6</u></b>	<b><u>87.8</u></b>	<b><u>78.6</u></b>	88.0	78.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS setting:

Operating Mode set to "Efficiency-Favor Performance"

Hyper-threading set to "Disable"

Sysinfo program /home/SPEC\_ic16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on x3500M5 Tue Sep 29 18:39:10 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2618L v3 @ 2.30GHz

2 "physical id"s (chips)

16 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 8

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info@spec.org

<http://www.spec.org/>

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### Platform Notes (Continued)

```
siblings      : 8
physical 0:   cores 0 1 2 3 4 5 6 7
physical 1:   cores 0 1 2 3 4 5 6 7
cache size   : 20480 KB
```

From /proc/meminfo

```
MemTotal:      263457684 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

uname -a:

```
Linux x3500M5 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64
x86_64 x86_64 GNU/Linux
```

SPEC is set to: /home/SPEC\_ic16

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs  927G  147G  780G  16% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[TAE105J-1.10]- 04/20/2015

Memory:

```
6x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1866 MHz
10x Hynix HMA42GR7MFR4N-TFT1 16 GB 2 rank 2133 MHz, configured at 1866 MHz
8x NO DIMM Unknown
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/SPEC\_ic16/libs/32:/home/SPEC\_ic16/libs/64:/home/SPEC\_ic16/sh"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

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## Peak Optimization Flags (Continued)

401.bzip2 (continued):

-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc

-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel

-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

-par-num-threads=1(pass 1) -prof-use(pass 2)

-opt-ra-region-strategy=block

-ansi-alias

-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.html>



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<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-HSW-D.20150923.xml>

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