



SPEC® CINT2006 Result

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Huawei

SPECint®2006 = 53.5

Huawei CH242 V3 (Intel Xeon E7-4850 v3)

SPECint_base2006 = 51.5

CPU2006 license: 3175

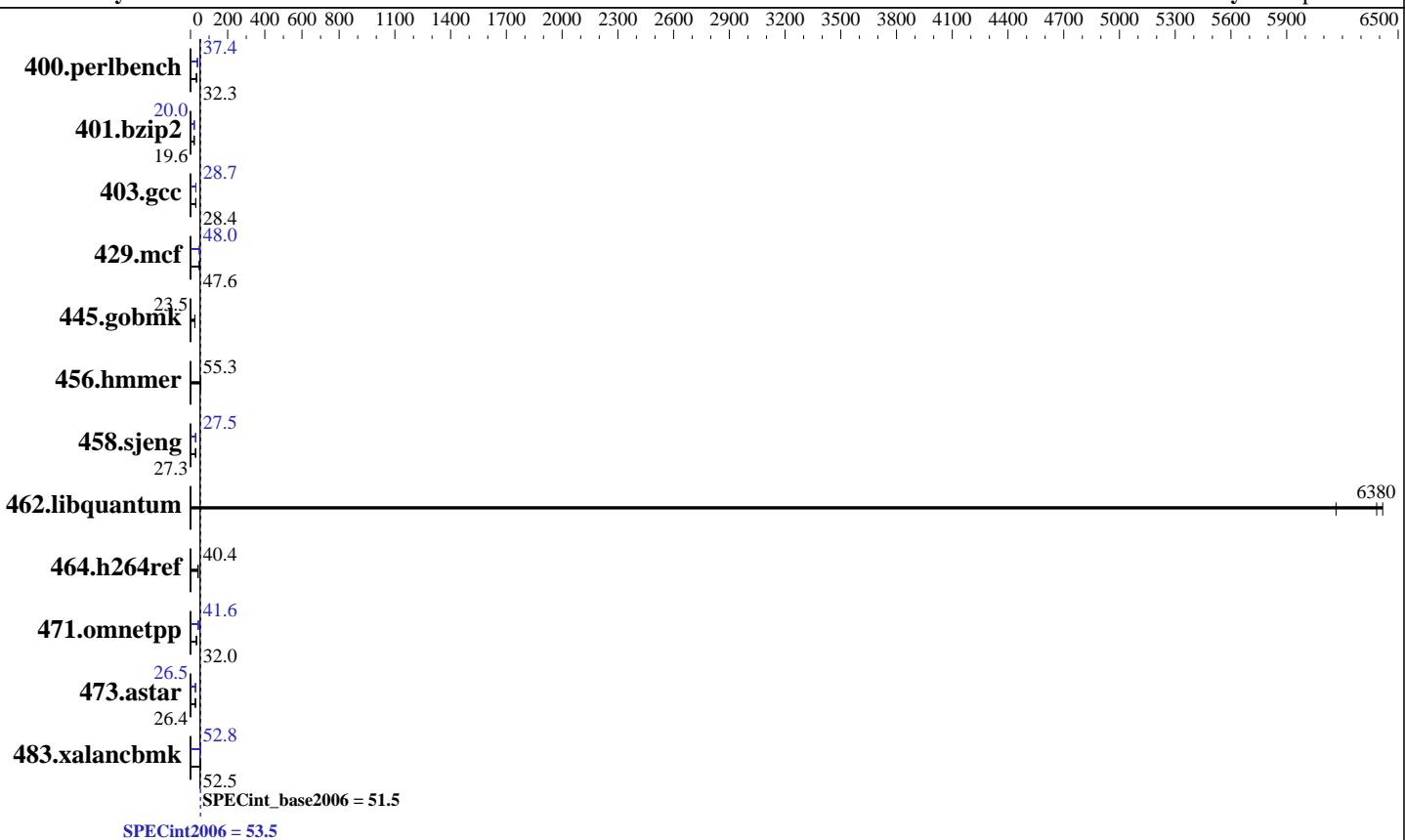
Test date: Jun-2015

Test sponsor: Huawei

Hardware Availability: May-2015

Tested by: Huawei

Software Availability: Sep-2014



Hardware		Software	
CPU Name:	Intel Xeon E7-4850 v3	Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo)
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz		3.10.0-123.el7.x86_64
CPU MHz:	2200	Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
FPU:	Integrated	Auto Parallel:	Yes
CPU(s) enabled:	56 cores, 4 chips, 14 cores/chip	File System:	xfs
CPU(s) orderable:	2,4 chips	System State:	Run level 3 (multi-user)
Primary Cache:	32 KB I + 32 KB D on chip per core	Base Pointers:	32/64-bit
Secondary Cache:	256 KB I+D on chip per core	Peak Pointers:	32/64-bit
L3 Cache:	35 MB I+D on chip per chip	Other Software:	Microquill SmartHeap V10.0
Other Cache:	None		
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)		
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM		
Other Hardware:	None		



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	303	32.3	303	32.3	303	32.3	261	37.4	262	37.3	261	37.4
401.bzip2	492	19.6	491	19.6	492	19.6	484	20.0	482	20.0	484	19.9
403.gcc	284	28.4	284	28.3	284	28.4	281	28.7	282	28.6	281	28.7
429.mcf	193	47.4	190	47.9	192	47.6	189	48.2	190	48.0	191	47.9
445.gobmk	446	23.5	446	23.5	446	23.5	446	23.5	446	23.5	446	23.5
456.hmmer	168	55.4	170	55.0	169	55.3	168	55.4	170	55.0	169	55.3
458.sjeng	443	27.3	443	27.3	443	27.3	441	27.5	441	27.4	441	27.5
462.libquantum	3.23	6420	3.36	6170	3.25	6380	3.23	6420	3.36	6170	3.25	6380
464.h264ref	550	40.2	548	40.4	545	40.6	550	40.2	548	40.4	545	40.6
471.omnetpp	195	32.1	195	32.0	196	31.9	150	41.6	150	41.6	150	41.8
473.astar	266	26.4	266	26.4	265	26.5	265	26.5	262	26.8	266	26.4
483.xalancbmk	132	52.4	131	52.5	131	52.5	131	52.9	131	52.6	131	52.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Set Hyper-Threading to Disabled

Set Lock_step to disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Fri Jun 5 05:07:42 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-4850 v3 @ 2.20GHz

4 "physical id"s (chips)

56 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

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Platform Notes (Continued)

```
caution.)  
    cpu cores : 14  
    siblings   : 14  
    physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
    physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
    physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
    physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
    cache size : 35840 KB  
  
From /proc/meminfo  
MemTotal:      528023856 kB  
HugePages_Total:        0  
Hugepagesize:     2048 kB  
  
From /etc/*release* /etc/*version*  
os-release:  
  NAME="Red Hat Enterprise Linux Server"  
  VERSION="7.0 (Maipo)"  
  ID="rhel"  
  ID_LIKE="fedora"  
  VERSION_ID="7.0"  
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"  
  ANSI_COLOR="0;31"  
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server  
  
uname -a:  
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57  
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Jun 5 04:34  
  
SPEC is set to: /spec  
Filesystem      Type  Size  Used Avail Use% Mounted on  
 /dev/sda2       xfs   440G  201G  239G  46% /  
Additional information from dmidecode:  
  
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.  
  
BIOS American Megatrends Inc. BLISV610 05/23/2015  
Memory:  
 32x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1333 MHz  
  
(End of data from sysinfo program)
```



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General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
memory using RedHat EL 7.0
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>



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For other inquiries, please contact webmaster@spec.org.

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