



# SPEC® CINT2006 Result

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## Huawei

**SPECint®2006 = 36.7**

Huawei RH5885H V3 (Intel Xeon E7-4820 v3)

**SPECint\_base2006 = 35.0**

CPU2006 license: 3175

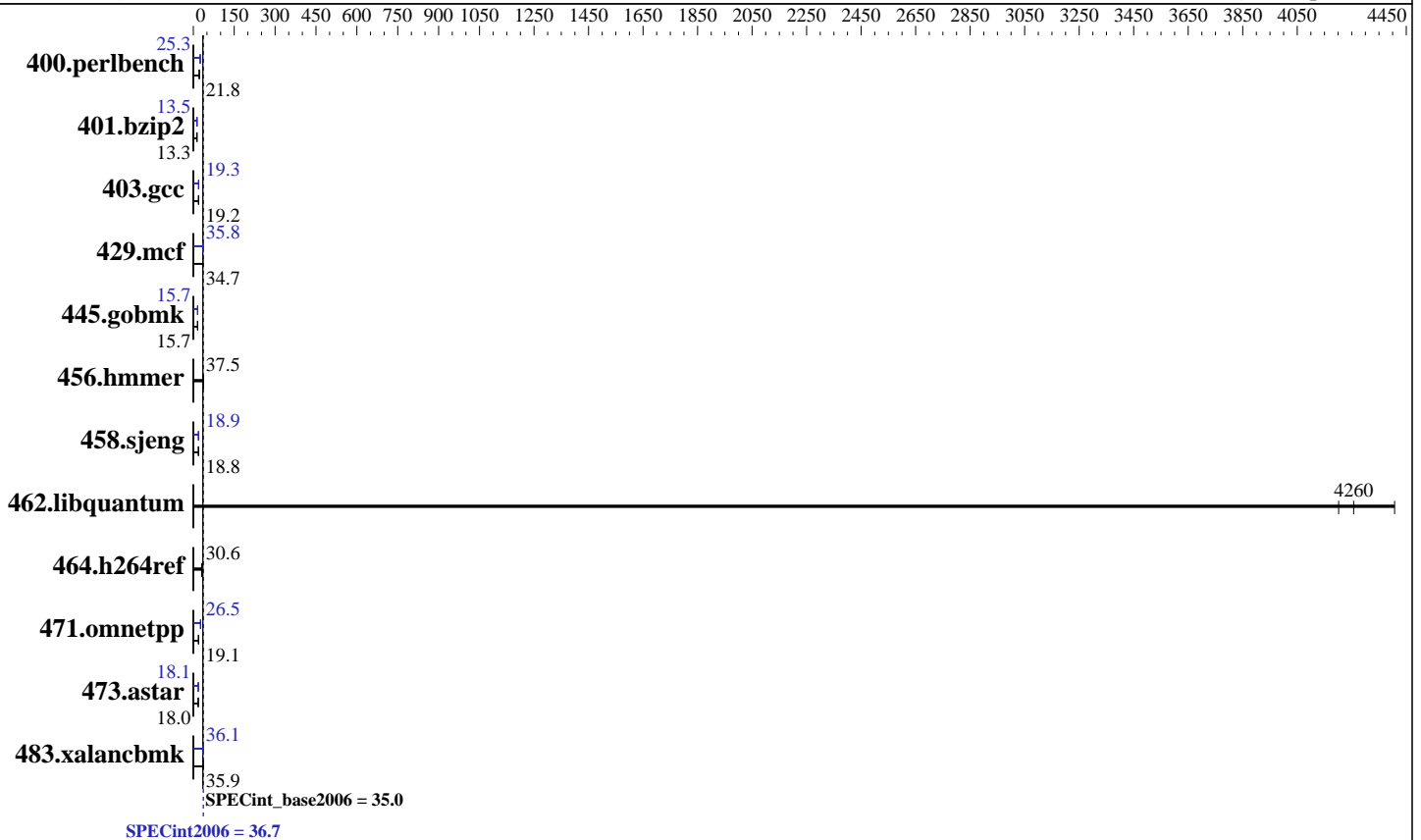
Test date: May-2015

Test sponsor: Huawei

Hardware Availability: May-2015

Tested by: Huawei

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E7-4820 v3  
 CPU Characteristics: 1900  
 CPU MHz: Integrated  
 FPU: 40 cores, 4 chips, 10 cores/chip  
 CPU(s) enabled: 2,4 chips  
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core  
 Primary Cache: 256 KB I+D on chip per core  
 Secondary Cache: 25 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)  
 Memory: 2 x 300 GB SAS, 10K RPM  
 Disk Subsystem: None  
 Other Hardware:

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	448	21.8	<b>448</b>	<b>21.8</b>	447	21.8	<b>386</b>	<b>25.3</b>	386	25.3	386	25.3
401.bzip2	<b>726</b>	<b>13.3</b>	728	13.3	725	13.3	714	13.5	714	13.5	<b>714</b>	<b>13.5</b>
403.gcc	420	19.2	<b>420</b>	<b>19.2</b>	420	19.2	417	19.3	417	19.3	<b>417</b>	<b>19.3</b>
429.mcf	<b>263</b>	<b>34.7</b>	263	34.7	260	35.0	256	35.7	253	36.0	<b>255</b>	<b>35.8</b>
445.gobmk	671	15.6	<b>669</b>	<b>15.7</b>	669	15.7	<b>668</b>	<b>15.7</b>	668	15.7	667	15.7
456.hammer	249	37.5	<b>249</b>	<b>37.5</b>	250	37.4	249	37.5	<b>249</b>	<b>37.5</b>	250	37.4
458.sjeng	643	18.8	643	18.8	<b>643</b>	<b>18.8</b>	640	18.9	641	18.9	<b>641</b>	<b>18.9</b>
462.libquantum	<b>4.87</b>	<b>4260</b>	4.70	4410	4.93	4200	<b>4.87</b>	<b>4260</b>	4.70	4410	4.93	4200
464.h264ref	728	30.4	<b>724</b>	<b>30.6</b>	723	30.6	728	30.4	<b>724</b>	<b>30.6</b>	723	30.6
471.omnetpp	<b>327</b>	<b>19.1</b>	326	19.2	331	18.9	236	26.5	<b>236</b>	<b>26.5</b>	237	26.3
473.astar	391	18.0	<b>389</b>	<b>18.0</b>	389	18.0	<b>388</b>	<b>18.1</b>	388	18.1	384	18.3
483.xalancbmk	193	35.8	192	35.9	<b>192</b>	<b>35.9</b>	<b>191</b>	<b>36.1</b>	191	36.1	191	36.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Lock\_step to disabled  
Baseboard Management Controller used to adjust the fan speed to 100%  
Set DRAM Maintenance to Manual  
Set DRAM Maintenance Mode to pTRR  
Set Hyper Threading to disabled  
Sysinfo program /spec/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on RH5885HV3 Thu May 21 06:46:19 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4820 v3 @ 1.90GHz
4 "physical id"s (chips)
40 "processors"
```

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
physical 2: cores 0 1 2 3 4 8 9 10 11 12
physical 3: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      1056474292 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

uname -a:

```
Linux RH5885HV3 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 21 06:41

SPEC is set to: /spec

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   537G  34G  504G   7% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV705 03/30/2015

Memory:

```
32x NO DIMM NO DIMM
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1333 MHz
```

(End of data from sysinfo program)

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## Platform Notes (Continued)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have two lines reading as:  
32x NO DIMM NO DIMM  
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1333 MHz

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"  
OMP\_NUM\_THREADS = "40"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmcr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-HSW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-HSW-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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