SPEC® CINT2006 Result
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Fujitsu

PRIMEQUEST 2800E2, Intel Xeon E7-8867 v3, 2.50 GHz

$SPECint^®\_rate2006 =$ 5070

$SPECint\_rate\_base2006 =$ 4920

CPU2006 license: 19	**Test date:** May-2015
Test sponsor: Fujitsu	**Hardware Availability:** May-2015
Tested by: Fujitsu	**Software Availability:** Oct-2014

400.perlbench	256: 4720 / 256: 3930
401.bzip2	256: 2550 / 256: 2450
403.gcc	256: 3570
429.mcf	256: 6130
445.gobmk	256: 3610 / 256: 3520
456.hmmer	256: 7720 / 256: 7600
458.sjeng	256: 3870 / 256: 3720
462.libquantum	256: 48400
464.h264ref	256: 6290 / 256: 6090
471.omnetpp	256: 2390 / 256: 2320
473.astar	256: 2640
483.xalancbmk	256: 5300

SPECint_rate_base2006 = 4920

SPECint_rate2006 = 5070

Hardware

CPU Name:	Intel Xeon E7-8867 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	128 cores, 8 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4,6,8 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	45 MB I+D on chip per chip
Other Cache:	None
Memory:	2 TB (128 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	2 x SATA, 600 GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.6 (Santiago) 2.6.32-504.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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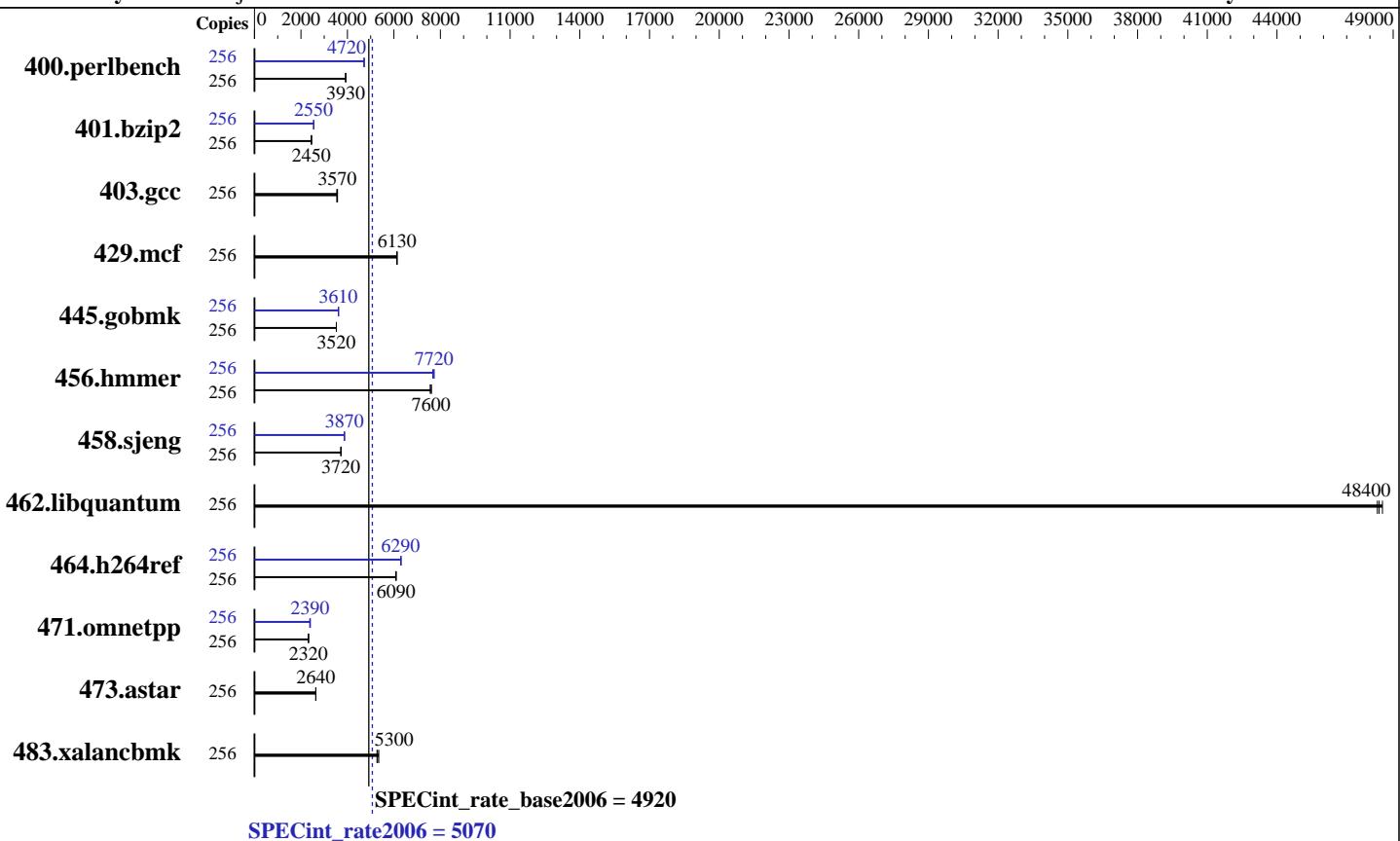
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	256	637	3930	638	3920	**637**	**3930**	256	531	4710	**530**	**4720**	529	4720
401.bzip2	256	1010	2440	1005	2460	**1007**	**2450**	256	967	2550	970	2550	**970**	**2550**
403.gcc	256	577	3570	**578**	**3570**	582	3540	256	577	3570	**578**	**3570**	582	3540
429.mcf	256	380	6140	381	6130	**381**	**6130**	256	380	6140	381	6130	**381**	**6130**
445.gobmk	256	762	3520	762	3520	**762**	**3520**	256	745	3610	741	3630	**744**	**3610**
456.hmmer	256	316	7560	**314**	**7600**	314	7610	256	311	7670	**309**	**7720**	309	7730
458.sjeng	256	833	3720	**832**	**3720**	832	3720	256	**800**	**3870**	800	3870	800	3870
462.libquantum	256	**110**	**48400**	110	48300	109	48500	256	**110**	**48400**	110	48300	109	48500
464.h264ref	256	929	6100	**930**	**6090**	932	6080	256	900	6290	897	6310	**900**	**6290**
471.omnetpp	256	686	2330	690	2320	**690**	**2320**	256	673	2380	665	2410	**669**	**2390**
473.astar	256	680	2640	681	2640	**681**	**2640**	256	680	2640	681	2640	**681**	**2640**
483.xalancbmk	256	335	5270	330	5350	**333**	**5300**	256	335	5270	330	5350	**333**	**5300**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Energy Performance = Performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

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General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc   -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc   -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

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Peak Compiler Invocation (Continued)

        456.hmmer: `icc -m64`

        458.sjeng: `icc -m64`

C++ benchmarks:
        `icpc -m32`

Peak Portability Flags

        400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
        401.bzip2: `-DSPEC_CPU_LP64`
        456.hmmer: `-DSPEC_CPU_LP64`
        458.sjeng: `-DSPEC_CPU_LP64`
        462.libquantum: `-DSPEC_CPU_LINUX`
        483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-auto-ilp32`

        401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-opt-prefetch -auto-ilp32 -ansi-alias`

        403.gcc: `basepeak = yes`

        429.mcf: `basepeak = yes`

        445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                `-ansi-alias -opt-mem-layout-trans=3`

        456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

        458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-unroll4 -auto-ilp32`

        462.libquantum: `basepeak = yes`

        464.h264ref: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-unroll2 -ansi-alias`

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Peak Optimization Flags (Continued)

C++ benchmarks:

> 471.omnetpp: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
> `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
> `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
> `-L/sh -lsmartheap`

> 473.astar: basepeak = yes

> 483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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