



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = 63.4

ProLiant DL580 Gen9
(2.50 GHz, Intel Xeon E7-8890 v3)

SPECint_base2006 = 61.5

CPU2006 license: 3

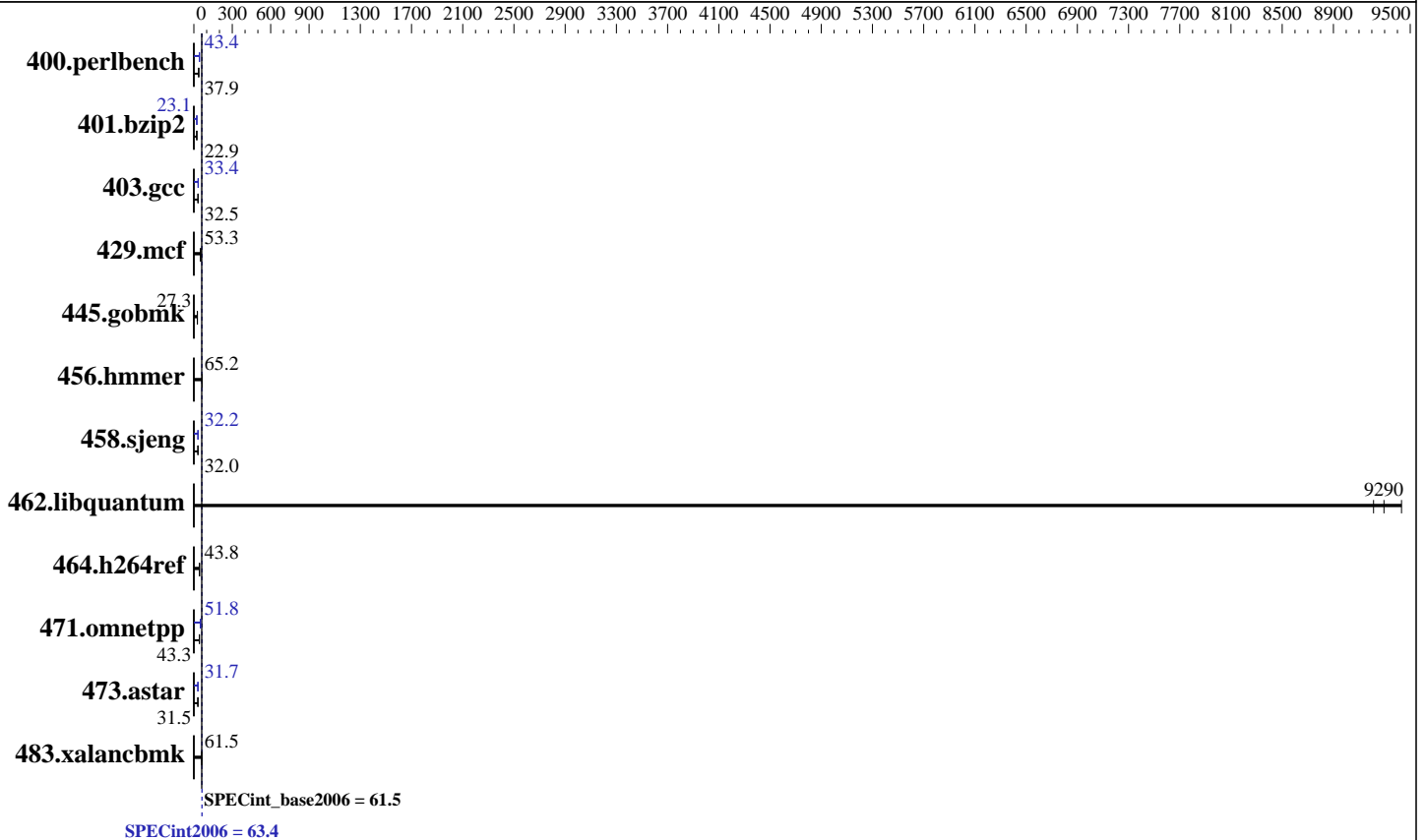
Test date: Apr-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: May-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E7-8890 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 45 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 Kernel 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	258	37.8	257	38.0	<u>258</u>	<u>37.9</u>	<u>225</u>	<u>43.4</u>	225	43.3	224	43.5
401.bzip2	421	22.9	422	22.9	<u>422</u>	<u>22.9</u>	418	23.1	418	23.1	<u>418</u>	<u>23.1</u>
403.gcc	248	32.5	247	32.6	<u>248</u>	<u>32.5</u>	242	33.3	237	34.0	<u>241</u>	<u>33.4</u>
429.mcf	169	53.9	<u>171</u>	<u>53.3</u>	175	52.1	169	53.9	<u>171</u>	<u>53.3</u>	175	52.1
445.gobmk	384	27.3	<u>384</u>	<u>27.3</u>	385	27.2	384	27.3	<u>384</u>	<u>27.3</u>	385	27.2
456.hammer	<u>143</u>	<u>65.2</u>	143	65.1	143	65.2	<u>143</u>	<u>65.2</u>	143	65.1	143	65.2
458.sjeng	378	32.0	<u>378</u>	<u>32.0</u>	378	32.0	<u>376</u>	<u>32.2</u>	376	32.2	375	32.2
462.libquantum	2.25	9210	<u>2.23</u>	<u>9290</u>	2.20	9430	2.25	9210	<u>2.23</u>	<u>9290</u>	2.20	9430
464.h264ref	506	43.7	<u>506</u>	<u>43.8</u>	505	43.8	506	43.7	<u>506</u>	<u>43.8</u>	505	43.8
471.omnetpp	<u>144</u>	<u>43.3</u>	144	43.3	146	42.7	121	51.5	<u>121</u>	<u>51.8</u>	121	51.9
473.astar	<u>223</u>	<u>31.5</u>	224	31.4	223	31.5	<u>221</u>	<u>31.7</u>	222	31.7	221	31.7
483.xalancbmk	112	61.4	<u>112</u>	<u>61.5</u>	112	61.7	<u>112</u>	<u>61.4</u>	<u>112</u>	<u>61.5</u>	112	61.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration
Intel Hypthreading Options set to Disabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to Package C6 (retention) State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Enabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-t8cw Sun Apr 19 23:19:02 2015

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8890 v3 @ 2.50GHz
 4 "physical id"s (chips)
 72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 18
siblings : 18
physical 0: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

```
From /proc/meminfo
MemTotal: 529317552 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-t8cw 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 19 17:49

```
SPEC is set to: /cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 371G 8.4G 362G 3% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
Continued on next page



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Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 03/13/2015

Memory:

64x UNKNOWN NOT AVAILABLE

32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:

32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz, configured at 1600 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

OMP_NUM_THREADS = "72"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -DSPEC_CPU_LP64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmarthheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks (except as noted below):
icpc -m64

471.omnetpp: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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