



# SPEC® CFP2006 Result

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## Huawei

SPECfp®2006 = **110**

Huawei XH622 V3 (Intel Xeon E5-2680 v3)

SPECfp\_base2006 = **105**

CPU2006 license: 3175

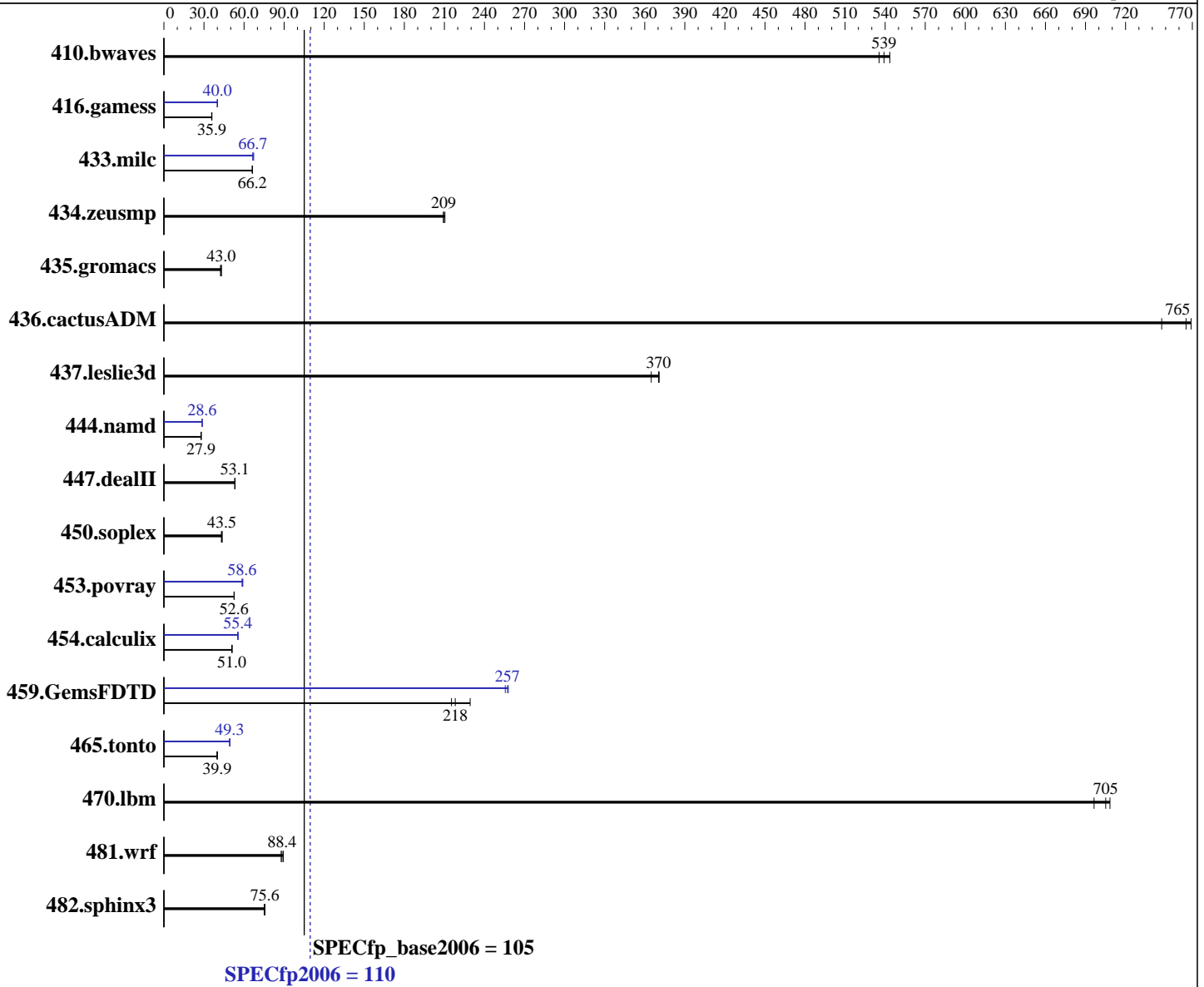
Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2680 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	25.4	535	25.0	543	<u>25.2</u>	<u>539</u>	25.4	535	25.0	543	<u>25.2</u>	<u>539</u>
416.gamess	<u>546</u>	<u>35.9</u>	548	35.7	545	35.9	488	40.1	<u>489</u>	<u>40.0</u>	490	39.9
433.milc	138	66.4	<u>139</u>	<u>66.2</u>	139	66.1	<u>138</u>	<u>66.7</u>	137	67.2	138	66.5
434.zeusmp	43.5	209	<u>43.5</u>	<u>209</u>	43.2	210	43.5	209	<u>43.5</u>	<u>209</u>	43.2	210
435.gromacs	169	42.3	<u>166</u>	<u>43.0</u>	166	43.0	169	42.3	<u>166</u>	<u>43.0</u>	166	43.0
436.cactusADM	<u>15.6</u>	<u>765</u>	16.0	747	15.5	769	<u>15.6</u>	<u>765</u>	16.0	747	15.5	769
437.leslie3d	25.8	365	<u>25.4</u>	<u>370</u>	25.4	371	25.8	365	<u>25.4</u>	<u>370</u>	25.4	371
444.namd	288	27.9	288	27.9	<u>288</u>	<u>27.9</u>	280	28.6	<u>280</u>	<u>28.6</u>	280	28.7
447.dealII	216	53.1	215	53.3	<u>215</u>	<u>53.1</u>	216	53.1	215	53.3	<u>215</u>	<u>53.1</u>
450.soplex	191	43.6	<u>192</u>	<u>43.5</u>	194	43.0	191	43.6	<u>192</u>	<u>43.5</u>	194	43.0
453.povray	<u>101</u>	<u>52.6</u>	101	52.7	101	52.5	90.8	58.6	90.0	59.1	<u>90.7</u>	<u>58.6</u>
454.calculix	<u>162</u>	<u>51.0</u>	162	51.0	162	50.9	149	55.5	149	55.4	<u>149</u>	<u>55.4</u>
459.GemsFDTD	49.3	215	46.3	229	<u>48.6</u>	<u>218</u>	<u>41.2</u>	<u>257</u>	41.1	258	41.5	256
465.tonto	247	39.8	246	40.0	<u>246</u>	<u>39.9</u>	199	49.4	200	49.2	<u>200</u>	<u>49.3</u>
470.lbm	19.4	708	<u>19.5</u>	<u>705</u>	19.7	696	19.4	708	<u>19.5</u>	<u>705</u>	19.7	696
481.wrf	125	89.4	<u>126</u>	<u>88.4</u>	127	87.8	125	89.4	<u>126</u>	<u>88.4</u>	127	87.8
482.sphinx3	<u>258</u>	<u>75.6</u>	260	75.1	258	75.7	<u>258</u>	<u>75.6</u>	260	75.1	258	75.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Set Snoop Mode to HS mode  
 Set Intel Hyper-threading Technology to Disable  
 Sysinfo program /spec15/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on localhost.localdomain Thu Mar 19 13:58:00 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 12

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

cache size : 30720 KB

From /proc/meminfo

MemTotal: 263720560 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.0 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.0"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.0:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.0:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-123.el7.x86\_64 #1 SMP Mon May 5 11:16:57

EDT 2014 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Mar 19 05:59

SPEC is set to: /spec15

Filesystem Type Size Used Avail Use% Mounted on

/dev/sdal ext4 458G 38G 397G 9% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 1.17 09/03/2014

Memory:

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## Platform Notes (Continued)

8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz  
8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

The Huawei XH622 V3 and Huawei XH628 V3

are electronically equivalent.

The results have been measured on a Huawei XH628 V3 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

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Software Availability: Sep-2014

## Base Portability Flags (Continued)

```

447.deall: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

## Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```

## Peak Portability Flags

Same as Base Portability Flags



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Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Peak Optimization Flags

### C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Software Availability: Sep-2014

## Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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