SPEC® CFP2006 Result
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Cisco Systems

Cisco UCS B200 M4 (Intel Xeon E5-2667 v3 @ 3.20GHz)

$SPECfp^{®}\_rate2006 = 673$

$SPECfp\_rate\_base2006 = 656$
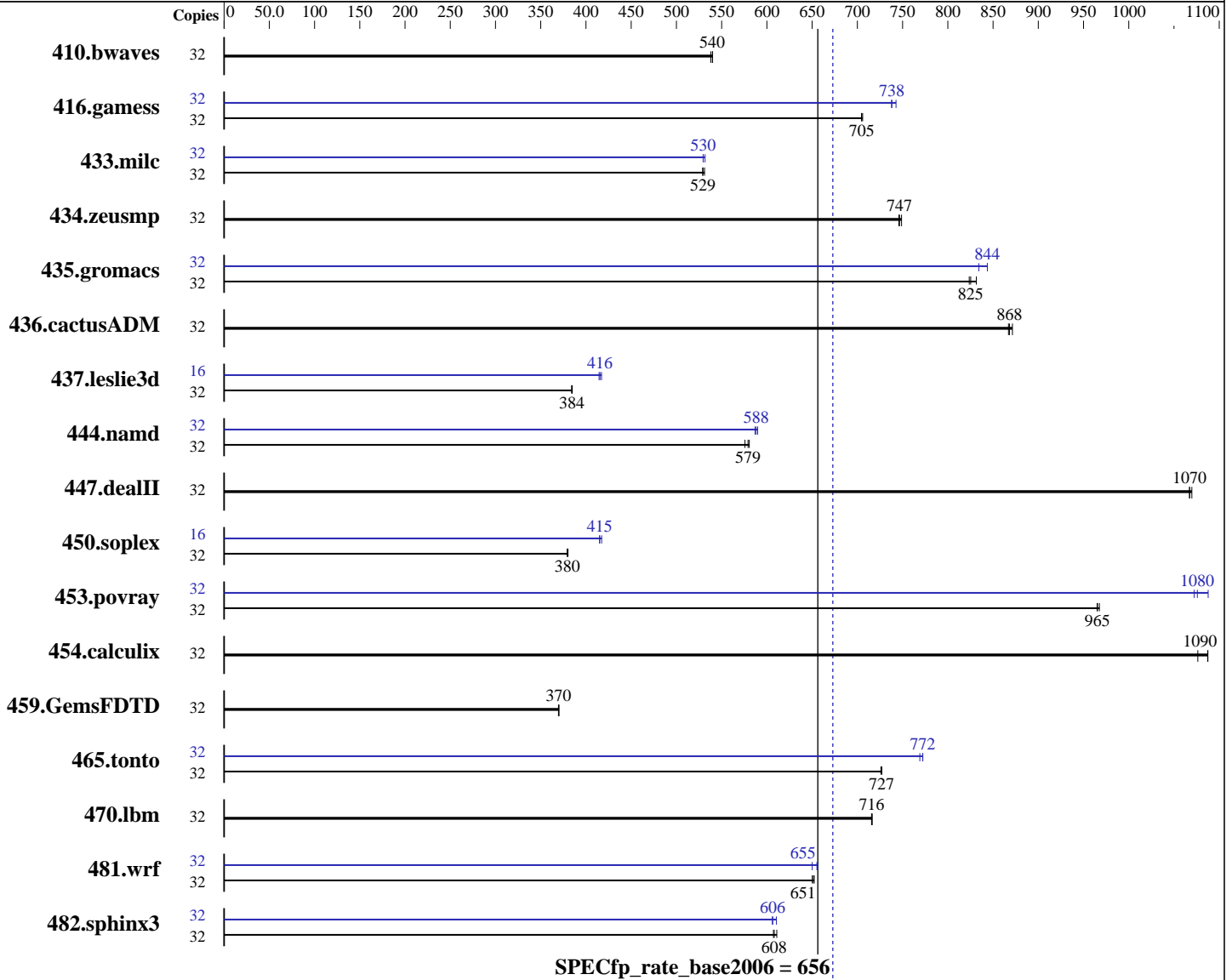
CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Feb-2015
Hardware Availability: Sep-2014
Software Availability: Jul-2014

Benchmark	Copies	Result
410.bwaves	32	540
416.gamess	32	738
416.gamess	32	705
433.milc	32	530
433.milc	32	529
434.zeusmp	32	747
435.gromacs	32	844
435.gromacs	32	825
436.cactusADM	32	868
437.leslie3d	16	416
437.leslie3d	32	384
444.namd	32	588
444.namd	32	579
447.dealII	32	1070
450.soplex	16	415
450.soplex	32	380
453.povray	32	1080
453.povray	32	965
454.calculix	32	1090
459.GemsFDTD	32	370
465.tonto	32	772
465.tonto	32	727
470.lbm	32	716
481.wrf	32	655
481.wrf	32	651
482.sphinx3	32	606
482.sphinx3	32	608

SPECfp_rate_base2006 = 656

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Hardware

CPU Name: Intel Xeon E5-2667 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 3200
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux; Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
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L3 Cache:	20 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 300GB SAS, 15K RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	**806**	**540**	805	540	808	538	32	**806**	**540**	805	540	808	538
416.gamess	32	**889**	**705**	889	705	888	706	32	849	738	844	743	**849**	**738**
433.milc	32	555	529	553	531	**555**	**529**	32	555	530	**555**	**530**	553	532
434.zeusmp	32	389	749	390	746	**390**	**747**	32	389	749	390	746	**390**	**747**
435.gromacs	32	275	832	**277**	**825**	277	824	32	274	834	271	844	**271**	**844**
436.cactusADM	32	441	867	439	871	**440**	**868**	32	441	867	439	871	**440**	**868**
437.leslie3d	32	**782**	**384**	782	385	783	384	16	**362**	**416**	363	414	360	417
444.namd	32	442	581	**443**	**579**	446	576	32	**436**	**588**	435	590	437	587
447.dealII	32	**343**	**1070**	342	1070	343	1070	32	**343**	**1070**	342	1070	343	1070
450.soplex	32	702	380	704	379	**703**	**380**	16	320	417	322	415	**321**	**415**
453.povray	32	176	965	176	968	**176**	**965**	32	159	1070	**158**	**1080**	157	1090
454.calculix	32	245	1080	243	1090	**243**	**1090**	32	245	1080	243	1090	**243**	**1090**
459.GemsFDTD	32	**918**	**370**	919	370	917	370	32	**918**	**370**	919	370	917	370
465.tonto	32	433	727	434	726	**433**	**727**	32	**408**	**772**	408	772	409	769
470.lbm	32	614	716	614	716	**614**	**716**	32	614	716	614	716	**614**	**716**
481.wrf	32	**549**	**651**	548	652	550	650	32	550	650	**545**	**655**	545	656
482.sphinx3	32	1021	611	1027	607	**1026**	**608**	32	1022	611	1029	606	**1028**	**606**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

CPU Performance set to HPC
Power Technology set to Custom
Processor Power State C6 set to Disabled

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Platform Notes (Continued)

```
  Energy Performance BIAS setting set to Performance
  Memory RAS configuration set to Maximum Performance
  QPI Snoop Mode set to Early Snoop
   Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914
   $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
   running on localhost.localdomain Fri Feb 13 11:17:11 2015

   This section contains SUT (System Under Test) info as seen by
   some common utilities.  To remove or add to this section, see:
     http://www.spec.org/cpu2006/Docs/config.html#sysinfo

  From /proc/cpuinfo
     model name : Intel(R) Xeon(R) CPU E5-2667 v3 @ 3.20GHz
        2 "physical id"s (chips)
        32 "processors"
     cores, siblings (Caution: counting these is hw and system dependent.  The
     following excerpts from /proc/cpuinfo might not be reliable.  Use with
     caution.)
        cpu cores : 8
        siblings  : 16
        physical 0: cores 0 1 2 3 4 5 6 7
        physical 1: cores 0 1 2 3 4 5 6 7
     cache size : 20480 KB

  From /proc/meminfo
     MemTotal:       263705952 kB
     HugePages_Total:        0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.0 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="7.0"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
        ANSI_COLOR="0;31"
        CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
     redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
     system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

  uname -a:
     Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
     EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Feb 13 04:52

  SPEC is set to: /opt/cpu2006-1.2
     Filesystem     Type  Size  Used Avail Use% Mounted on
     /dev/sda2      xfs   196G   13G  183G   7% /
```
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Platform Notes (Continued)

```
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Cisco Systems, Inc. B200M4.2.2.3d.0.111420141438 11/14/2014
  Memory:
   16x 0xCE00 M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
   8x NO DIMM NO DIMM

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>        /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
     icc   -m64
```

C++ benchmarks:
```
     icpc  -m64
```

Fortran benchmarks:
```
     ifort -m64
```

Benchmarks using both Fortran and C:
```
     icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
```

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SPECfp_rate2006 =	673
SPECfp_rate_base2006 =	656

Base Portability Flags (Continued)

```
     433.milc: -DSPEC_CPU_LP64
   434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
   447.dealII: -DSPEC_CPU_LP64
   450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
  450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:
```
ifort -m64
```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
    436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
       453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
     459.GemsFDTD: -DSPEC_CPU_LP64
        465.tonto: -DSPEC_CPU_LP64
         470.lbm: -DSPEC_CPU_LP64
         481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
      482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
        433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
                  -auto-ilp32

         470.lbm: basepeak = yes

      482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
                  -unroll2
```

C++ benchmarks:

```
        444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2)
                  -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
                  -auto-ilp32

       447.dealII: basepeak = yes
```

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revC.xml

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