



# SPEC<sup>®</sup> CFP2006 Result

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## HITACHI

### SPECfp<sup>®</sup>\_rate2006 = 847

### BladeSymphony BS520H (Intel Xeon E5-2697 v3)

### SPECfp\_rate\_base2006 = 821

CPU2006 license: 35

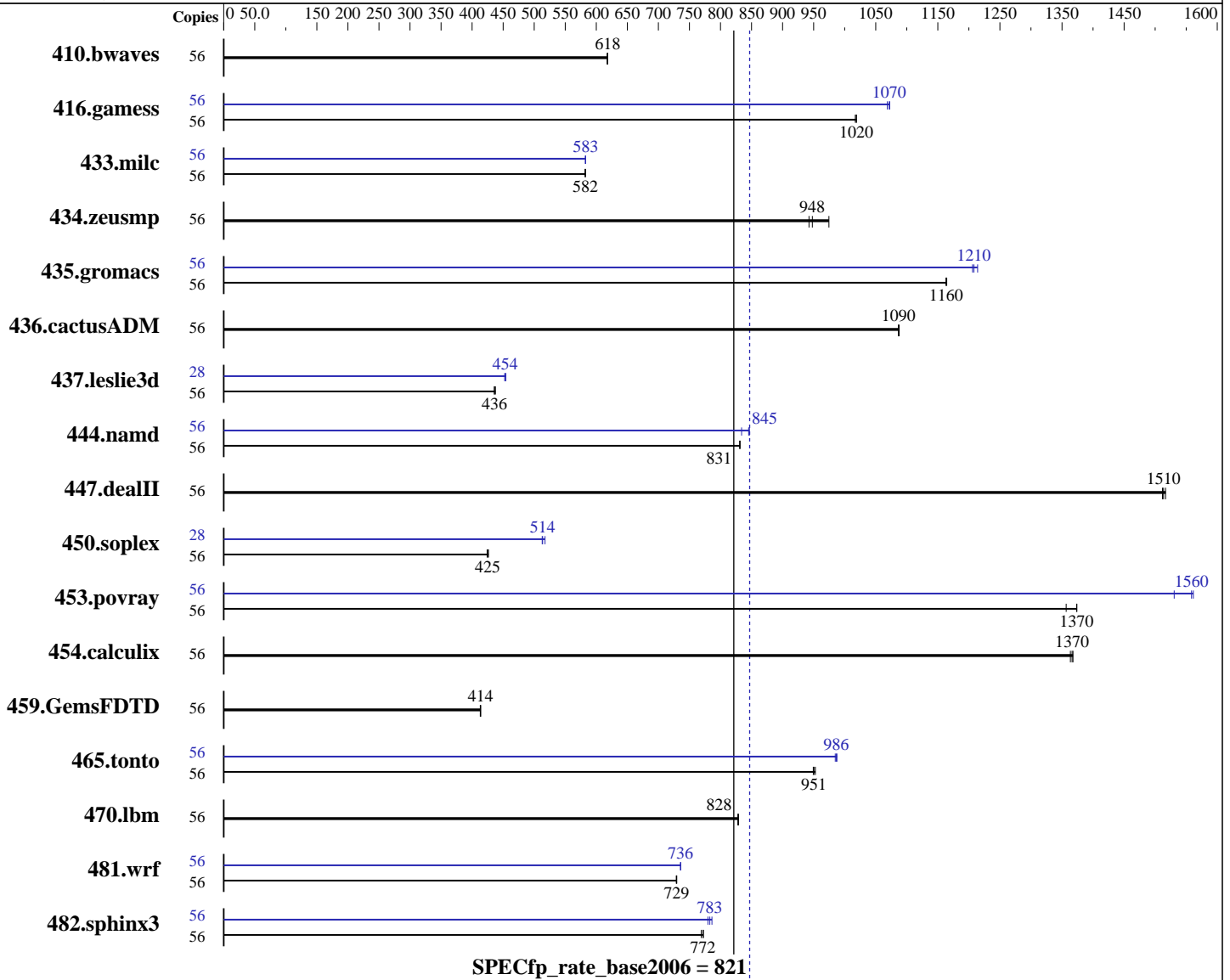
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jan-2015

Hardware Availability: Dec-2014

Software Availability: Nov-2013



#### Hardware

CPU Name: Intel Xeon E5-2697 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

#### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

Continued on next page



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L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 2 x 600 GB SAS, 10000 RPM, RAID1  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: none

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	<b>1231</b>	<b>618</b>	1231	618	1233	617	56	<b>1231</b>	<b>618</b>	1231	618	1233	617
416.gamess	56	1078	1020	1076	1020	<b>1076</b>	<b>1020</b>	56	<b>1023</b>	<b>1070</b>	1026	1070	1022	1070
433.milc	56	883	582	<b>883</b>	<b>582</b>	883	582	56	882	583	<b>882</b>	<b>583</b>	883	582
434.zeusmp	56	523	975	541	943	<b>538</b>	<b>948</b>	56	523	975	541	943	<b>538</b>	<b>948</b>
435.gromacs	56	344	1160	<b>344</b>	<b>1160</b>	343	1160	56	329	1210	<b>331</b>	<b>1210</b>	332	1210
436.cactusADM	56	<b>616</b>	<b>1090</b>	616	1090	615	1090	56	<b>616</b>	<b>1090</b>	616	1090	615	1090
437.leslie3d	56	1208	436	1203	438	<b>1207</b>	<b>436</b>	28	579	454	<b>580</b>	<b>454</b>	582	452
444.namd	56	<b>540</b>	<b>831</b>	540	831	540	831	56	539	834	<b>531</b>	<b>845</b>	531	847
447.dealII	56	<b>423</b>	<b>1510</b>	422	1520	424	1510	56	<b>423</b>	<b>1510</b>	422	1520	424	1510
450.soplex	56	<b>1098</b>	<b>425</b>	1101	424	1095	426	28	451	517	<b>455</b>	<b>514</b>	455	513
453.povray	56	220	1360	<b>217</b>	<b>1370</b>	217	1370	56	<b>191</b>	<b>1560</b>	195	1530	191	1560
454.calculix	56	338	1370	339	1360	<b>338</b>	<b>1370</b>	56	338	1370	339	1360	<b>338</b>	<b>1370</b>
459.GemsFDTD	56	<b>1436</b>	<b>414</b>	1438	413	1436	414	56	<b>1436</b>	<b>414</b>	1438	413	1436	414
465.tonto	56	580	949	<b>580</b>	<b>951</b>	578	953	56	560	985	558	988	<b>559</b>	<b>986</b>
470.lbm	56	928	829	<b>929</b>	<b>828</b>	929	828	56	928	829	<b>929</b>	<b>828</b>	929	828
481.wrf	56	858	729	857	730	<b>858</b>	<b>729</b>	56	850	736	<b>850</b>	<b>736</b>	850	736
482.sphinx3	56	1413	773	<b>1415</b>	<b>772</b>	1419	769	56	<b>1394</b>	<b>783</b>	1388	786	1400	780

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Patrol Scrub = Disable  
Per Core P-state = Disable

Continued on next page



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BladeSymphony BS520H (Intel Xeon E5-2697 v3)

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CPU2006 license: 35

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Test date: Jan-2015

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### Platform Notes (Continued)

COD Preferenc = Enable

Sysinfo program /home/speccpu2006/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on 520Hx36564 Fri Jan 16 19:28:26 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2697 v3 @ 2.60GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 14
  siblings     : 28
  physical 0   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1   : cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size     : 17920 KB
```

```
From /proc/meminfo
MemTotal:      263987312 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux 520Hx36564 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 16 19:12

```
SPEC is set to: /home/speccpu2006/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_520hx36564-lv_home ext4      485G  5.5G  455G   2% /home
```

```
Additional information from dmidecode:
BIOS HITACHI 08-20 01/06/2015
Memory:
 8x NO DIMM Unknown
16x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
```

Continued on next page



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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/speccpu2006/cpu2006/libs/32:/home/speccpu2006/cpu2006/libs/64:/home/speccpu2006/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 HC0A1 are electronically equivalent.

The results have been measured on a Hitachi Compute Blade 520H.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.deallI: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

Continued on next page

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Page 4



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## Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
 -ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64



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## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
            -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
         -ansi-alias

```

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.20150127.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.20150127.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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