SPEC® CINT2006 Result
Copyright 2006-2015 Standard Performance Evaluation Corporation

ACTION S.A.

ACTINA SOLAR 100 S6+ (Intel Xeon E5-2650 v3, 2.30 GHz)

$SPECint^{®}\_rate2006 =$ 441

$SPECint\_rate\_base2006 =$ 425

CPU2006 license: 9008	**Test date:** Jan-2015
Test sponsor: ACTION S.A.	**Hardware Availability:** Sep-2014
Tested by: ACTION S.A.	**Software Availability:** Sep-2014

SPECint_rate_base2006 = 425

SPECint_rate2006 = 441

Hardware

CPU Name:	Intel Xeon E5-2650 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	10 cores, 1 chip, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 240 GB SATA II SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.4 (Santiago) 2.6.32-358.11.1.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

ACTION S.A.

ACTINA SOLAR 100 S6+ (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_rate2006 = 441
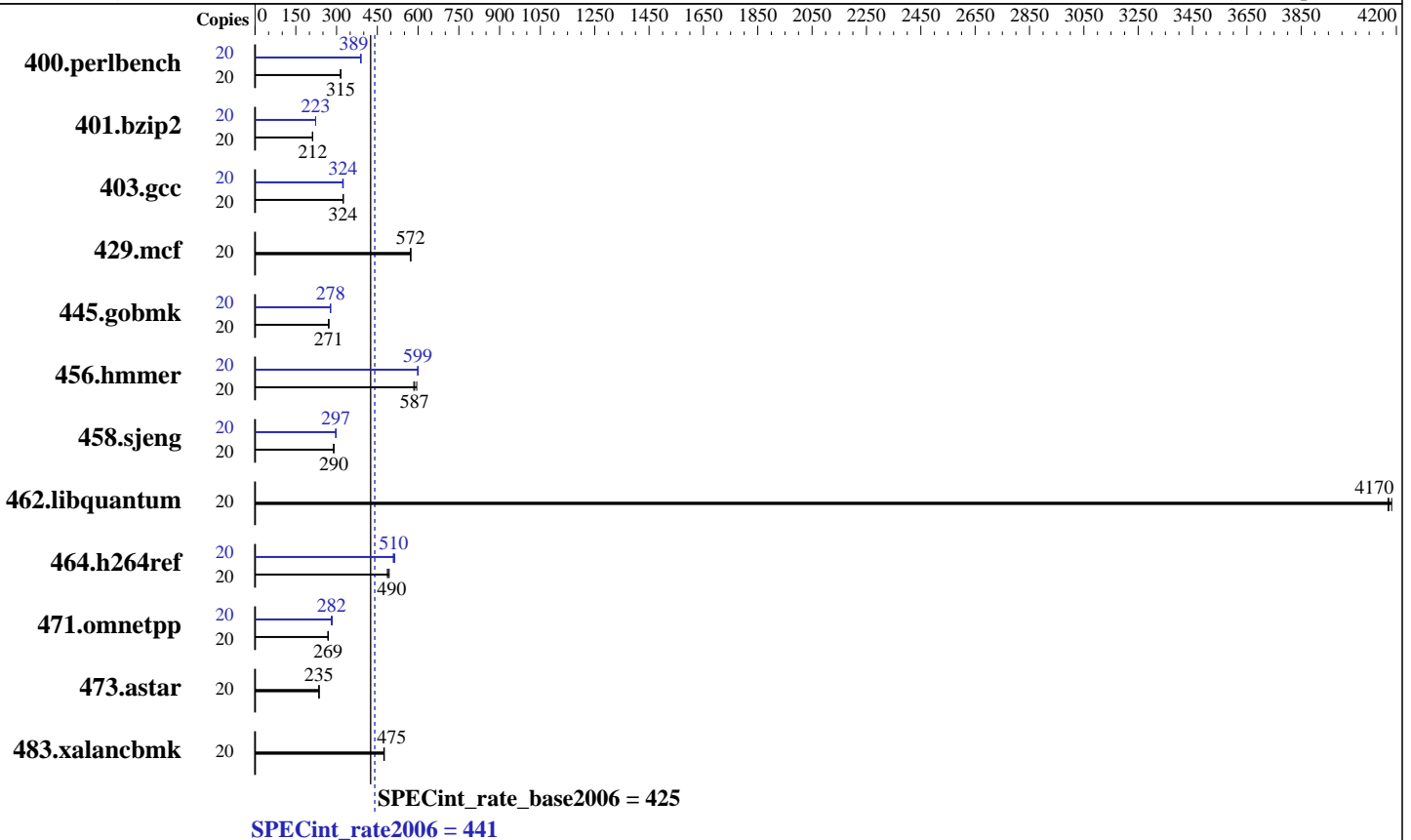
SPECint_rate_base2006 = 425

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	20	**620**	**315**	621	315	619	315	20	**502**	**389**	502	389	501	390
401.bzip2	20	**912**	**212**	909	212	913	211	20	866	223	865	223	**865**	**223**
403.gcc	20	**497**	**324**	497	324	495	325	20	499	323	**497**	**324**	496	324
429.mcf	20	318	573	**319**	**572**	319	572	20	318	573	**319**	**572**	319	572
445.gobmk	20	773	271	**774**	**271**	774	271	20	**755**	**278**	755	278	756	277
456.hmmer	20	**318**	**587**	314	595	319	584	20	**311**	**599**	312	598	311	600
458.sjeng	20	**835**	**290**	835	290	836	290	20	814	297	813	298	**814**	**297**
462.libquantum	20	99.1	4180	**99.3**	**4170**	99.4	4170	20	99.1	4180	**99.3**	**4170**	99.4	4170
464.h264ref	20	**903**	**490**	909	487	897	494	20	861	514	870	509	**868**	**510**
471.omnetpp	20	464	269	**465**	**269**	466	268	20	445	281	440	284	**443**	**282**
473.astar	20	598	235	**597**	**235**	595	236	20	598	235	**597**	**235**	595	236
483.xalancbmk	20	290	475	**290**	**475**	291	474	20	290	475	**290**	**475**	291	474

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The taskset mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate taskset commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    Bios Settings
    Power Technology = Energy Efficient
    Enforce POR = Disabled
    COD Enable = Enable

    BMC Setting
    Fan Mode = Full Speed

    Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
    $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
    running on localhost.localdomain Wed Jan 21 15:16:30 2015

    This section contains SUT (System Under Test) info as seen by
    some common utilities. To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

ACTION S.A.

ACTINA SOLAR 100 S6+ (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_rate2006 =   441

SPECint_rate_base2006 = 425

CPU2006 license: 9008		**Test date:**	Jan-2015
Test sponsor: ACTION S.A.		**Hardware Availability:**	Sep-2014
Tested by: ACTION S.A.		**Software Availability:**	Sep-2014

Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz
      1 "physical id"s (chips)
      20 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 20
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       132051672 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost.localdomain 2.6.32-358.11.1.el6.x86_64 #1 SMP Tue Nov 19
   17:43:04 CET 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 21 15:14

SPEC is set to: /cpu2006.1.2
   Filesystem      Type    Size  Used Avail Use% Mounted on
   /dev/sda1       ext4    193G   43G  141G  24% /

Additional information from dmidecode:
  BIOS American Megatrends Inc. 1.0b 10/29/2014
  Memory:
   8x   16 GB
   8x NO DIMM NO DIMM
   2x Samsung (date:14/33) M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
   6x Samsung (date:14/4p) M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank

(End of data from sysinfo program)
Dmidecode does not properly detect memory modules
8 modules of 16 GB were used to run the test (128 GB total)
```

SPEC CINT2006 Result
Copyright 2006-2015 Standard Performance Evaluation Corporation

ACTION S.A.

ACTINA SOLAR 100 S6+ (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_rate2006 =   441

SPECint_rate_base2006 = 425

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Binaries compiled on a system with 2x Xeon E5-2670 v3 chips + 128 GB memory
 using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
      icc   -m32

C++ benchmarks:
      icpc -m32

Base Portability Flags

      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
      -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
      -opt-mem-layout-trans=3

C++ benchmarks:
      -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
      -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap

Base Other Flags

C benchmarks:

      403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
      icc   -m32

Continued on next page

SPEC CINT2006 Result
Copyright 2006-2015 Standard Performance Evaluation Corporation

ACTION S.A.

ACTINA SOLAR 100 S6+ (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_rate2006 = 441

SPECint_rate_base2006 = 425

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4 -auto-ilp32`

ACTION S.A.

ACTINA SOLAR 100 S6+ (Intel Xeon E5-2650 v3, 2.30 GHz)

SPECint_rate2006 = 441

SPECint_rate_base2006 = 425

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Jan-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/cpu2006.1.2/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevC-jan-2015-For-Supermicro-Platform.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Feb 25 11:29:33 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 February 2015.