SPEC® CINT2006 Result

SGI

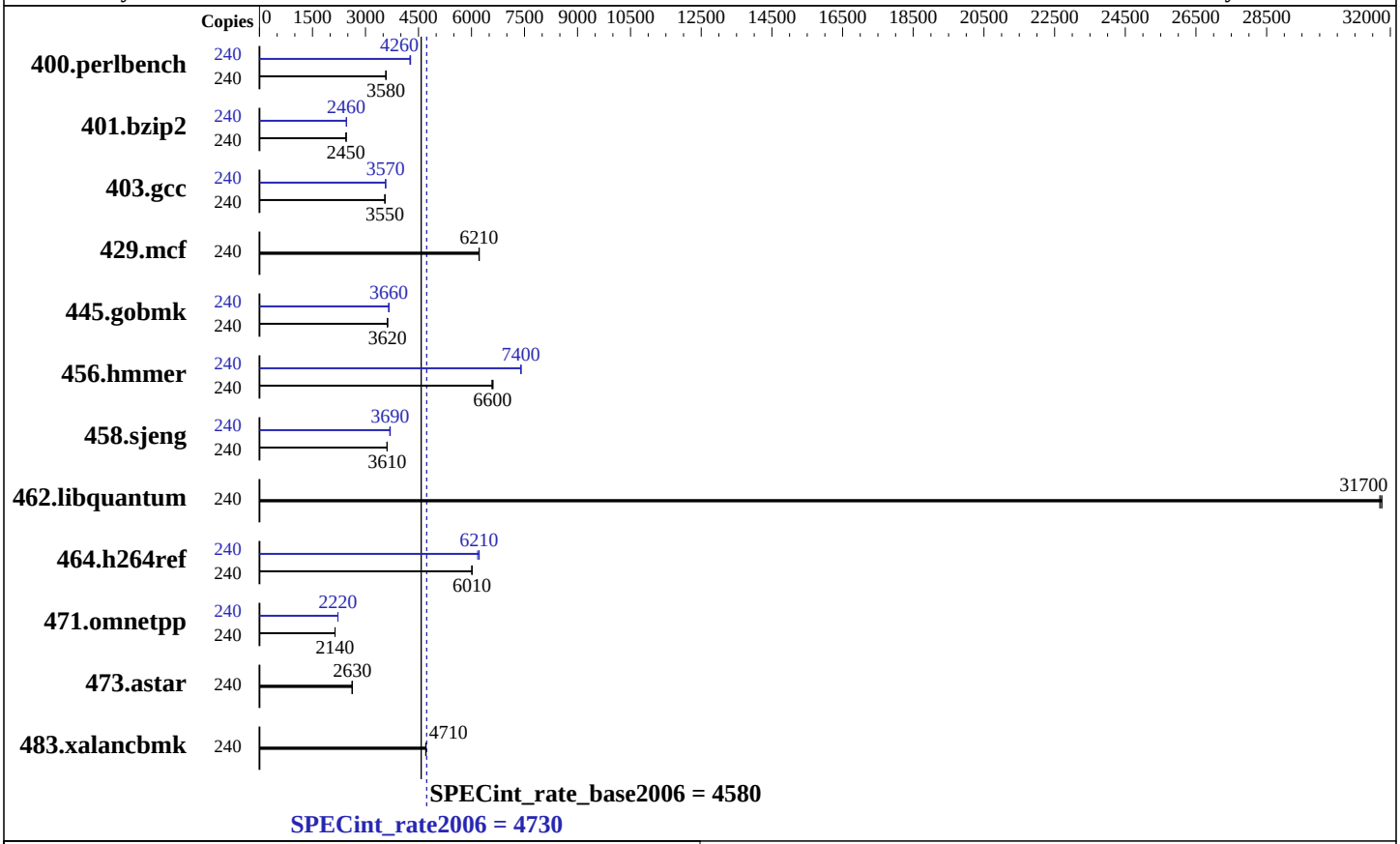
SGI UV 300 (Intel Xeon E7-8890 v2, 2.8 GHz)

SPECint®_rate2006 = 4730

SPECint_rate_base2006 = 4580

CPU2006 license: 4
Test sponsor: SGI
Tested by: SGI

Test date: Jan-2015
Hardware Availability: Dec-2014
Software Availability: Nov-2014



Hardware

CPU Name:	Intel Xeon E7-8890 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	120 cores, 8 chips, 15 cores/chip, 2 threads/core
CPU(s) orderable:	4-32 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	37.5 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (128 x 8 GB 2Rx4 PC3-14900R-13, ECC, running at 1333 MHz)
Disk Subsystem:	1 TB tmpfs
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP3, Kernel 3.0.101-0.46-default
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0 SGI Foundation Software 2.11, Build 711rp42.sles11sp3-1412152100

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	240	**655**	**3580**	656	3580	654	3580	240	548	4280	**550**	**4260**	550	4260
401.bzip2	240	**945**	**2450**	945	2450	944	2450	240	**941**	**2460**	942	2460	941	2460
403.gcc	240	544	3550	**544**	**3550**	545	3550	240	543	3560	541	3570	**541**	**3570**
429.mcf	240	352	6220	**352**	**6210**	352	6210	240	352	6220	**352**	**6210**	352	6210
445.gobmk	240	**695**	**3620**	696	3620	694	3630	240	**688**	**3660**	688	3660	687	3670
456.hmmer	240	**339**	**6600**	339	6610	340	6580	240	**303**	**7400**	302	7400	303	7390
458.sjeng	240	**804**	**3610**	804	3610	804	3610	240	787	3690	784	3700	**786**	**3690**
462.libquantum	240	**157**	**31700**	157	31700	157	31800	240	**157**	**31700**	157	31700	157	31800
464.h264ref	240	884	6010	**883**	**6010**	883	6020	240	860	6170	854	6220	**855**	**6210**
471.omnetpp	240	701	2140	702	2140	**701**	**2140**	240	**676**	**2220**	676	2220	675	2220
473.astar	240	**642**	**2630**	641	2630	643	2620	240	**642**	**2630**	641	2630	643	2620
483.xalancbmk	240	352	4700	351	4710	**352**	**4710**	240	352	4700	351	4710	**352**	**4710**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"

Tmpfs filesystem set up with:
  mkdir -p /mnt/shm
  mount -t tmpfs -o size=1024g,rw tmpfs /mnt/shm/

Turbo mode activated with:
  modprobe acpi_cpufreq
  cpupower frequency-set -u 3400MHz -d 3400MHz -g performance
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/mnt/shm/cpu2006-1.2/libs/32:/mnt/shm/cpu2006-1.2/libs/64:/mnt/shm/cpu2006-1.2/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:
```
icc -m32 -L/sw/sdev/intel/parallel_studio_2015/composer_xe_2015/lib/ia32/
```

C++ benchmarks:
```
icpc -m32 -L/sw/sdev/intel/parallel_studio_2015/composer_xe_2015/lib/ia32/
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32 -L/sw/sdev/intel/parallel_studio_2015/composer_xe_2015/lib/ia32/
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32 -L/sw/sdev/intel/parallel_studio_2015/composer_xe_2015/lib/ia32/
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/SGI-UV300-Platform-Flags.html`
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/SGI-UV300-Platform-Flags.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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