



SPEC® CFP2006 Result

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Huawei

SPECfp®_rate2006 = 828

Huawei CH222 V3 (Intel Xeon E5-2690 v3)

SPECfp_rate_base2006 = 803

CPU2006 license: 3175

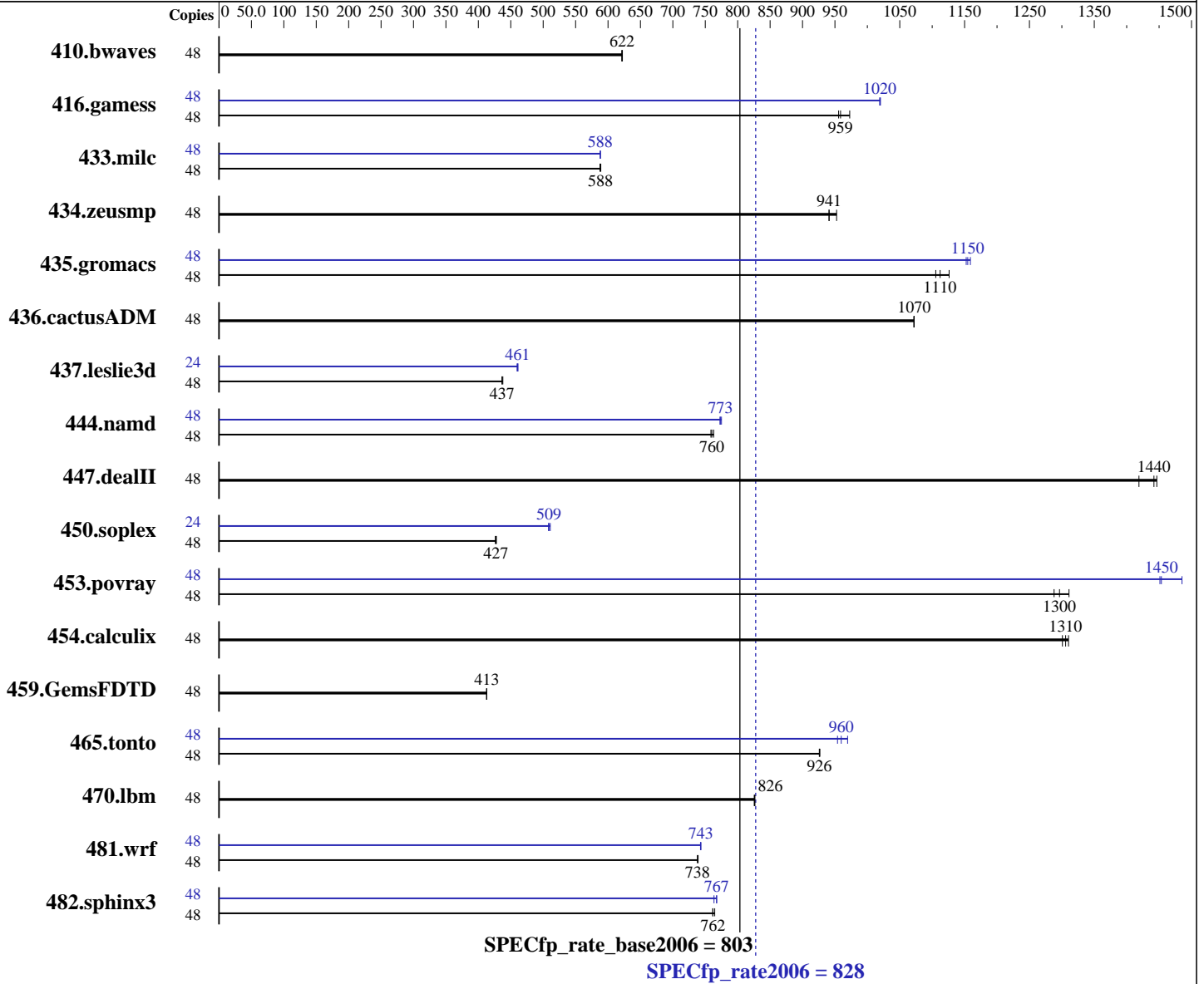
Test sponsor: Huawei

Tested by: Huawei

Test date: Dec-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2690 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|------------|-------------|-------------|-------------|--------|-------------|------------|------------|-------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 48 | 1050 | 621 | 1048 | 622 | <u>1049</u> | <u>622</u> | 48 | 1050 | 621 | 1048 | 622 | <u>1049</u> | <u>622</u> |
| 416.gamess | 48 | 983 | 956 | 966 | 973 | <u>980</u> | <u>959</u> | 48 | 922 | 1020 | <u>922</u> | <u>1020</u> | 921 | 1020 |
| 433.milc | 48 | 750 | 588 | 748 | 589 | <u>749</u> | <u>588</u> | 48 | 749 | 588 | <u>749</u> | <u>588</u> | 749 | 588 |
| 434.zeusmp | 48 | 464 | 941 | <u>464</u> | <u>941</u> | 459 | 953 | 48 | 464 | 941 | <u>464</u> | <u>941</u> | 459 | 953 |
| 435.gromacs | 48 | <u>308</u> | <u>1110</u> | 310 | 1110 | 304 | 1130 | 48 | 297 | 1150 | <u>297</u> | <u>1150</u> | 296 | 1160 |
| 436.cactusADM | 48 | 535 | 1070 | <u>535</u> | <u>1070</u> | 535 | 1070 | 48 | 535 | 1070 | <u>535</u> | <u>1070</u> | 535 | 1070 |
| 437.leslie3d | 48 | 1031 | 438 | 1033 | 437 | <u>1033</u> | <u>437</u> | 24 | <u>490</u> | <u>461</u> | 489 | 461 | 491 | 460 |
| 444.namd | 48 | <u>506</u> | <u>760</u> | 507 | 759 | 505 | 763 | 48 | 497 | 775 | <u>498</u> | <u>773</u> | 498 | 773 |
| 447.dealII | 48 | 387 | 1420 | <u>381</u> | <u>1440</u> | 380 | 1450 | 48 | 387 | 1420 | <u>381</u> | <u>1440</u> | 380 | 1450 |
| 450.soplex | 48 | 936 | 428 | 939 | 426 | <u>937</u> | <u>427</u> | 24 | 392 | 511 | 394 | 508 | <u>393</u> | <u>509</u> |
| 453.povray | 48 | 198 | 1290 | 195 | 1310 | <u>197</u> | <u>1300</u> | 48 | 176 | 1450 | <u>176</u> | <u>1450</u> | 172 | 1490 |
| 454.calculix | 48 | 302 | 1310 | <u>303</u> | <u>1310</u> | 304 | 1300 | 48 | 302 | 1310 | <u>303</u> | <u>1310</u> | 304 | 1300 |
| 459.GemsFDTD | 48 | <u>1233</u> | <u>413</u> | 1234 | 413 | 1233 | 413 | 48 | <u>1233</u> | <u>413</u> | 1234 | 413 | 1233 | 413 |
| 465.tonto | 48 | 510 | 927 | 510 | 926 | <u>510</u> | <u>926</u> | 48 | 487 | 970 | 495 | 954 | <u>492</u> | <u>960</u> |
| 470.lbm | 48 | 798 | 827 | 799 | 826 | <u>798</u> | <u>826</u> | 48 | 798 | 827 | 799 | 826 | <u>798</u> | <u>826</u> |
| 481.wrf | 48 | 726 | 739 | <u>726</u> | <u>738</u> | 726 | 738 | 48 | 721 | 743 | 722 | 743 | <u>722</u> | <u>743</u> |
| 482.sphinx3 | 48 | 1228 | 762 | 1223 | 765 | <u>1228</u> | <u>762</u> | 48 | <u>1219</u> | <u>767</u> | 1225 | 763 | 1219 | 768 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to COD

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Platform Notes (Continued)

```

Set Patrol Scrub to Disable
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Tue Dec 23 00:11:17 2014

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2690 v3 @ 2.60GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 12
  siblings     : 24
  physical 0:  cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1:  cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 15360 KB

```

```

From /proc/meminfo
MemTotal:      264272692 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Dec 21 21:40

```

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext4  268G  110G  144G  44% /

```

```

Additional information from dmidecode:
BIOS Insyde Corp. 1.13 08/12/2014
Memory:
 8x NO DIMM NO DIMM      3 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 1 rank
 8x Samsung M393A2G40DB0-CPB 16 GB 2133 MHz 2 rank
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```



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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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Tested by: Huawei

Software Availability: Nov-2013

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
            -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
         -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
         -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2)
         -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
         -ansi-alias

```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.1.20141216.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.1.20141216.xml>



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For other inquiries, please contact webmaster@spec.org.

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