



SPEC® CINT2006 Result

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Supermicro

SuperServer 2028UT-BTNRT
(X10DBT-T, Intel Xeon E7-2870 v2)

SPECint®2006 = 48.5

SPECint_base2006 = 46.6

CPU2006 license: 001176

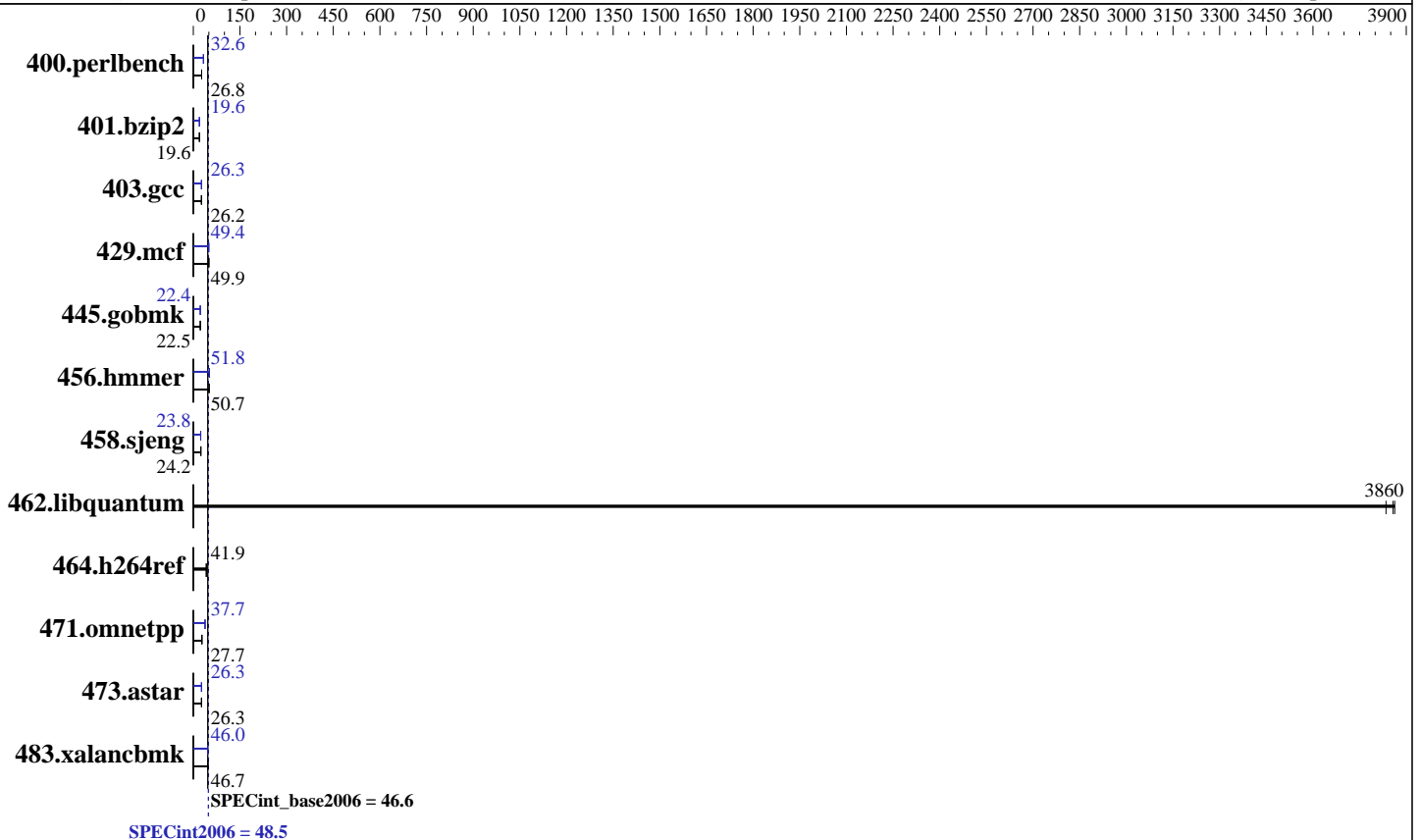
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2014

Hardware Availability: Oct-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E7-2870 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 30 cores, 2 chips, 15 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 2Rx8 PC3-14900R-13, ECC, running at 1333 MHz)
 Disk Subsystem: 1 x 400 GB SATA III, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0, Kernel 3.10.0-123.el7.x86_64
 Compiler: C/C++; Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	365	26.8	<u>365</u>	<u>26.8</u>	364	26.8	<u>300</u>	<u>32.6</u>	299	32.6	300	32.6
401.bzip2	491	19.7	492	19.6	<u>492</u>	<u>19.6</u>	492	19.6	<u>492</u>	<u>19.6</u>	492	19.6
403.gcc	307	26.2	<u>307</u>	<u>26.2</u>	307	26.2	<u>307</u>	<u>26.3</u>	307	26.3	307	26.2
429.mcf	185	49.4	182	50.2	<u>183</u>	<u>49.9</u>	<u>184</u>	<u>49.4</u>	183	49.9	185	49.4
445.gobmk	467	22.4	<u>467</u>	<u>22.5</u>	467	22.5	468	22.4	468	22.4	<u>468</u>	<u>22.4</u>
456.hammer	<u>184</u>	<u>50.7</u>	184	50.7	184	50.7	180	51.7	<u>180</u>	<u>51.8</u>	180	51.8
458.sjeng	500	24.2	500	24.2	<u>500</u>	<u>24.2</u>	508	23.8	<u>509</u>	<u>23.8</u>	509	23.8
462.libquantum	<u>5.37</u>	<u>3860</u>	5.36	3860	5.40	3830	<u>5.37</u>	<u>3860</u>	5.36	3860	5.40	3830
464.h264ref	528	41.9	<u>529</u>	<u>41.9</u>	529	41.8	528	41.9	<u>529</u>	<u>41.9</u>	529	41.8
471.omnetpp	228	27.4	<u>226</u>	<u>27.7</u>	226	27.7	<u>166</u>	<u>37.7</u>	166	37.6	165	37.9
473.astar	267	26.3	<u>267</u>	<u>26.3</u>	265	26.5	265	26.4	<u>267</u>	<u>26.3</u>	267	26.3
483.xalancbmk	<u>148</u>	<u>46.7</u>	148	46.7	148	46.7	149	46.2	150	46.0	<u>150</u>	<u>46.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Hyper-Threading = Disable

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

OMP_NUM_THREADS = "30"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):

`icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
-auto-p32`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jan 13 10:54:58 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 January 2015.