SPEC® CINT2006 Result
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Huawei

Huawei RH2288A V2 (Intel Xeon E5-2630L v2)

$SPECint®2006 = $ 45.9

$SPECint_base2006 = $ 42.4

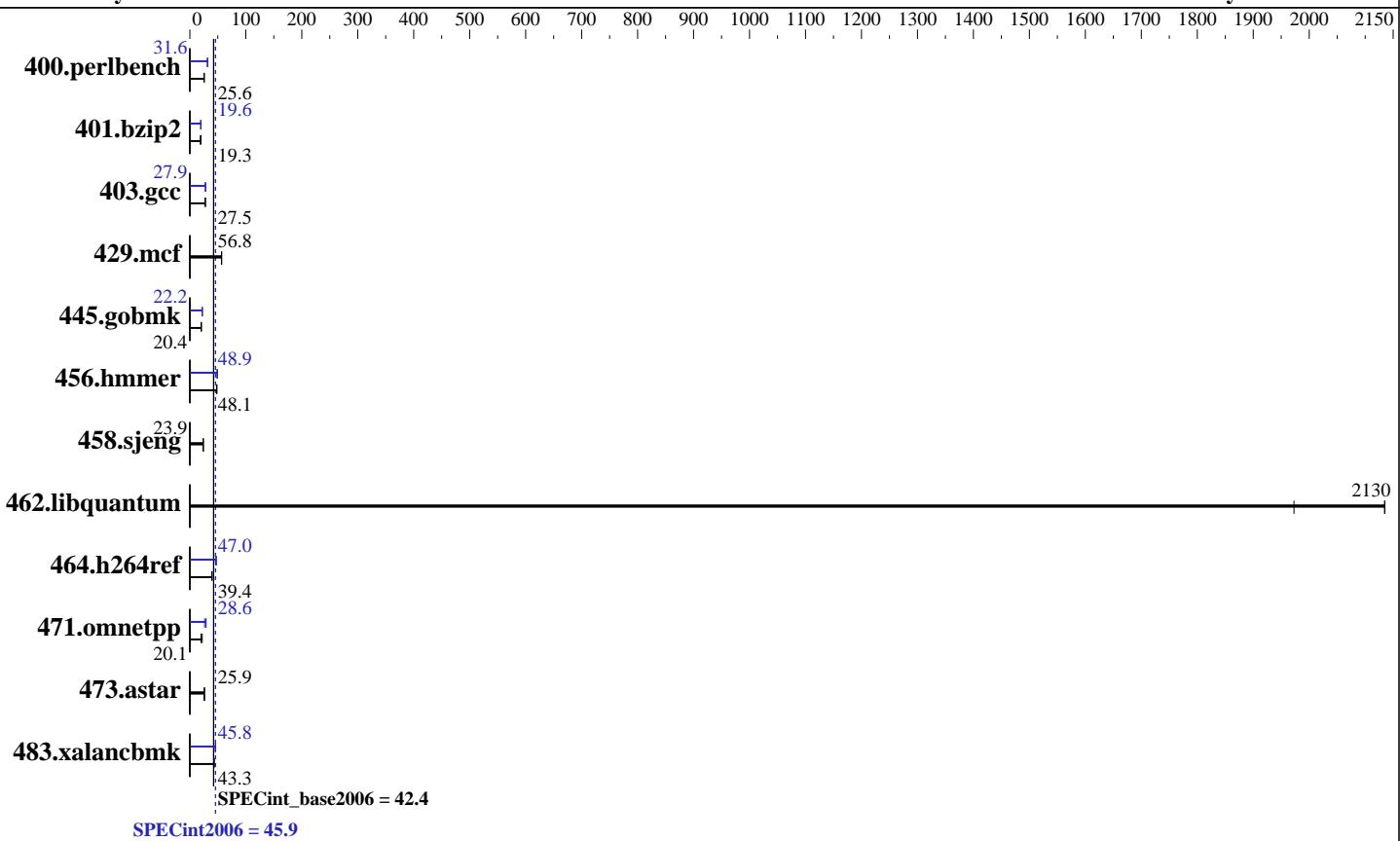|	
CPU2006 license: 3175	**Test date:** Sep-2014
Test sponsor: Huawei	**Hardware Availability:** Sep-2013
Tested by: Huawei	**Software Availability:** Nov-2013

Benchmark results (bar chart):

- 400.perlbench: 31.6 / 25.6
- 401.bzip2: 19.6 / 19.3
- 403.gcc: 27.9 / 27.5
- 429.mcf: 56.8
- 445.gobmk: 22.2 / 20.4
- 456.hmmer: 48.9 / 48.1
- 458.sjeng: 23.9
- 462.libquantum: 2130
- 464.h264ref: 47.0 / 39.4
- 471.omnetpp: 28.6 / 20.1
- 473.astar: 25.9
- 483.xalancbmk: 45.8 / 43.3

SPECint_base2006 = 42.4

SPECint2006 = 45.9

Hardware

CPU Name:	Intel Xeon E5-2630L v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	15 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (8 x 16 GB 2Rx4 PC3-14900R-11, ECC, running at 1600 MHz)
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	382	25.6	382	25.6	**382**	**25.6**	311	31.5	**309**	**31.6**	309	31.6
401.bzip2	**501**	**19.3**	501	19.2	501	19.3	492	19.6	**492**	**19.6**	493	19.6
403.gcc	291	27.7	293	27.5	**293**	**27.5**	288	27.9	289	27.9	**289**	**27.9**
429.mcf	162	56.4	**161**	**56.8**	161	56.8	162	56.4	**161**	**56.8**	161	56.8
445.gobmk	**515**	**20.4**	514	20.4	515	20.4	473	22.2	**473**	**22.2**	473	22.2
456.hmmer	195	47.9	193	48.3	**194**	**48.1**	191	48.9	**191**	**48.9**	191	48.9
458.sjeng	505	23.9	**506**	**23.9**	506	23.9	505	23.9	**506**	**23.9**	506	23.9
462.libquantum	**9.71**	**2130**	9.71	2130	10.5	1970	**9.71**	**2130**	9.71	2130	10.5	1970
464.h264ref	560	39.5	562	39.4	**561**	**39.4**	**471**	**47.0**	471	47.0	471	47.0
471.omnetpp	**311**	**20.1**	313	20.0	282	22.2	**219**	**28.6**	237	26.4	215	29.1
473.astar	271	25.9	271	25.9	**271**	**25.9**	271	25.9	271	25.9	**271**	**25.9**
483.xalancbmk	159	43.4	**159**	**43.3**	160	43.1	151	45.8	151	45.8	**151**	**45.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
    BIOS configuration:
    Set Power Efficiency Mode to Custom
    Baseboard Management Controller used to adjust the fan speed to 100%
    Sysinfo program /spec/config/sysinfo.rev6800
    $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
    running on localhost Tue Sep  2 21:24:04 2014

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
      model name : Intel(R) Xeon(R) CPU E5-2630L v2 @ 2.40GHz
         2 "physical id"s (chips)
         12 "processors"
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 6
         siblings  : 6
         physical 0: cores 0 1 2 3 4 5
         physical 1: cores 0 1 2 3 4 5
      cache size : 15360 KB
```

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Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:       132103760 kB
   HugePages_Total:        0
   Hugepagesize:      2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux localhost 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 2 21:21

SPEC is set to: /spec
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda1       ext4   439G   74G  343G  18% /

Additional information from dmidecode:
  Memory:
   8x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)
 Dmidecode mistakenly believes the memory was running at 1866 MHz, when it should only run at 1600 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "12"

 Binaries compiled on a system with 2x Xeon X5645 CPU + 16GB memory
 using RHEL 6.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 The Huawei RH2288A V2 and Huawei RH1288A V2
 are electronically equivalent.
 The results have been measured on a Huawei RH2288A V2 model
```

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Base Compiler Invocation

C benchmarks:
    `icc -m64`

C++ benchmarks:
    `icpc -m64`

Base Portability Flags

        400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
           401.bzip2: `-DSPEC_CPU_LP64`
            403.gcc: `-DSPEC_CPU_LP64`
            429.mcf: `-DSPEC_CPU_LP64`
         445.gobmk: `-DSPEC_CPU_LP64`
         456.hmmer: `-DSPEC_CPU_LP64`
          458.sjeng: `-DSPEC_CPU_LP64`
      462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
         464.h264ref: `-DSPEC_CPU_LP64`
         471.omnetpp: `-DSPEC_CPU_LP64`
           473.astar: `-DSPEC_CPU_LP64`
       483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:
    `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
    `-Wl,-z,muldefs -L/smartheap -lsmartheap64`

Base Other Flags

C benchmarks:

        403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
    `icc -m64`

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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