SPEC® CINT2006 Result
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Hewlett-Packard Company

ProLiant DL360 Gen9
(1.80 GHz, Intel Xeon E5-2650L v3)

SPECint®2006 = 48.4

SPECint_base2006 = 46.6

CPU2006 license: 3   **Test date:** Oct-2014
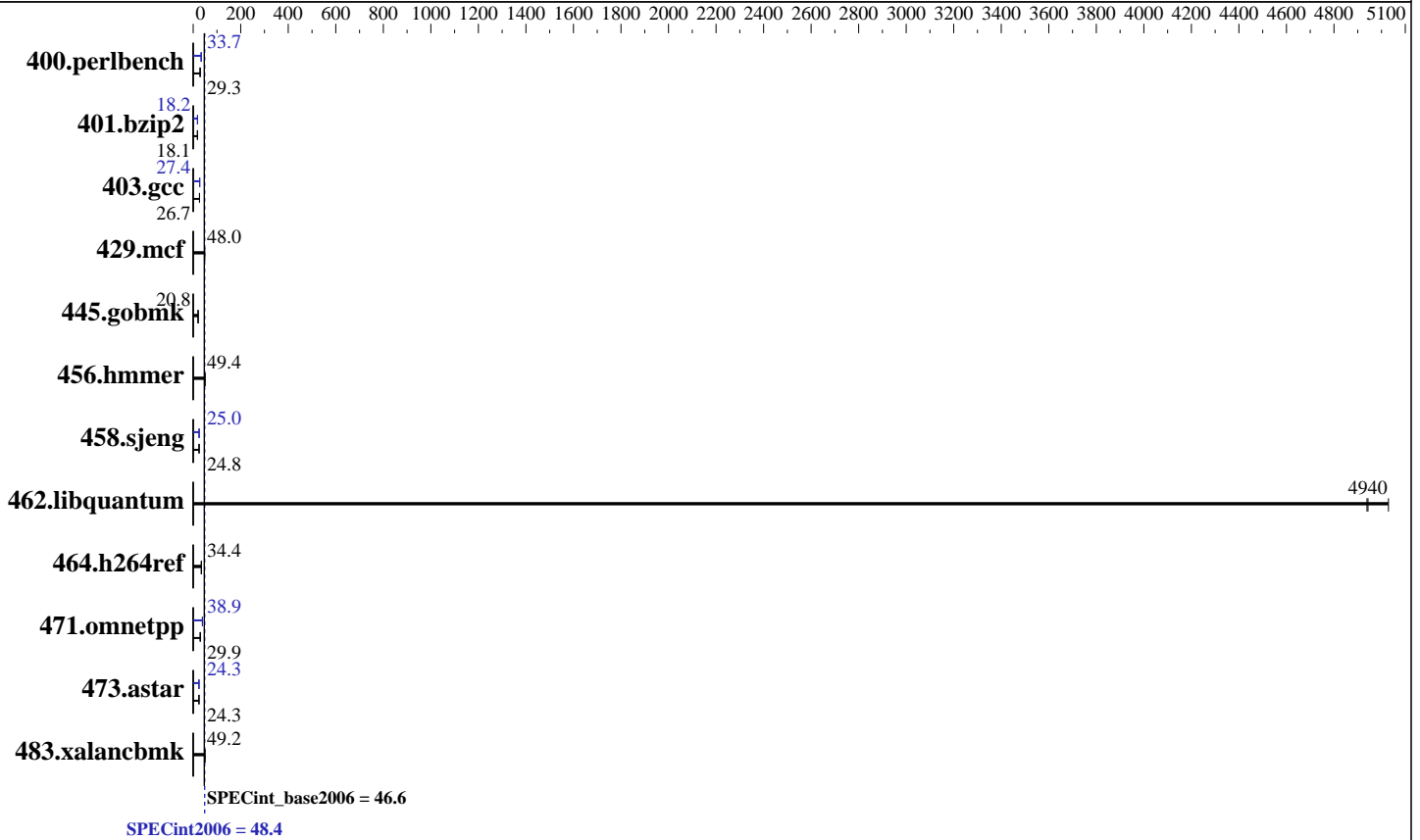**Test sponsor:** Hewlett-Packard Company   **Hardware Availability:** Sep-2014
Tested by: Hewlett-Packard Company   **Software Availability:** Sep-2014

400.perlbench	33.7 / 29.3
401.bzip2	18.2 / 18.1
403.gcc	27.4 / 26.7
429.mcf	48.0
445.gobmk	20.8
456.hmmer	49.4
458.sjeng	25.0 / 24.8
462.libquantum	4940
464.h264ref	34.4
471.omnetpp	38.9 / 29.9
473.astar	24.3 / 24.3
483.xalancbmk	49.2

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Hardware

CPU Name:	Intel Xeon E5-2650L v3
CPU Characteristics:	Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 400 GB SSA SAS, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.0 (Maipo)
	Kernel 3.10.0-123.el7.x86_64
Compiler:	C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	333	29.3	335	29.2	**334**	**29.3**	**290**	**33.7**	289	33.8	290	33.7
401.bzip2	533	18.1	**534**	**18.1**	536	18.0	**532**	**18.2**	531	18.2	532	18.2
403.gcc	**302**	**26.7**	301	26.7	303	26.6	**294**	**27.4**	293	27.5	294	27.4
429.mcf	188	48.5	**190**	**48.0**	191	47.8	188	48.5	**190**	**48.0**	191	47.8
445.gobmk	**504**	**20.8**	504	20.8	509	20.6	**504**	**20.8**	504	20.8	509	20.6
456.hmmer	188	49.5	189	49.3	**189**	**49.4**	188	49.5	189	49.3	**189**	**49.4**
458.sjeng	**487**	**24.8**	488	24.8	487	24.8	**484**	**25.0**	484	25.0	484	25.0
462.libquantum	**4.19**	**4940**	4.12	5030	4.20	4940	**4.19**	**4940**	4.12	5030	4.20	4940
464.h264ref	**643**	**34.4**	640	34.6	643	34.4	**643**	**34.4**	640	34.6	643	34.4
471.omnetpp	208	30.0	**209**	**29.9**	211	29.6	**161**	**38.9**	161	38.9	160	39.0
473.astar	290	24.2	**289**	**24.3**	289	24.3	286	24.6	**289**	**24.3**	289	24.3
483.xalancbmk	141	49.0	140	49.4	**140**	**49.2**	141	49.0	140	49.4	**140**	**49.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
 HP Power Profile set to Custom
 HP Power Regulator to HP Static High Performance Mode
 Minimum Processor Idle Power Core State set to C6 State
 Minimum Processor Idle Power Package State set to Package C6 (non-retention) State
 QPI Snoop Configuration set to Home Snoop
 Thermal Configuration set to Maximum Cooling
 Collaborative Power Control set to Disabled
 Processor Power and Utilization Monitoring set to Disabled
 Memory Double Refresh Rate set to 1x Refresh
 Intel Hyperthreading Technology set to Disabled

 Sysinfo program /home/cpu2006/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on localhost.localdomain Wed Oct 22 17:21:01 2014

 This section contains SUT (System Under Test) info as seen by

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Platform Notes (Continued)

```
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650L v3 @ 1.80GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 30720 KB

From /proc/meminfo
   MemTotal:       263845892 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
   EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 22 17:19

SPEC is set to: /home/cpu2006
   Filesystem          Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-home xfs   318G   94G  225G  30% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP P89 07/11/2014
```

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Platform Notes (Continued)

```
    Memory:
     8x HP 752369-081 16 GB 2 rank 2133 MHz
     8x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz
     8x UNKNOWN NOT AVAILABLE

  (End of data from sysinfo program)
 Regarding the sysinfo display about the memory installed, the correct amount of
 memory is 256 GB and the dmidecode description should have two lines reading as:
  8x HP 752369-081 16 GB 2 rank 2133 MHz
 8x HP NOT AVAILABLE 16 GB 2 rank 2133 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
```

Base Compiler Invocation

C benchmarks:
```
    icc  -m64
```

C++ benchmarks:
```
    icpc -m64
```

Base Portability Flags

```
   400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
     401.bzip2: -DSPEC_CPU_LP64
       403.gcc: -DSPEC_CPU_LP64
       429.mcf: -DSPEC_CPU_LP64
     445.gobmk: -DSPEC_CPU_LP64
     456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
      473.astar: -DSPEC_CPU_LP64
   483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

   400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):
```
icpc -m64
```

   471.omnetpp: `icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

```
     400.perlbench: -DSPEC_CPU_LINUX_IA32
         401.bzip2: -DSPEC_CPU_LP64
           403.gcc: -DSPEC_CPU_LP64
           429.mcf: -DSPEC_CPU_LP64
        445.gobmk: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
         458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
       464.h264ref: -DSPEC_CPU_LP64
         473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

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Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-ra-region-strategy=block          -ansi-alias`
`-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`
`-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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