



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

**SPECfp<sup>®</sup>2006 = 114**

Express5800/R120f-2M (Intel Xeon E5-2699 v3)

**SPECfp\_base2006 = 108**

CPU2006 license: 9006

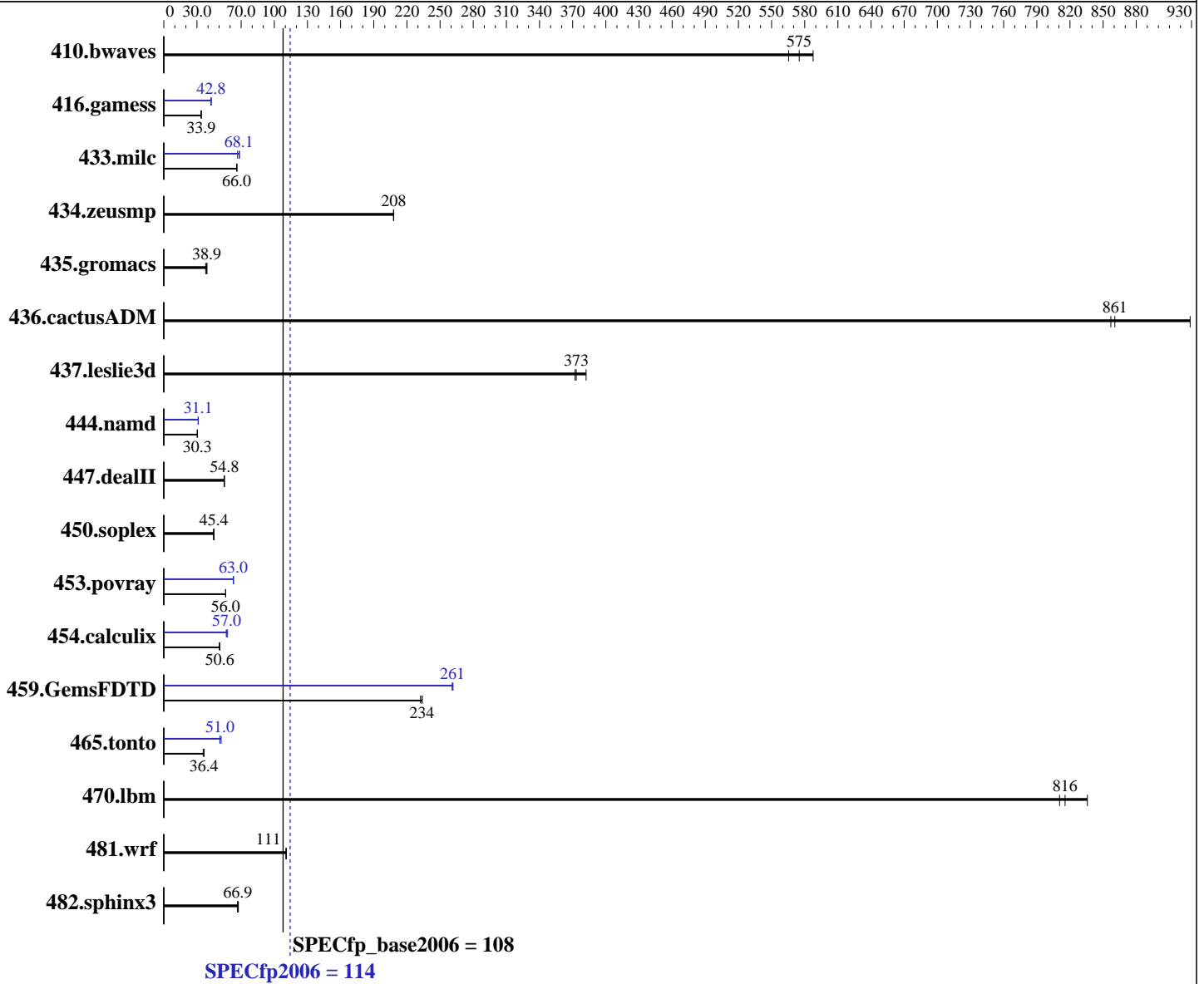
Test date: Sep-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



**Hardware**

CPU Name: Intel Xeon E5-2699 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 Kernel 2.6.32-431.17.1.el6.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24.0	565	23.1	588	<u>23.6</u>	<u>575</u>	24.0	565	23.1	588	<u>23.6</u>	<u>575</u>
416.gamess	579	33.8	578	33.9	<u>578</u>	<u>33.9</u>	458	42.8	<u>457</u>	<u>42.8</u>	457	42.9
433.milc	139	66.0	<u>139</u>	<u>66.0</u>	139	66.1	134	68.6	138	66.7	<u>135</u>	<u>68.1</u>
434.zeusmp	43.8	208	<u>43.8</u>	<u>208</u>	43.7	208	43.8	208	<u>43.8</u>	<u>208</u>	43.7	208
435.gromacs	183	39.0	189	37.9	<u>184</u>	<u>38.9</u>	183	39.0	189	37.9	<u>184</u>	<u>38.9</u>
436.cactusADM	12.9	929	13.9	857	<u>13.9</u>	<u>861</u>	12.9	929	13.9	857	<u>13.9</u>	<u>861</u>
437.leslie3d	24.6	382	25.3	372	<u>25.2</u>	<u>373</u>	24.6	382	25.3	372	<u>25.2</u>	<u>373</u>
444.namd	264	30.3	<u>264</u>	<u>30.3</u>	264	30.3	258	31.1	<u>258</u>	<u>31.1</u>	258	31.1
447.dealII	209	54.9	210	54.6	<u>209</u>	<u>54.8</u>	209	54.9	210	54.6	<u>209</u>	<u>54.8</u>
450.soplex	183	45.5	<u>184</u>	<u>45.4</u>	186	44.9	183	45.5	<u>184</u>	<u>45.4</u>	186	44.9
453.povray	<u>95.0</u>	<u>56.0</u>	95.1	55.9	95.0	56.0	<u>84.4</u>	<u>63.0</u>	84.6	62.9	84.4	63.1
454.calculix	163	50.6	164	50.4	<u>163</u>	<u>50.6</u>	143	57.7	146	56.5	<u>145</u>	<u>57.0</u>
459.GemsFDTD	45.7	232	45.4	234	<u>45.4</u>	<u>234</u>	40.5	262	<u>40.6</u>	<u>261</u>	40.6	261
465.tonto	276	35.6	270	36.4	<u>270</u>	<u>36.4</u>	190	51.9	194	50.7	<u>193</u>	<u>51.0</u>
470.lbm	17.0	811	<u>16.8</u>	<u>816</u>	16.4	836	17.0	811	<u>16.8</u>	<u>816</u>	16.4	836
481.wrf	<u>101</u>	<u>111</u>	101	111	101	111	<u>101</u>	<u>111</u>	101	111	101	111
482.sphinx3	289	67.5	292	66.8	<u>291</u>	<u>66.9</u>	289	67.5	292	66.8	<u>291</u>	<u>66.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Power Management Policy: Custom  
 Energy Performance: Performance  
 Patrol Scrub: Disabled  
 Demand Scrub: Disabled  
 Early Snoop: Disabled  
 Hyper-Threading: Disabled



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "36"

The Express5800/R120f-1M (Intel Xeon E5-2699 v3) and the Express5800/R120f-2M (Intel Xeon E5-2699 v3) models are electronically equivalent. The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2699 v3) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>



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<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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